

SMPS MOSFET

IRF7471PbF

Applications

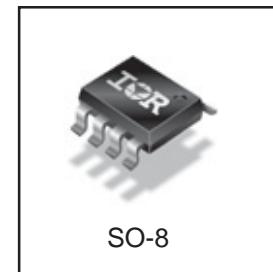
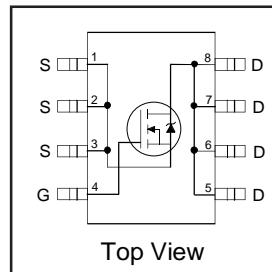
- High Frequency Isolated DC-DC Converters with Synchronous Rectification for Telecom and Industrial Use
- High Frequency Buck Converters for Computer Processor Power
- Lead-Free

Benefits

- Ultra-Low Gate Impedance
- Very Low $R_{DS(on)}$
- Fully Characterized Avalanche Voltage and Current

HEXFET® Power MOSFET

V_{DSS}	$R_{DS(on)\ max}$	I_D
40V	13mΩ	10A



Absolute Maximum Ratings

Symbol	Parameter	Max.	Units
V_{DS}	Drain-Source Voltage	40	V
V_{GS}	Gate-to-Source Voltage	± 20	V
$I_D @ T_A = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	10	A
$I_D @ T_A = 70^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	8.3	
I_{DM}	Pulsed Drain Current①	83	
$P_D @ T_A = 25^\circ C$	Maximum Power Dissipation③	2.5	W
$P_D @ T_A = 70^\circ C$	Maximum Power Dissipation③	1.6	W
	Linear Derating Factor	0.02	mW/°C
T_J, T_{STG}	Junction and Storage Temperature Range	-55 to + 150	°C

Thermal Resistance

Symbol	Parameter	Typ.	Max.	Units
R_{0JL}	Junction-to-Drain Lead	—	20	
R_{0JA}	Junction-to-Ambient ④	—	50	°C/W

Notes ① through ④ are on page 8

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Static @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(\text{BR})\text{DSS}}$	Drain-to-Source Breakdown Voltage	40	—	—	V	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$
$\Delta V_{(\text{BR})\text{DSS}}/\Delta T_J$	Breakdown Voltage Temp. Coefficient	—	0.043	—	V/ $^\circ\text{C}$	Reference to $25^\circ\text{C}, I_D = 1\text{mA}$
$R_{DS(\text{on})}$	Static Drain-to-Source On-Resistance	—	9.5	13	m Ω	$V_{GS} = 10\text{V}, I_D = 10\text{A}$ ③
		—	12	16		$V_{GS} = 4.5\text{V}, I_D = 8.0\text{A}$ ③
$V_{GS(\text{th})}$	Gate Threshold Voltage	1.0	—	3.0	V	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$
I_{DSS}	Drain-to-Source Leakage Current	—	—	20	μA	$V_{DS} = 32\text{V}, V_{GS} = 0\text{V}$
		—	—	100		$V_{DS} = 32\text{V}, V_{GS} = 0\text{V}, T_J = 125^\circ\text{C}$
I_{GSS}	Gate-to-Source Forward Leakage	—	—	200	nA	$V_{GS} = 16\text{V}$
	Gate-to-Source Reverse Leakage	—	—	-200		$V_{GS} = -16\text{V}$

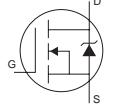
Dynamic @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
g_{fs}	Forward Transconductance	22	—	—	S	$V_{DS} = 20\text{V}, I_D = 8.0\text{A}$
Q_g	Total Gate Charge	—	21	32		$I_D = 8.0\text{A}$
Q_{gs}	Gate-to-Source Charge	—	7.2	11	nC	$V_{DS} = 20\text{V}$
Q_{gd}	Gate-to-Drain ("Miller") Charge	—	8.2	12		$V_{GS} = 4.5\text{V}$ ③
Q_{oss}	Output Gate Charge	—	23	35		$V_{GS} = 0\text{V}, V_{DS} = 16\text{V}$
$t_{d(on)}$	Turn-On Delay Time	—	12	—		$V_{DD} = 20\text{V}$
t_r	Rise Time	—	2.7	—	ns	$I_D = 8.0\text{A}$
$t_{d(off)}$	Turn-Off Delay Time	—	15	—		$R_G = 1.8\Omega$
t_f	Fall Time	—	4.1	—		$V_{GS} = 4.5\text{V}$ ③
C_{iss}	Input Capacitance	—	2820	—		$V_{GS} = 0\text{V}$
C_{oss}	Output Capacitance	—	700	—		$V_{DS} = 20\text{V}$
C_{rss}	Reverse Transfer Capacitance	—	46	—	pF	$f = 1.0\text{MHz}$

Avalanche Characteristics

Symbol	Parameter	Typ.	Max.	Units
E_{AS}	Single Pulse Avalanche Energy②	—	300	mJ
I_{AR}	Avalanche Current①	—	8.0	A

Diode Characteristics

Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
I_S	Continuous Source Current (Body Diode)	—	—	2.3	A	MOSFET symbol showing the integral reverse p-n junction diode.
I_{SM}	Pulsed Source Current (Body Diode) ①	—	—	83		
V_{SD}	Diode Forward Voltage	—	0.80	1.3	V	$T_J = 25^\circ\text{C}, I_S = 8.0\text{A}, V_{GS} = 0\text{V}$ ③
		—	0.65	—		$T_J = 125^\circ\text{C}, I_S = 8.0\text{A}, V_{GS} = 0\text{V}$
t_{rr}	Reverse Recovery Time	—	69	100	ns	$T_J = 25^\circ\text{C}, I_F = 8.0\text{A}, V_R = 20\text{V}$
Q_{rr}	Reverse Recovery Charge	—	130	200	nC	$di/dt = 100\text{A}/\mu\text{s}$ ③
t_{rr}	Reverse Recovery Time	—	73	110	ns	$T_J = 125^\circ\text{C}, I_F = 8.0\text{A}, V_R = 20\text{V}$
Q_{rr}	Reverse Recovery Charge	—	160	240	nC	$di/dt = 100\text{A}/\mu\text{s}$ ③

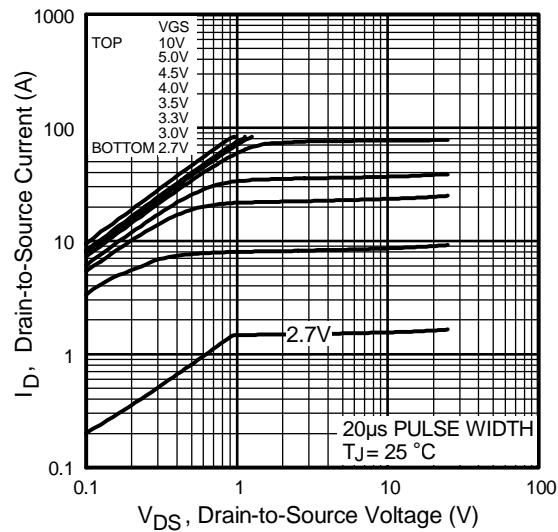


Fig 1. Typical Output Characteristics

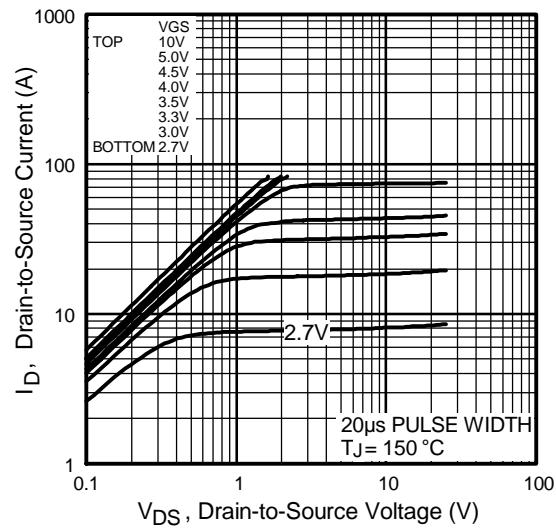


Fig 2. Typical Output Characteristics

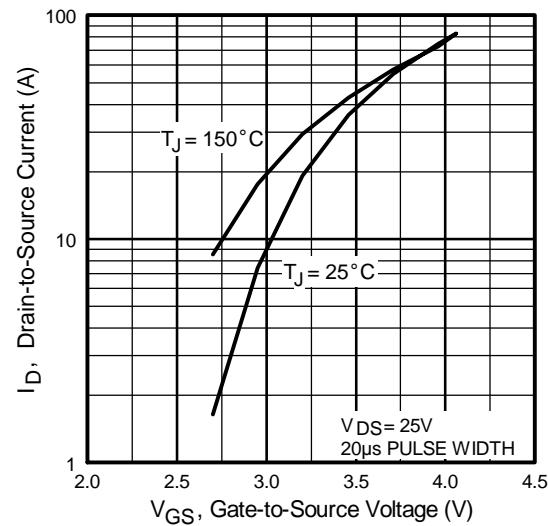


Fig 3. Typical Transfer Characteristics

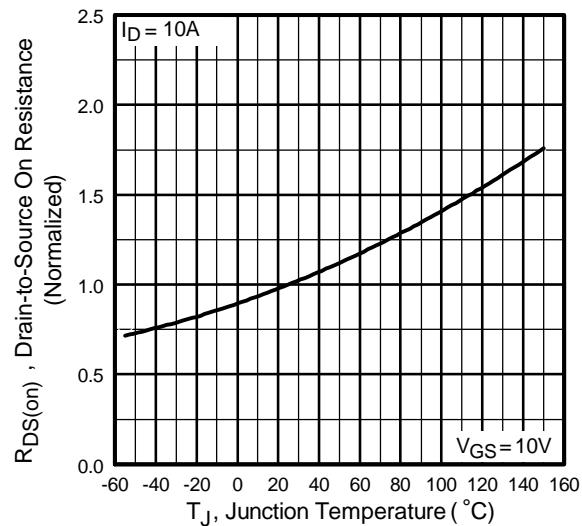


Fig 4. Normalized On-Resistance Vs. Temperature

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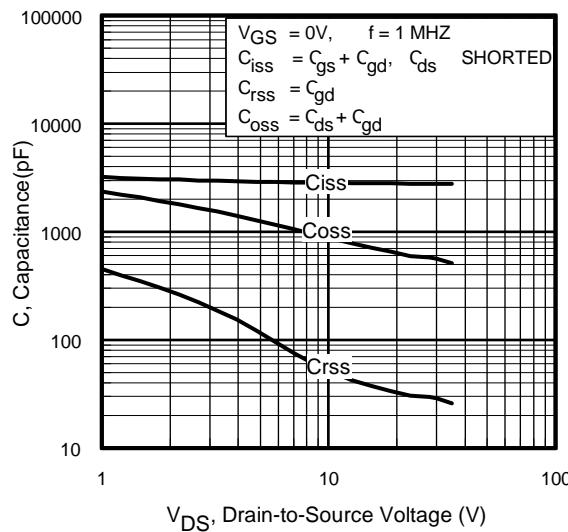


Fig 5. Typical Capacitance Vs.
Drain-to-Source Voltage

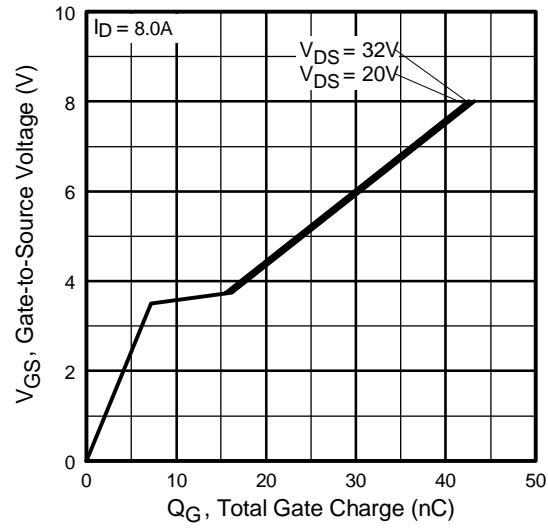


Fig 6. Typical Gate Charge Vs.
Gate-to-Source Voltage

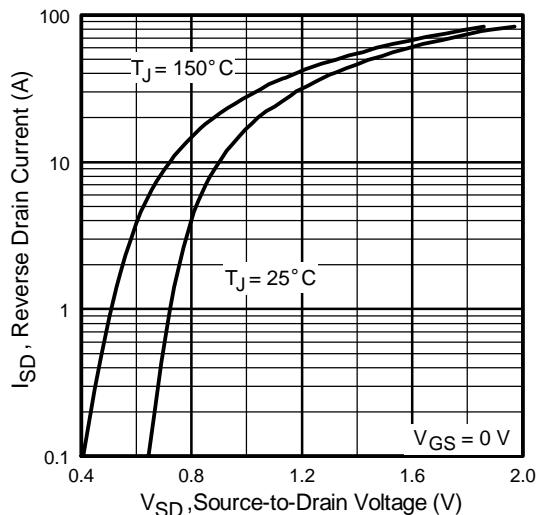


Fig 7. Typical Source-Drain Diode
Forward Voltage

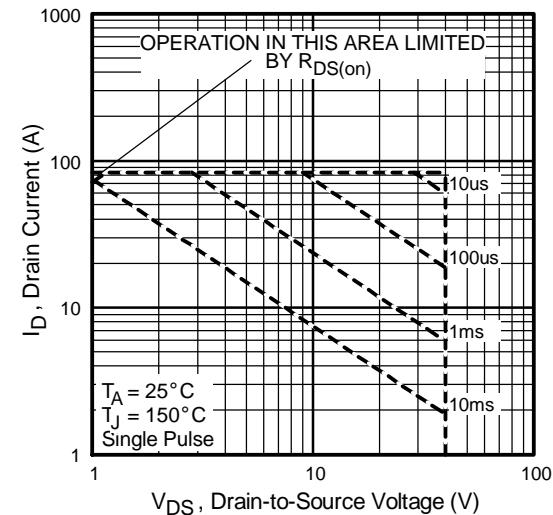


Fig 8. Maximum Safe Operating Area

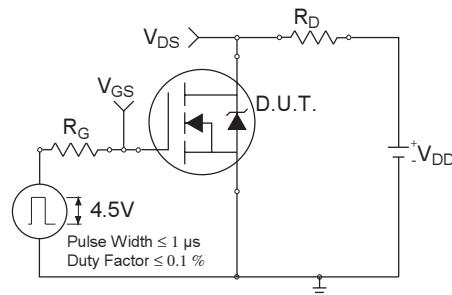
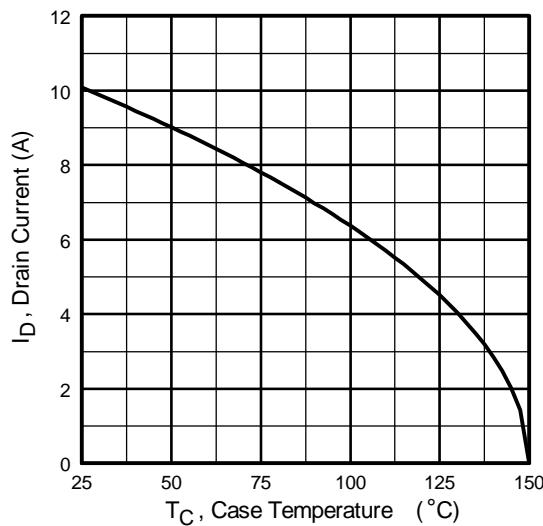


Fig 10a. Switching Time Test Circuit

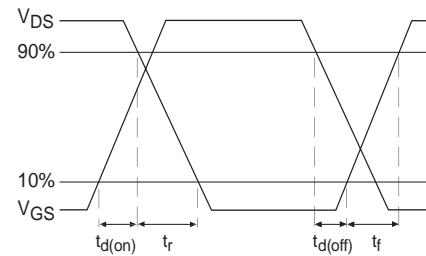


Fig 10b. Switching Time Waveforms

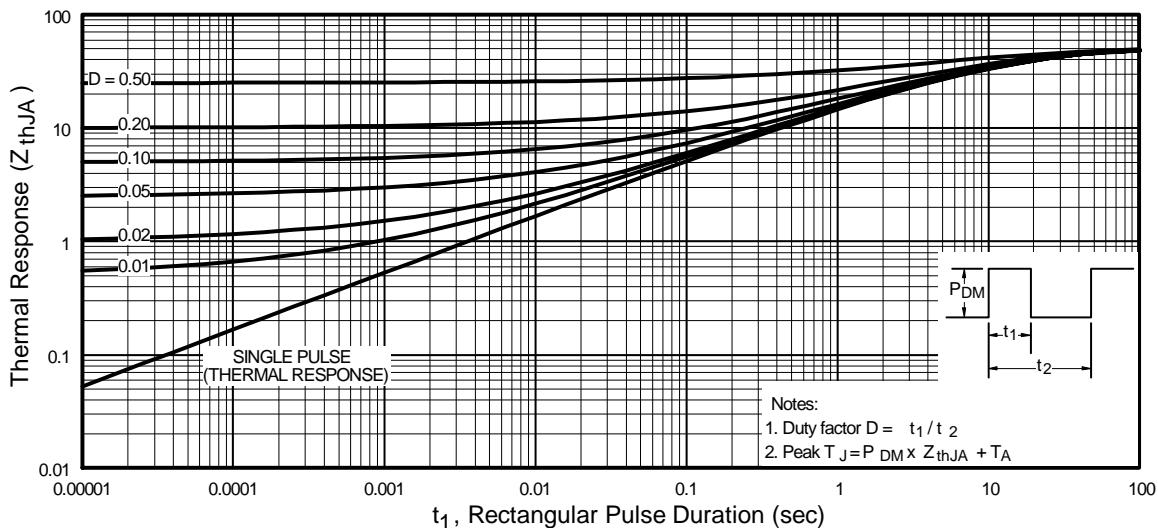


Fig 10. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

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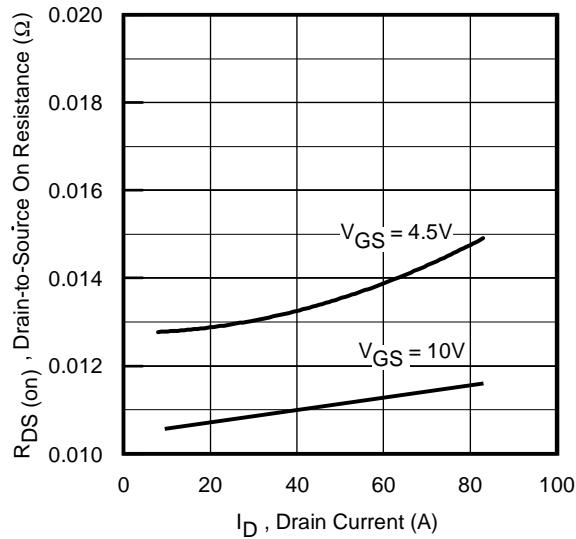


Fig 12. On-Resistance Vs. Drain Current

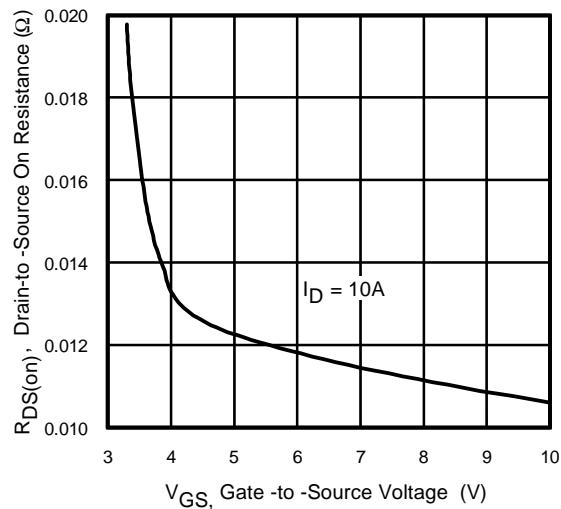


Fig 13. On-Resistance Vs. Gate Voltage

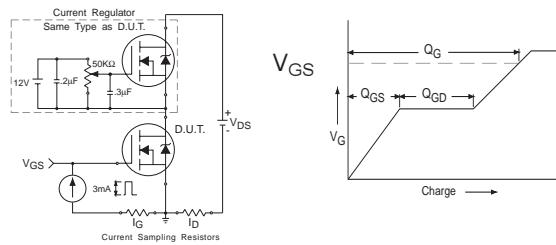


Fig 13a&b. Basic Gate Charge Test Circuit and Waveform

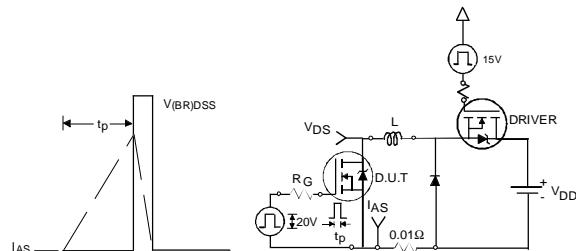


Fig 14a&b. Unclamped Inductive Test circuit and Waveforms

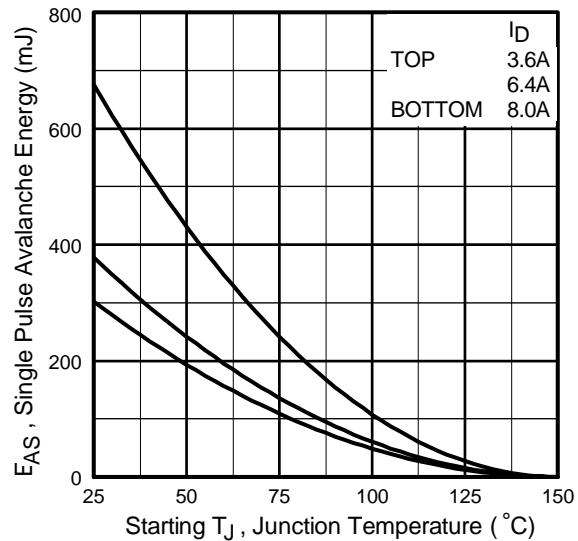


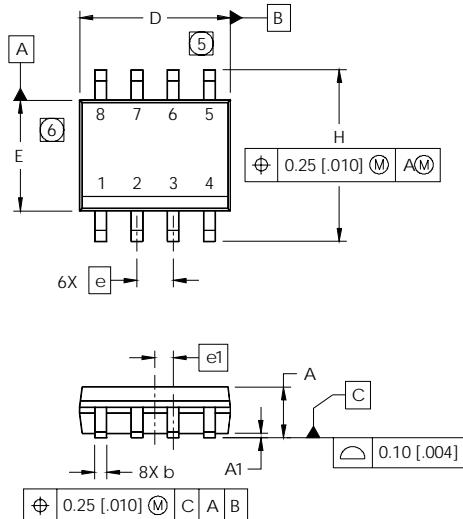
Fig 14c. Maximum Avalanche Energy Vs. Drain Current

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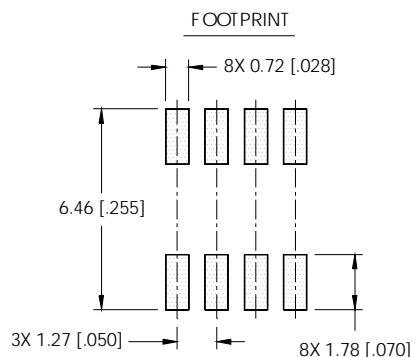
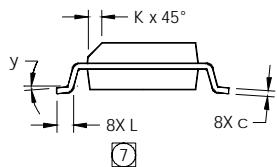
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SO-8 Package Outline

Dimensions are shown in millimeters (inches)

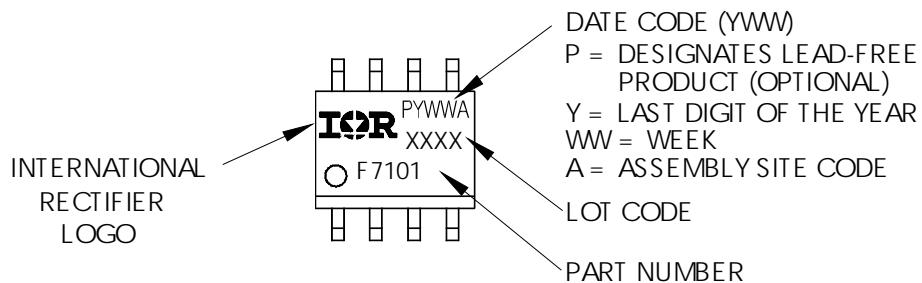


DIM	INCHES		MILLIMETERS	
	MN	MAX	MIN	MAX
A	.0532	.0688	1.35	1.75
A1	.0040	.0098	0.10	0.25
b	.013	.020	0.33	0.51
c	.0075	.0098	0.19	0.25
D	.189	.1968	4.80	5.00
E	.1497	.1574	3.80	4.00
e	.050	BASIC	1.27	BASIC
e1	.025	BASIC	0.635	BASIC
H	.2284	.2440	5.80	6.20
K	.0099	.0196	0.25	0.50
L	.016	.050	0.40	1.27
y	0°	8°	0°	8°



SO-8 Part Marking

EXAMPLE: THIS IS AN IRF7101 (MOSFET)

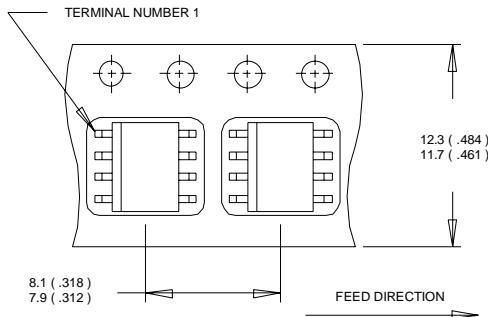


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SO-8 Tape and Reel

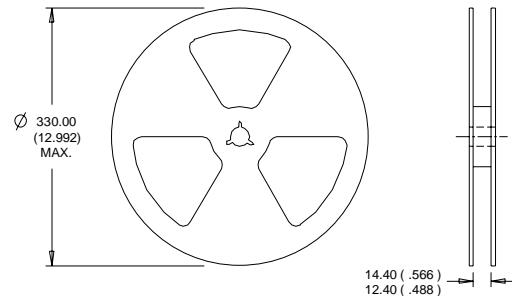
Dimensions are shown in millimeters (inches)

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NOTES:

1. CONTROLLING DIMENSION : MILLIMETER.
2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS(INCHES).
3. OUTLINE CONFORMS TO EIA-481 & EIA-541.



NOTES :

1. CONTROLLING DIMENSION : MILLIMETER.
2. OUTLINE CONFORMS TO EIA-481 & EIA-541.

Notes:

- | | |
|---|---|
| ① Repetitive rating; pulse width limited by max. junction temperature. | ③ Pulse width \leq 400 μ s; duty cycle \leq 2%. |
| ② Starting $T_J = 25^\circ\text{C}$, $L = 9.4\text{mH}$
$R_G = 25\Omega$, $I_{AS} = 8.0\text{A}$. | ④ When mounted on 1 inch square copper board. |

Data and specifications subject to change without notice.
 This product has been designed and qualified for the Consumer market.
 Qualifications Standards can be found on IR's Web site.

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