

Fast IGBT in NPT-technology

- 40% lower E_{off} compared to previous generation

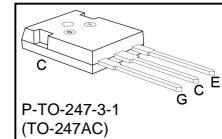
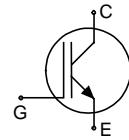
• Short circuit withstand time – 10 μs

• Designed for:

- Motor controls
- Inverter
- SMPS

• NPT-Technology offers:

- very tight parameter distribution
- high ruggedness, temperature stable behaviour
- parallel switching capability



- Complete product spectrum and PSpice Models : <http://www.infineon.com/igbt/>

Type	V_{CE}	I_C	E_{off}	T_j	Package	Ordering Code
SGW25N120	1200V	25A	2.9mJ	150°C	TO-247AC	Q67040-S4277

Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage	V_{CE}	1200	V
DC collector current $T_C = 25^\circ\text{C}$	I_C	46	A
$T_C = 100^\circ\text{C}$		25	
Pulsed collector current, t_p limited by $T_{j\text{max}}$	$I_{C\text{puls}}$	84	
Turn off safe operating area $V_{\text{CE}} \leq 1200\text{V}, T_j \leq 150^\circ\text{C}$	-	84	
Gate-emitter voltage	V_{GE}	± 20	V
Avalanche energy, single pulse $I_C = 25\text{A}, V_{CC} = 50\text{V}, R_{GE} = 25\Omega$, start at $T_j = 25^\circ\text{C}$	E_{AS}	130	mJ
Short circuit withstand time ¹⁾ $V_{GE} = 15\text{V}, 100\text{V} \leq V_{CC} \leq 1200\text{V}, T_j \leq 150^\circ\text{C}$	t_{SC}	10	μs
Power dissipation $T_C = 25^\circ\text{C}$	P_{tot}	313	W
Operating junction and storage temperature	T_j, T_{stg}	-55...+150	$^\circ\text{C}$
Soldering temperature, 1.6mm (0.063 in.) from case for 10s	-	260	

¹⁾ Allowed number of short circuits: <1000; time between short circuits: >1s.

Thermal Resistance

Parameter	Symbol	Conditions	Max. Value	Unit
Characteristic				
IGBT thermal resistance, junction – case	R_{thJC}		0.4	K/W
Thermal resistance, junction – ambient	R_{thJA}	TO-247AC	40	

Electrical Characteristic, at $T_j = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	

Static Characteristic

Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{GE}=0\text{V}$, $I_C=1500\mu\text{A}$	1200	-	-	V
Collector-emitter saturation voltage	$V_{CE(\text{sat})}$	$V_{GE} = 15\text{V}$, $I_C=25\text{A}$ $T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	2.5	3.1	3.6	
Gate-emitter threshold voltage	$V_{GE(\text{th})}$	$I_C=1000\mu\text{A}$, $V_{CE}=V_{GE}$	3	4	5	
Zero gate voltage collector current	I_{CES}	$V_{CE}=1200\text{V}$, $V_{GE}=0\text{V}$ $T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	-	-	350	μA
Gate-emitter leakage current	I_{GES}	$V_{CE}=0\text{V}$, $V_{GE}=20\text{V}$	-	-	100	nA
Transconductance	g_{fs}	$V_{CE}=20\text{V}$, $I_C=25\text{A}$		20	-	S

Dynamic Characteristic

Input capacitance	C_{iss}	$V_{CE}=25\text{V}$, $V_{GE}=0\text{V}$, $f=1\text{MHz}$	-	2150	2600	pF
Output capacitance	C_{oss}		-	160	190	
Reverse transfer capacitance	C_{rss}		-	110	130	
Gate charge	Q_{Gate}	$V_{CC}=960\text{V}$, $I_C=25\text{A}$ $V_{GE}=15\text{V}$	-	225	300	nC
Internal emitter inductance measured 5mm (0.197 in.) from case	L_E	TO-247AC	-	13	-	nH
Short circuit collector current ¹⁾	$I_{C(\text{SC})}$	$V_{GE}=15\text{V}$, $t_{SC}\leq 10\mu\text{s}$ $100\text{V}\leq V_{CC}\leq 1200\text{V}$, $T_j \leq 150^\circ\text{C}$	-	240	-	A

¹⁾ Allowed number of short circuits: <1000; time between short circuits: >1s.

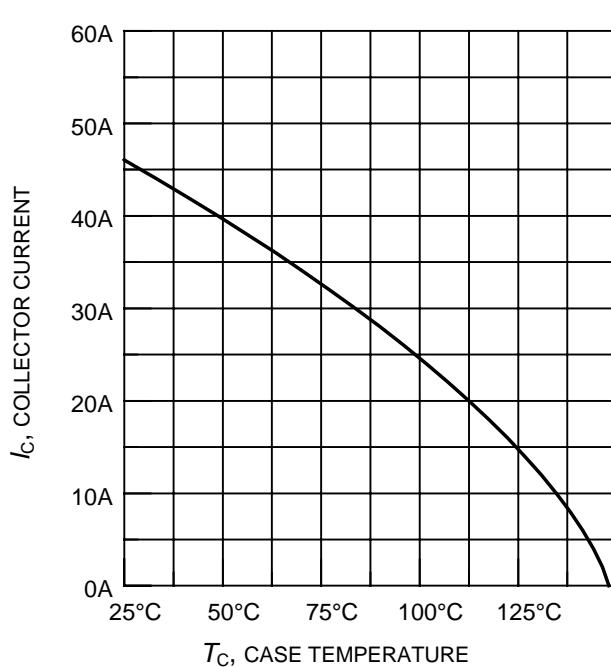
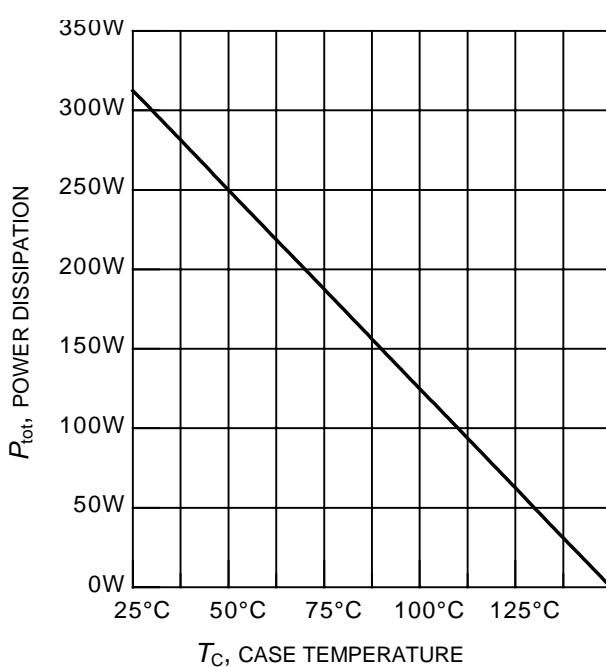
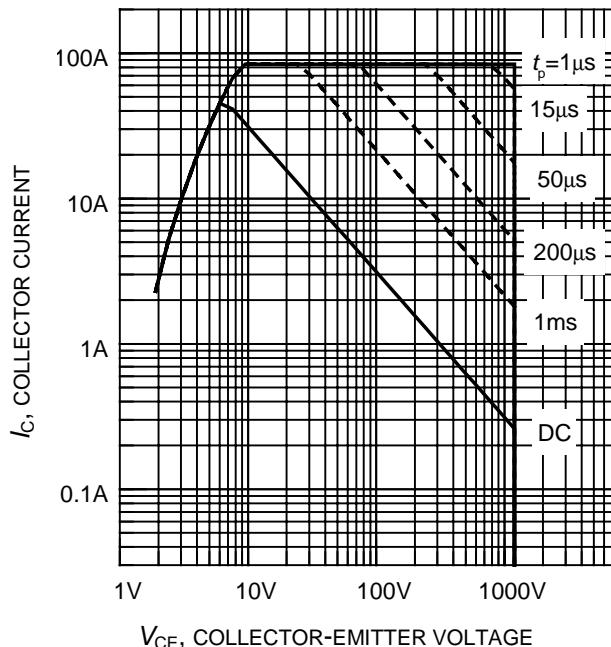
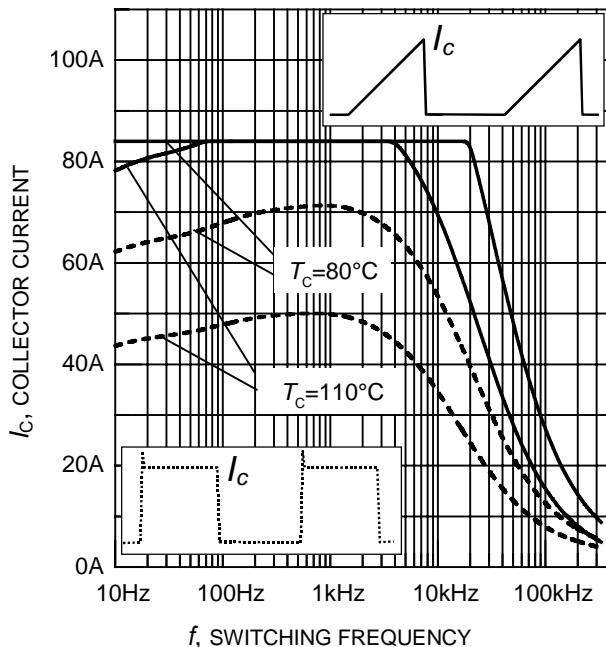
Switching Characteristic, Inductive Load, at $T_j=25\text{ }^\circ\text{C}$

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
IGBT Characteristic						
Turn-on delay time	$t_{d(on)}$	$T_j=25\text{ }^\circ\text{C}, V_{CC}=800\text{V}, I_C=25\text{A}, V_{GE}=15\text{V/0V}, R_G=22\Omega, L_\sigma^{(1)}=180\text{nH}, C_\sigma^{(1)}=40\text{pF}$ Energy losses include "tail" and diode reverse recovery.	-	45	60	ns
Rise time	t_r		-	40	52	
Turn-off delay time	$t_{d(off)}$		-	730	950	
Fall time	t_f		-	30	39	
Turn-on energy	E_{on}		-	2.2	2.9	mJ
Turn-off energy	E_{off}		-	1.5	2.0	
Total switching energy	E_{ts}		-	3.7	4.9	

Switching Characteristic, Inductive Load, at $T_j=150\text{ }^\circ\text{C}$

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
IGBT Characteristic						
Turn-on delay time	$t_{d(on)}$	$T_j=150\text{ }^\circ\text{C}, V_{CC}=800\text{V}, I_C=25\text{A}, V_{GE}=15\text{V/0V}, R_G=22\Omega, L_\sigma^{(1)}=180\text{nH}, C_\sigma^{(1)}=40\text{pF}$ Energy losses include "tail" and diode reverse recovery.	-	50	60	ns
Rise time	t_r		-	36	43	
Turn-off delay time	$t_{d(off)}$		-	820	990	
Fall time	t_f		-	42	50	
Turn-on energy	E_{on}		-	3.8	4.6	mJ
Turn-off energy	E_{off}		-	2.9	3.8	
Total switching energy	E_{ts}		-	6.7	8.4	

¹⁾ Leakage inductance L_σ and stray capacity C_σ due to dynamic test circuit in figure E.



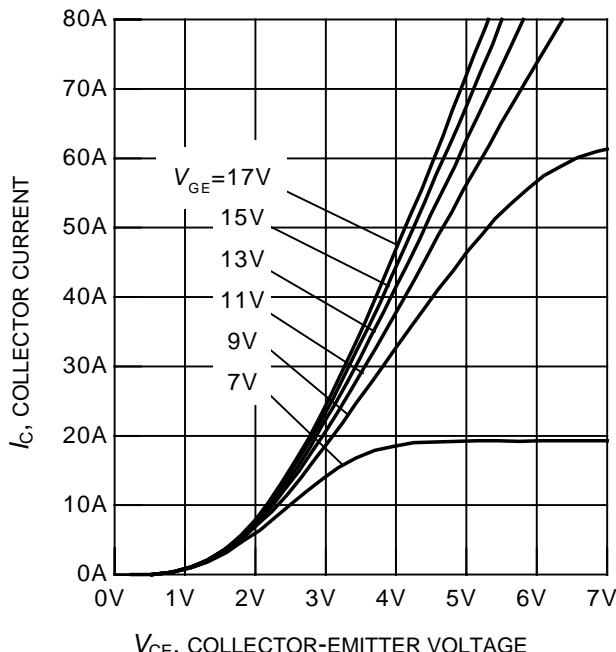


Figure 5. Typical output characteristics
($T_j = 25^\circ\text{C}$)

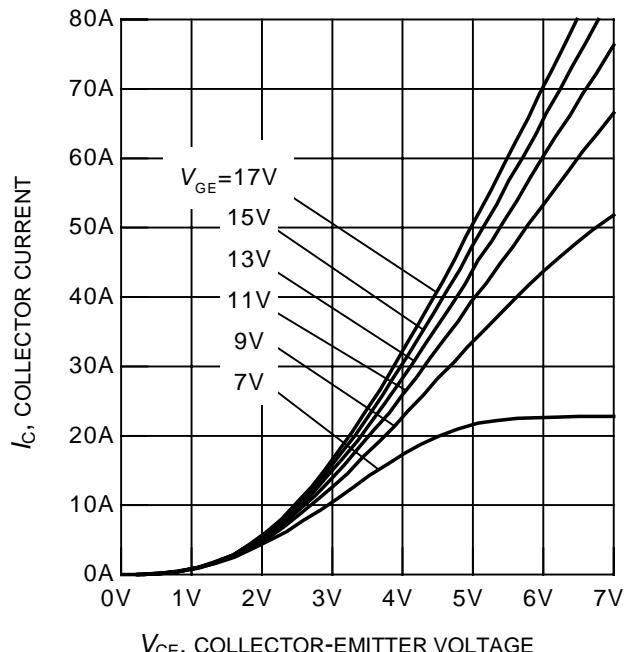


Figure 6. Typical output characteristics
($T_j = 150^\circ\text{C}$)

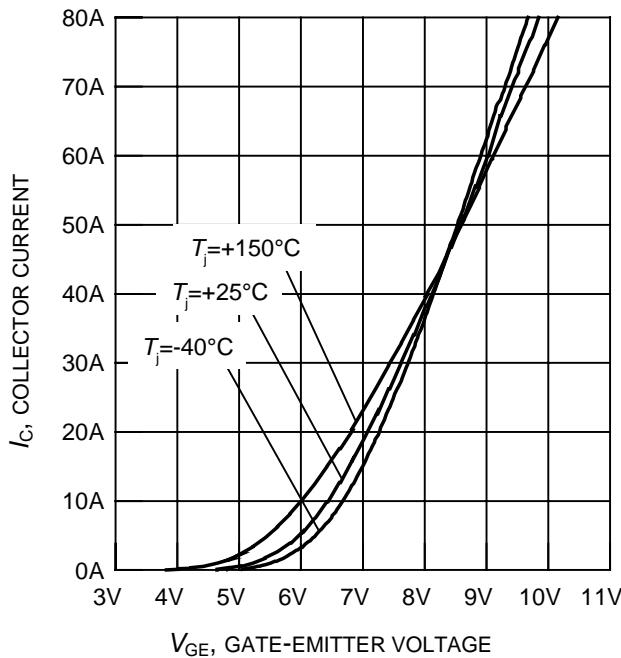


Figure 7. Typical transfer characteristics
($V_{CE} = 20\text{V}$)

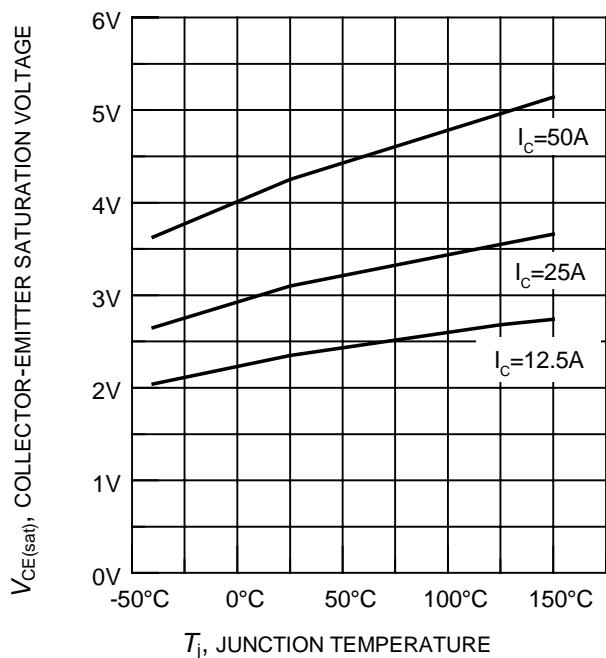
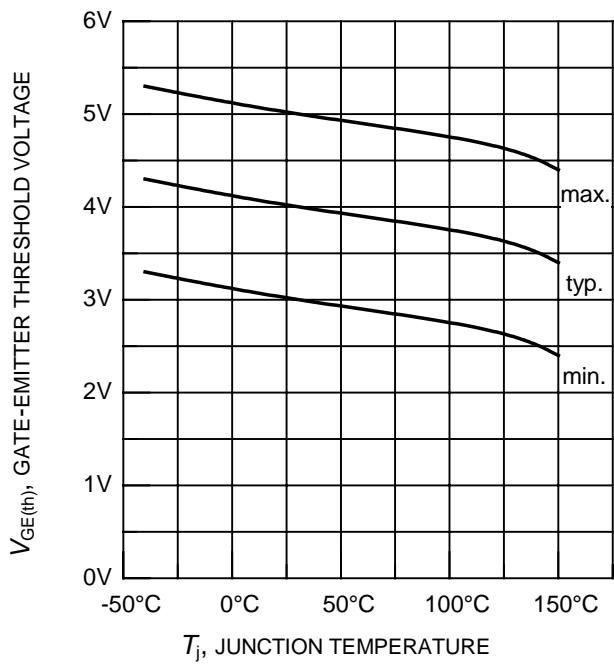
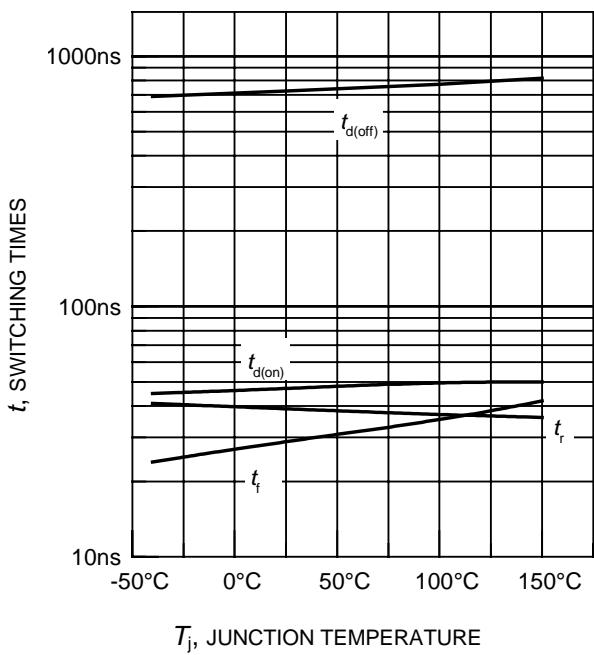
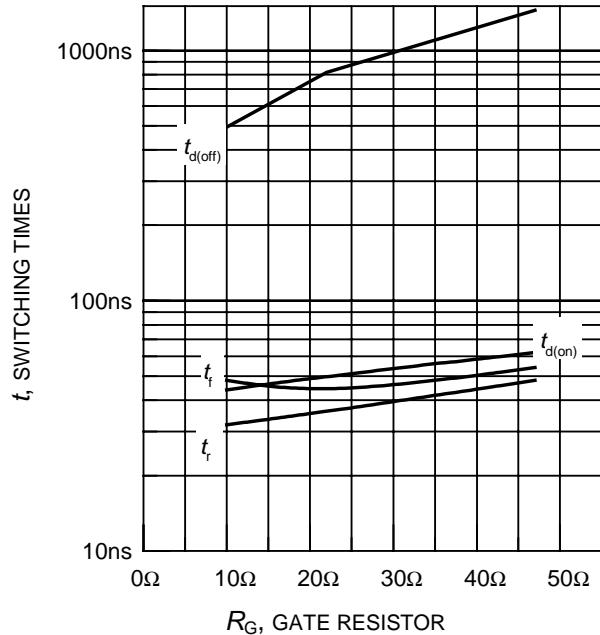
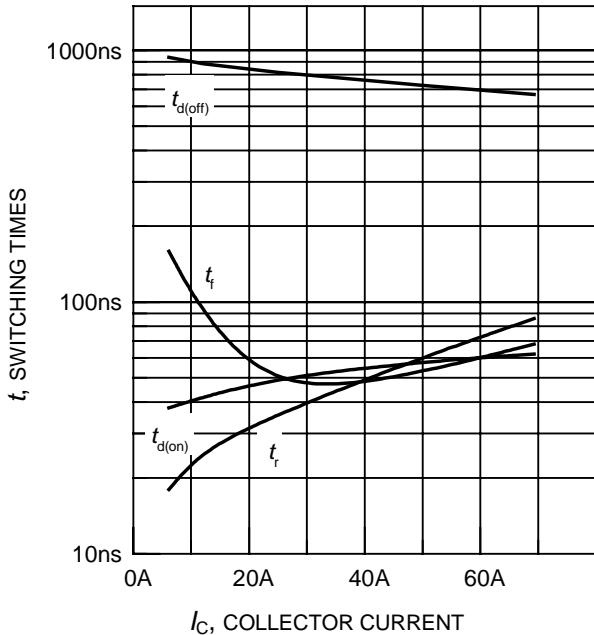


Figure 8. Typical collector-emitter saturation voltage as a function of junction temperature
($V_{GE} = 15\text{V}$)



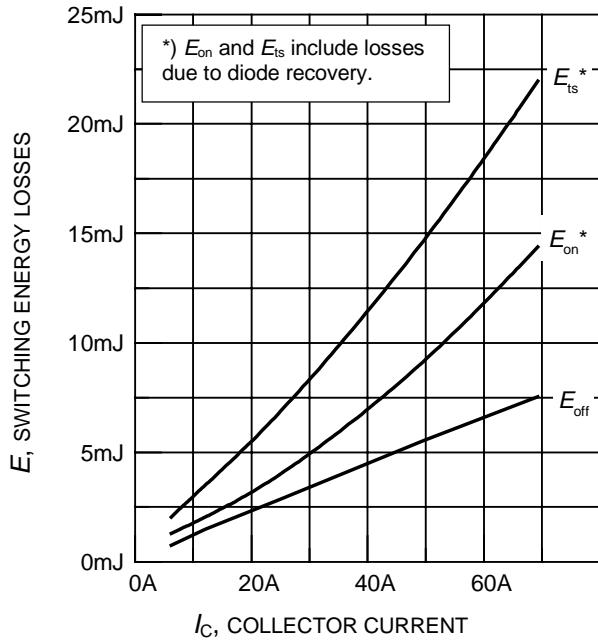


Figure 13. Typical switching energy losses as a function of collector current

(inductive load, $T_j = 150^\circ\text{C}$,
 $V_{CE} = 800\text{V}$, $V_{GE} = +15\text{V}/0\text{V}$, $R_G = 22\Omega$,
dynamic test circuit in Fig.E)

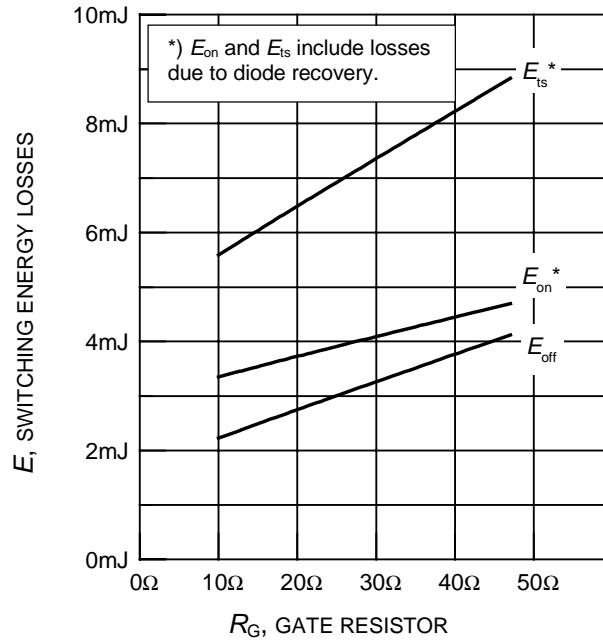


Figure 14. Typical switching energy losses as a function of gate resistor

(inductive load, $T_j = 150^\circ\text{C}$,
 $V_{CE} = 800\text{V}$, $V_{GE} = +15\text{V}/0\text{V}$, $I_C = 25\text{A}$,
dynamic test circuit in Fig.E)

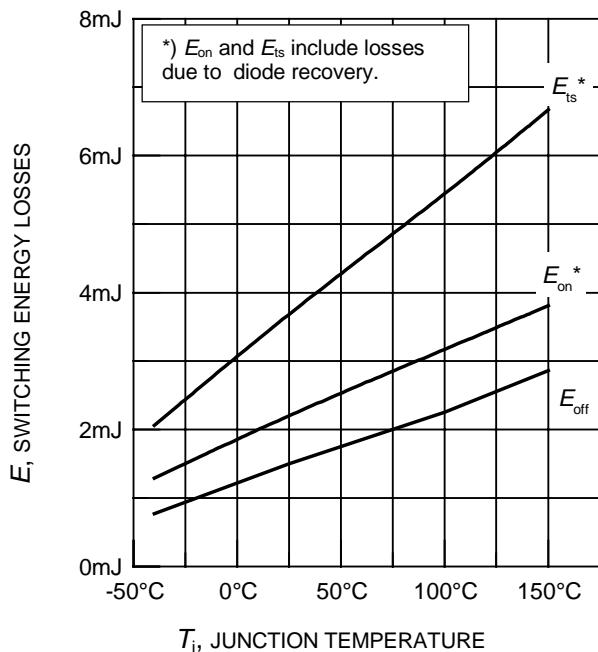


Figure 15. Typical switching energy losses as a function of junction temperature

(inductive load, $V_{CE} = 800\text{V}$,
 $V_{GE} = +15\text{V}/0\text{V}$, $I_C = 25\text{A}$, $R_G = 22\Omega$,
dynamic test circuit in Fig.E)

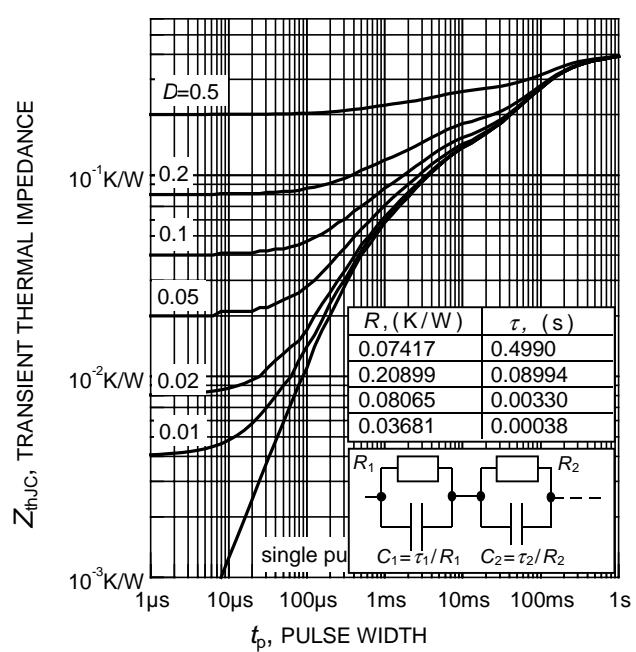


Figure 16. IGBT transient thermal impedance as a function of pulse width

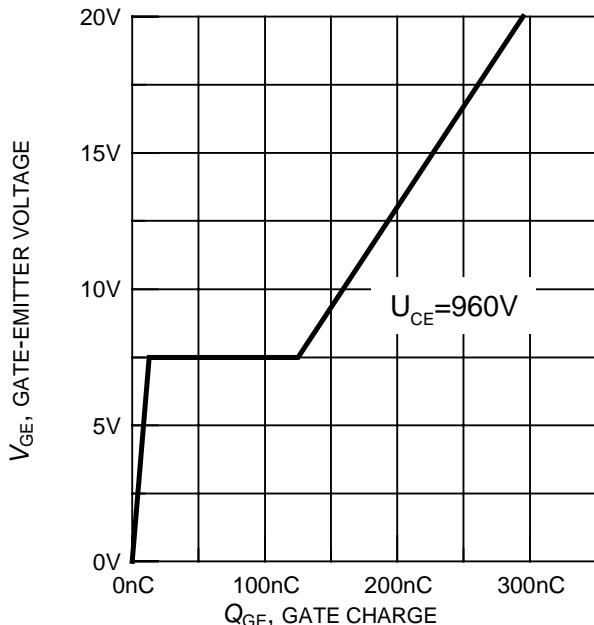


Figure 17. Typical gate charge
($I_C = 25A$)

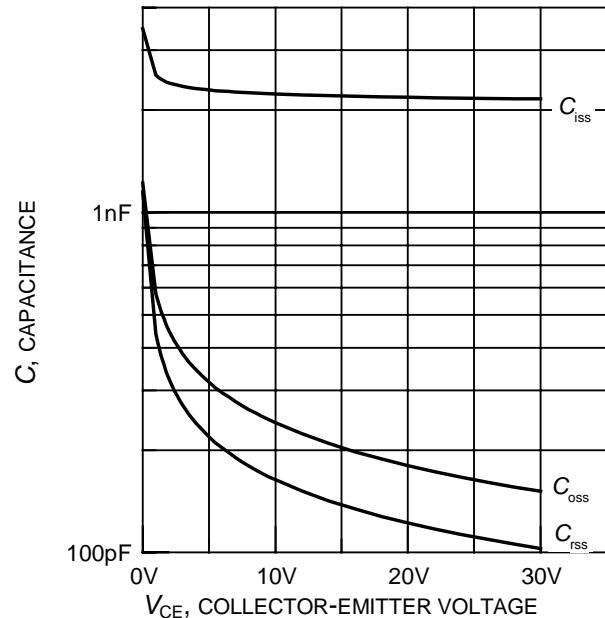


Figure 18. Typical capacitance as a function of collector-emitter voltage
($V_{GE} = 0V, f = 1MHz$)

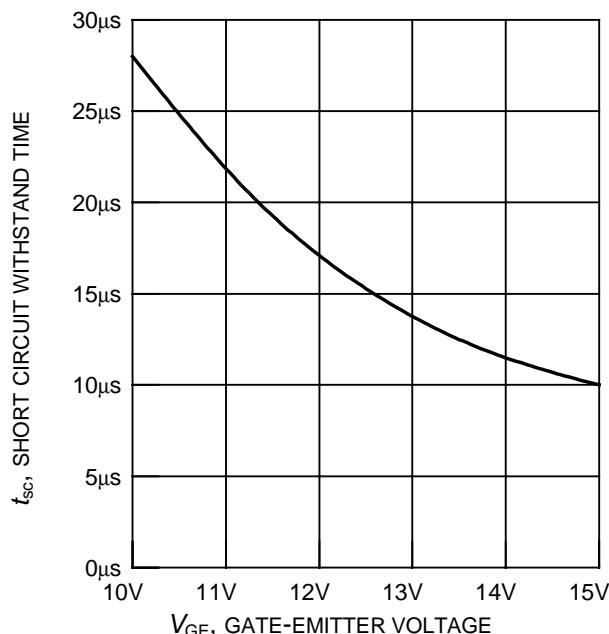


Figure 19. Short circuit withstand time as a function of gate-emitter voltage
($V_{CE} = 1200V$, start at $T_j = 25^\circ C$)

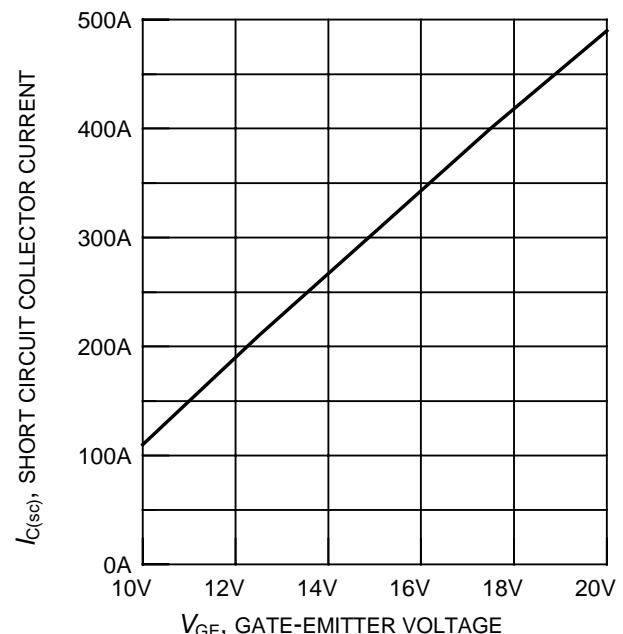
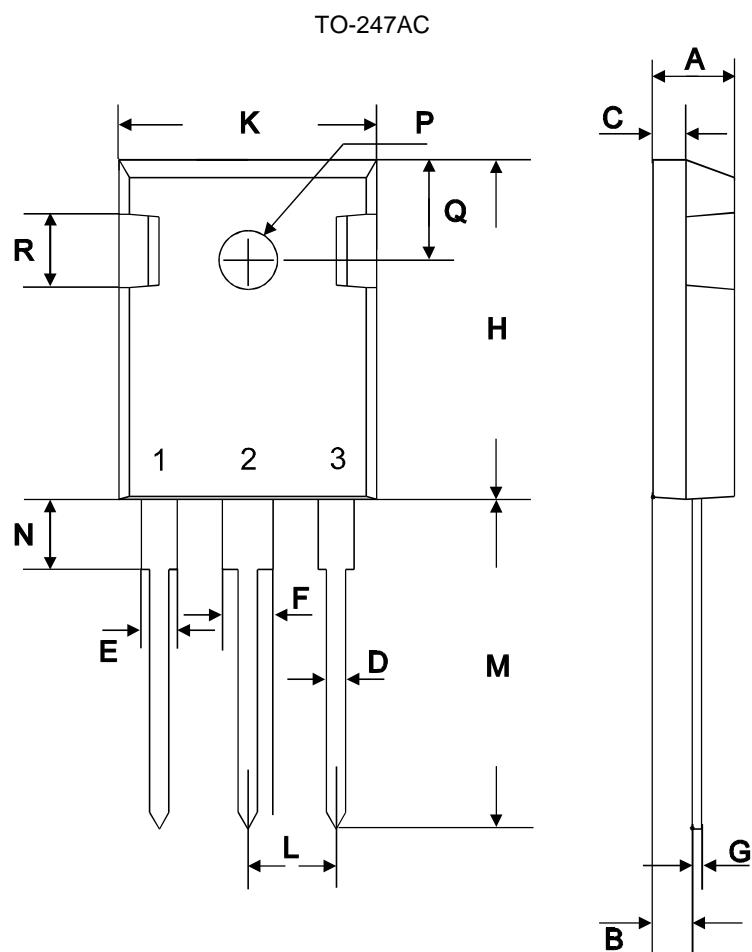
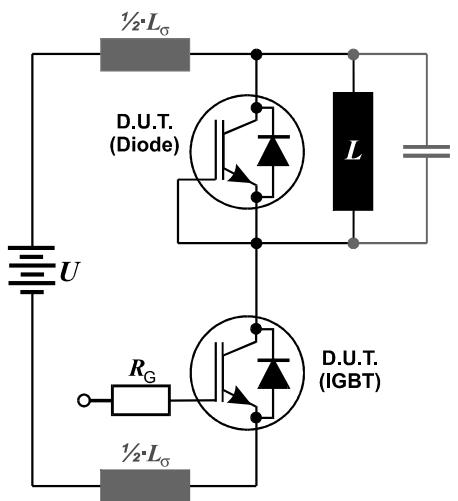
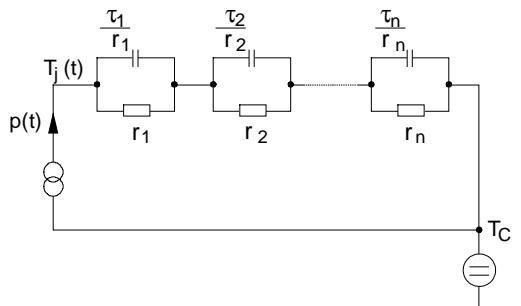
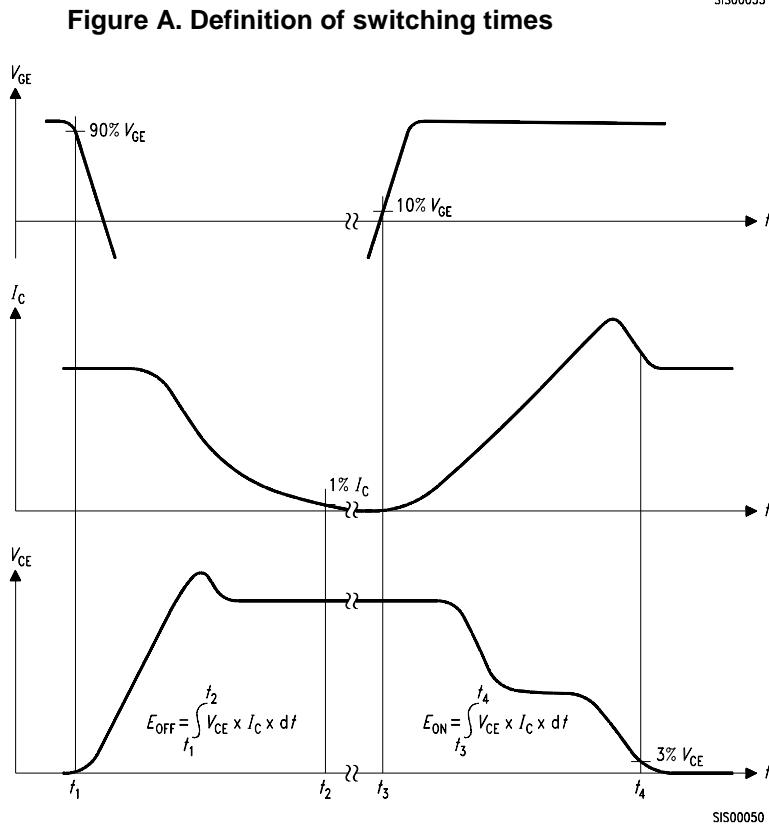
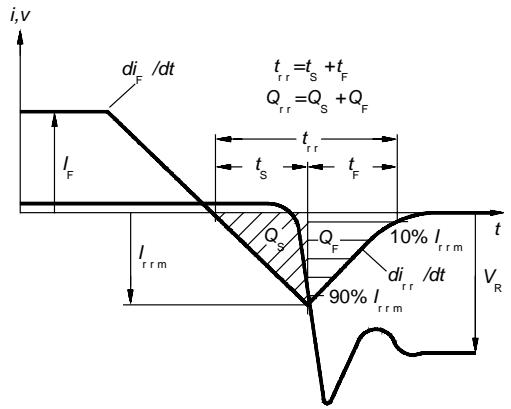
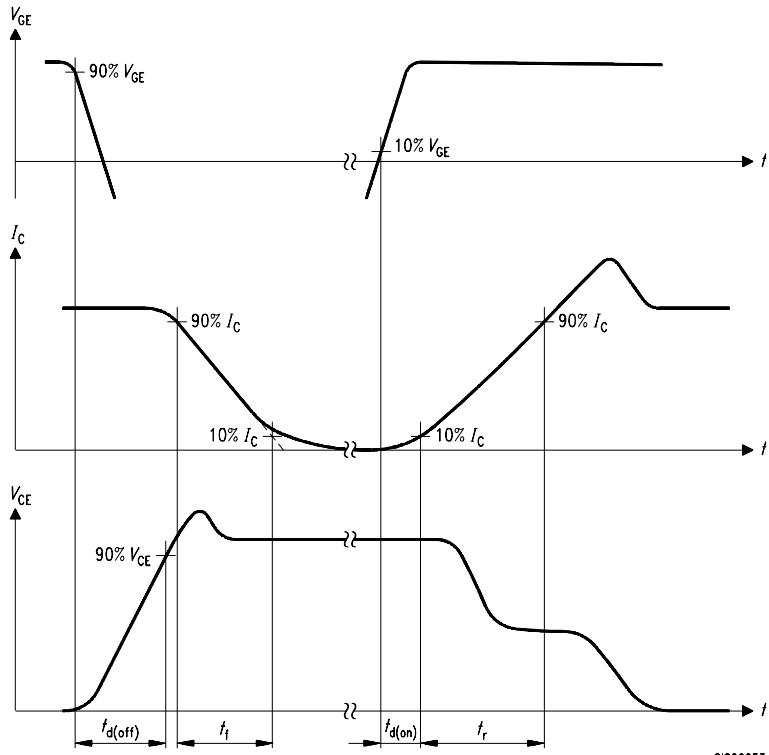


Figure 20. Typical short circuit collector current as a function of gate-emitter voltage
($100V \leq V_{CE} \leq 1200V, T_C = 25^\circ C, T_j \leq 150^\circ C$)



symbol	dimensions			
	[mm]		[inch]	
	min	max	min	max
A	4.78	5.28	0.1882	0.2079
B	2.29	2.51	0.0902	0.0988
C	1.78	2.29	0.0701	0.0902
D	1.09	1.32	0.0429	0.0520
E	1.73	2.06	0.0681	0.0811
F	2.67	3.18	0.1051	0.1252
G	0.76 max		0.0299 max	
H	20.80	21.16	0.8189	0.8331
K	15.65	16.15	0.6161	0.6358
L	5.21	5.72	0.2051	0.2252
M	19.81	20.68	0.7799	0.8142
N	3.560	4.930	0.1402	0.1941
ØP	3.61		0.1421	
Q	6.12	6.22	0.2409	0.2449





Published by
Infineon Technologies AG i Gr.,
Bereich Kommunikation
St.-Martin-Strasse 53,
D-81541 München
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