

ILC7011 Preliminary

80mA SC70 Ultra Low Noise CMOS RF-LDO™ Regulator



General Description

The ILC7011 is an 80mA, Ultra Low Noise, Low Dropout (LDO) linear regulator, designed and processed in Impala's proprietary CMOS process technology. This process combines the best CMOS features of low guiescent current, small size and low dropout voltage with the best bipolar features of high ripple rejection, ultra low noise and power handling capability. The ILC7011 offers a guiescent current of less than 100µA, a logic level enable (regulator on/off) pin, a footprint that is half the size of the industry standard SOT-23 and a low dropout voltage of 50mV at 10mA. With better than 70dB (1kHz) of ripple rejection, ultra low noise of 10μV_{RMS} and 1% output voltage accuracy, the ILC7011 sets a new standard in linear regulators for communications and personal electronics applications.

The ILC7011 is designed to operate with small, low cost, ceramic capacitors and is stable over a wide range of ESR values. In addition to the output capacitor, the ILC7011 requires only a 1µF input capacitor. The enable pin can be tied to V_{IN} for easy device layout. The ILC7011 is available in a number of fixed output voltages ranging from 2.5V to 8V. An adjacent version will be available shortly.

The ILC7011 is ideally suited for use in small size cordless and cellular handsets as well as many other low battery powered electronic devices. Please contact Impala for samples and application information.

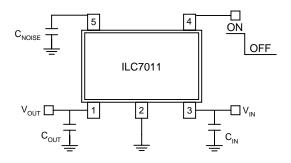
Features

- 1% output voltage accuracy
- Only 10µV_{RMS} noise from 300Hz to 100kHz
- · Uses low ESR ceramic or Tantalum output capacitor to minimize noise and output ripple.
- Only 90µA ground current at 80mA load
- Ripple rejection up to 70dB at 1kHz, 60dB at 1MHz
- Excellent line and load transient response
- Guaranteed to 80mA output current
- Industry standard five lead SC70 packages
- Fixed 2.8V, 3.0V, 3.3V, 3.6V, 4.7V, 5.0V, output voltage options
- Metal mask option available for custom voltages between 2.5V and 8V

Applications

- · Cellular phones
- Wireless communicators
- PDAs / palmtops / organizers
- Battery powered portable electronics

Typical Circuit



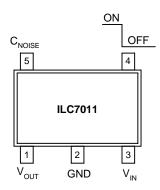
Ordering Information (T _A = 0°C to 70°C)		
ILC7011C5-XX	80mA, fixed voltage, SC-70 Package	

Note: Fixed voltage options are defined by 2-digit code as shown in the package markings information section of the data sheet

Pin Description ILC7011 (fixed voltage version)

Pin Number	Pin Name	Pin Description
4	ON/OFF	By applying less than 0.4V to this pin the device will be turned off
5	C _{NOISE}	Noise Bypass Capacitor
2	GND	Ground pin. Local ground for C _{OUT}
1	V _{OUT}	Output voltage. Connect C _{OUT} between this pin and the GND (pin 3)
2	V _{IN}	Connect Directly to Supply

Pin Package Configurations



Parameter	Symbol	Ratings	Units
Input Voltage 7011	V _{IN}	-0.3 to +9V	V
ON/OFF Input Voltage	V _{ON/OFF}	-0.3 to V _{IN}	V
Output Current	I _{OUT}	Short Circuit Protected	mA
Output Voltage	V _{OUT}	-0.3 to V _{IN} +0.3	V
Package Power Dissipation	P _D	TBD	mW
Maximum Juncion Temp. Range	T _{J(MAX)}	-40 to +125	°C
Storage Temperature	T _{STG}	-40 to +125	°C
Operation Ambient Temperature	T _{STG}	-40 to +125	°C

Electrical Characteristics ILC7011

Unless otherwise specified all limits are at $T_A = 25^{\circ}C$, $V_{IN} = V_{OUT} (NOM) + 1V$, $I_{OUT} = 1mA$, $C_{OUT} = 1mF$, $V_{ON/OFF} = 2V$

Parameter	Symbol	Conditions		Тур	Units	
Input Voltage Range	V _{IN}			2.5-8	V	
Output Voltage Accuracy				±1	%	
Line Regulation	$\Delta V_{OUT/}(V_{OUT}^*\Delta V_{IN})$	V _{OUT} (NOM) + 1V ≤ 8V		0.007	%/V	
		$I_{OUT} = 10\mu A$		0.1		
Dropout Voltage (Note 3)	V _{IN} V _{OUT}	I _{OUT} = 10mA		50	m\/	
		I _{OUT} = 20mA		70	- mV -	
		$I_{OUT} = 80mA$		235		
		I _{OUT =} 0mA		66		
Ground Pin Current	I _{GND}	I _{OUT} = 10mA		67	μΑ	
		$I_{OUT} = 80 \text{mA}$		90		
Shutdown (OFF) Current	I _{ON/OFF}	I _{ON/OFF} = 0V		0.1	μΑ	
ON/OFF Input Voltage	V _{ON/OFF}	High = Regulator On Low = Regulator Off		1.5-2.0 0.6	V	
ON/OFF Pin Input Current (Note 5)	I _{IN ON/OFF}	V _{ON/OFF} 0.6V Regulator OFF V _{ON/OFF} 2V Regulator ON		0.3 1	μA	
Peak Output Current (Note 4)	I _{OUT (peak)}	$V_{OUT} \ge 0.95 V_{OUT (NOM)}, t_{pw} = 2ms$		100-120	mA	
Output Noise Voltage	e _N	BW = 300Hz to 50kHz, C_{IN} = 1 μ F C_{NOISE} = 0.01 μ F, C_{OUT} = 1.0 μ F, I_{OUT} = 10mA		TBD	μVrms	
	AN/ /AN/	C _{OUT} = 4.7µF Tantalum	Freq = 1kHz	70		
Ripple Rejection	$\Delta V_{OUT}/\Delta V_{IN}$	I _{OUT} = 80mA	Freq = 10kHz	50	dB	
			Freq = 1MHz	65		
Dynamic Line Regulation	$\Delta V_{ ext{OUT(line)}}$	V_{IN} : $V_{OUT (NOM)} + 1V$ to V_{OUT} (NOM) + 2V, tr/tf = 2 μ s; I_{OUT} = 80mA		14	mV	
Dynamic Load Regulation	$\Delta V_{OUT(load)}$	I _{OUT} : 1mA to 80mA; tr < 5μS		40	mV	
Short Circuit Current	I _{SC}	V _{OUT} = 0V		200	mA	
Resistance Shutdown Discharge				1.5	kΩ	

Note 1: Absolute maximum ratings indicate limits which when exceeded may result in damage to the component. Electrical specifications do not apply when operating the device outside of its rated operating conditions.

Note 2: Specified Min/Max limits are production tested or guaranteed through correlation based on statistical control methods. Measurments are taken at constant junction temperature as close to ambient as possible using low duty pulse testing.

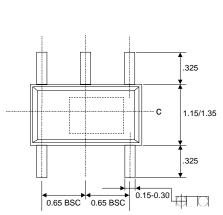
- Note 3 Dropout voltage is defined as the input to output differential voltage at which the output voltage drops 2% below the nominal value measured with a 1V differential.
- Note 4: Guaranteed by design
- Note 5: The device's shutdown pin inloudes a $2M\Omega$ internal pull down resistor connected to ground.

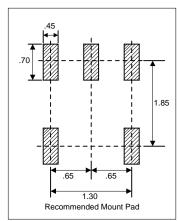


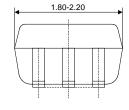
SC-70 Package Markings ILC7011C5-xx

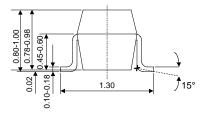
Output Voltage (V)	Grade	Order Information	Supplied As:
2.8	А	ILC7011C5-28	3K Units on Tape and Reel
3.0	A	ILC7011C5-30	3K Units on Tape and Reel
3.3	A	ILC7011C5-33	3K Units on Tape and Reel
3.6	A	ILC7011C5-36	3K Units on Tape and Reel
4.7	A	ILC7011C5-47	3K Units on Tape and Reel
5.0	A	ILC7011C5-50	3K Units on Tape and Reel

*NOTE: First two characters identify the product and the last two characters identify the datecode









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