Complementary Silicon Plastic Power Transistors

Specifically designed for power audio output, or high power drivers in audio amplifiers.

- DC Current Gain Specified up to 8.0 Amperes at Temperature
- All On Characteristics at Temperature
- High SOA: 20 A, 18 V, 100 ms
- TO-247AE Package

MAXIMUM RATINGS

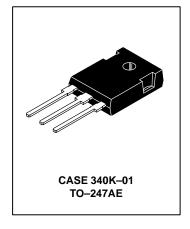
Rating	Symbol	MJW21191 MJW21192	Unit
Collector–Emitter Voltage	VCEO	150	Vdc
Collector–Base Voltage	V _{CB} 150 Vdc	Vdc	
Emitter–Base Voltage	V _{EB}	5.0	Vdc
Collector Current — Continuous — Peak	lC	8.0 16	Adc
Base Current	ΙΒ	2.0	Adc
Total Power Dissipation @ T _C = 25°C Derate above 25°C	PD	100 0.65	Watts W/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-65 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$R_{\theta JC}$	0.65	°C/W
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	50	°C/W

NPN MJW21192 PNP MJW21191

8.0 AMPERES
POWER TRANSISTORS
COMPLEMENTARY
SILICON
150 VOLTS
100 WATTS



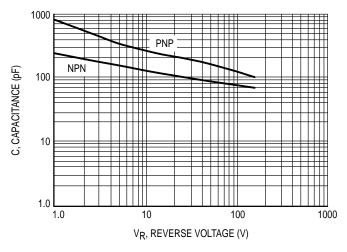


Figure 1. Typical Capacitance @ 25°C



MJW21192 MJW21191

ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector–Emitter Sustaining Voltage (1) (I _C = 10 mAdc, I _B = 0)	VCEO(sus)	150	_	Vdc
Collector Cutoff Current (V _{CB} = 250 Vdc, I _E = 0)	ICES	_	10	μAdc
Emitter Cutoff Current (VBE = 5.0 Vdc, I _C = 0)	IEBO	_	10	μAdc
ON CHARACTERISTICS (1)				
DC Current Gain ($I_C = 4.0$ Adc, $V_{CE} = 2.0$ Vdc) ($I_C = 8.0$ Adc, $V_{CE} = 2.0$ Vdc)	hFE	15 5.0	_ _	100
Collector–Emitter Saturation Voltage (I _C = 4.0 Adc, I _B = 0.4 Adc) (I _C = 8.0 Adc, I _B = 1.6 Adc)	VCE(sat)		1.0 2.0	Vdc
Base–Emitter On Voltage (I _C = 4.0 Adc, V _{CE} = 2.0 Vdc)	VBE(on)	_	2.0	Vdc
DYNAMIC CHARACTERISTICS				
Current Gain — Bandwidth Product (2) ($I_C = 1.0$ Adc, $V_{CE} = 10$ Vdc, $f_{test} = 1.0$ MHz)	fΤ	4.0	_	MHz

⁽¹⁾ Pulse Test: Pulse Width $\leq 300 \,\mu\text{s}$, Duty Cycle $\leq 2.0\%$.

⁽²⁾ $f_T = |h_{fe}| \cdot f_{test}$

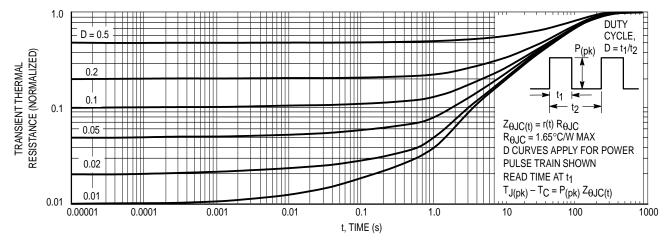


Figure 2. Thermal Response

There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate $I_{\text{C}} - V_{\text{CE}}$ limits of the transistor that must be observed for reliable operation, i.e., the transistor must not be subjected to greater dissipation then the curves indicate.

The data of Figures 3 and 4 is based on $T_{J(pk)} = 150^{\circ}C$; T_{C} is variable depending on conditions. Second breakdown pulse limits are valid for duty cycles to 10% provided $T_{J(pk)} < 150^{\circ}C$. $T_{J(pk)}$ may be calculated from the data in Figure 2. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.

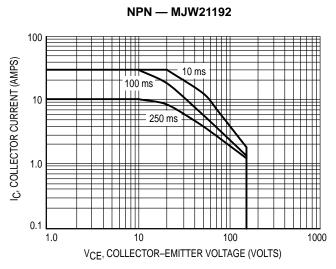


Figure 3. NPN — MJW21192 Safe Operating Area

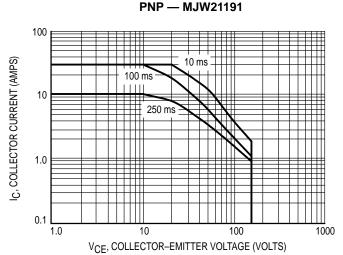


Figure 4. PNP — MJW21191 Safe Operating Area

PNP — MJW21191

TYPICAL CHARACTERISTICS

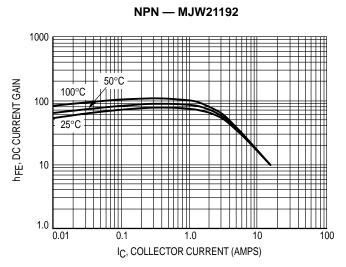


Figure 5. NPN — MJW21192 VCE = 2.0 V DC Current Gain

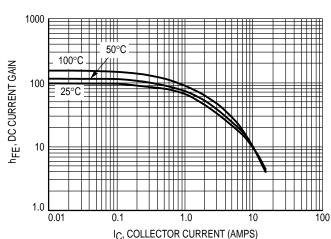


Figure 6. PNP — MJW21191 VCE = 2.0 V DC Current Gain



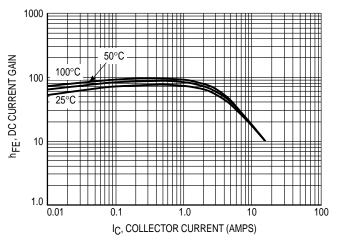
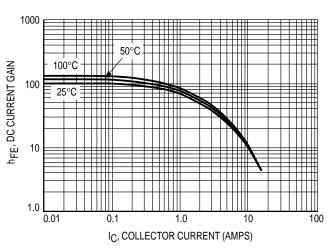


Figure 7. NPN — MJW21192 V_{CE} = 5.0 V DC Current Gain



PNP — MJW21191

Figure 8. PNP — MJW21191 VCE = 5.0 V DC Current Gain

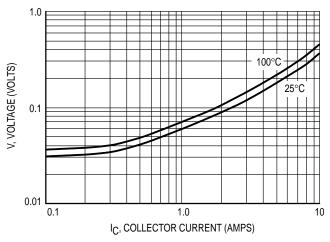


Figure 9. NPN — MJW21192 $V_{CE(sat)} I_{C}/I_{B} = 5.0$

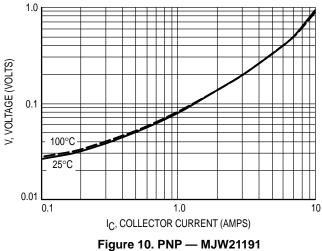


Figure 10. PNP — MJW21191 VCE(sat) IC/IB = 5.0

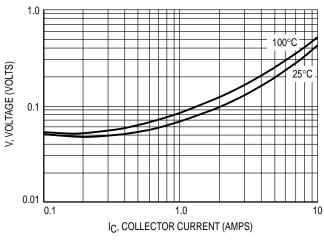


Figure 11. NPN — MJW21192 VCE(sat) IC/IB = 10

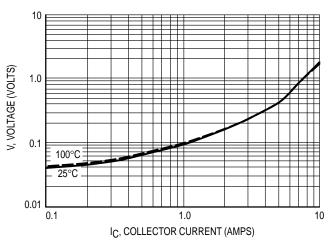


Figure 12. PNP — MJW21191 $V_{CE(sat)} I_{C}/I_{B} = 10$

NPN — MJW21192

1.0 25°C 50°C 50°C 1.0 0.1 0.001 0.1 1.0 10 IC, COLLECTOR CURRENT (AMPS)

Figure 13. NPN — MJW21192 V_{CE} = 2.0 V V_{BE(on)} Curve

PNP — MJW21191

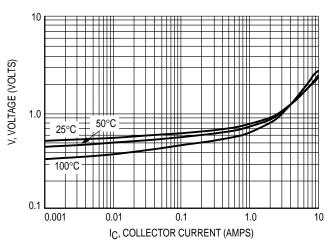
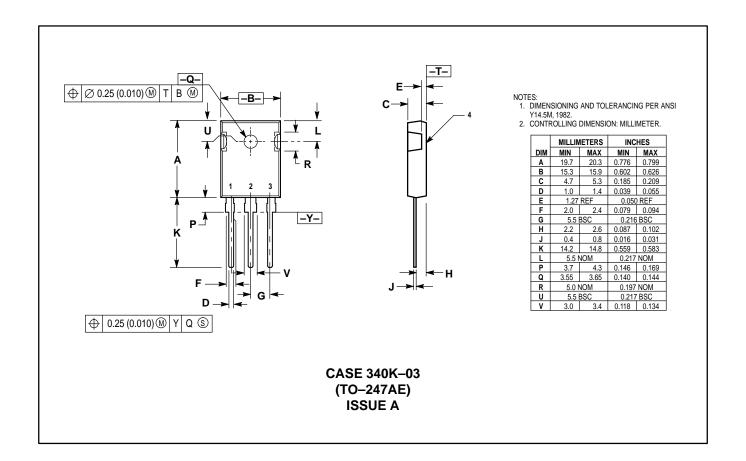


Figure 14. PNP — MJW21191 VCE = 2.0 V VBE(on) Curve

PACKAGE DIMENSIONS



MJW21192 MJW21191

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USA/EUROPE/Locations Not Listed: Motorola Literature Distribution; P.O. Box 5405, Denver, Colorado 80217. 303-675-2140 or 1-800-441-2447 JAPAN: Nippon Motorola Ltd.: SPD, Strategic Planning Office, 4–32–1, Nishi–Gotanda, Shinagawa–ku, Tokyo 141, Japan. 81–3–5487–8488

Mfax™: RMFAX0@email.sps.mot.com – TOUCHTONE 602–244–6609
ASIA/PACIFIC: Motorola Semiconductors H.K. Ltd.; 8B Tai Ping Industrial Park,
51 Ting Kok Road, Tai Po, N.T., Hong Kong. 852–26629298

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