

# DATA SHEET

**HIGH POWER CHIP RESISTORS** 

RC high power series

5%, 1% sizes 0603/0805/1206/2512

RoHS compliant & Halogen free



YAGEO Phícomp



#### SCOPE

This specification describes RC0603 to RC2512 high power chip resistors with lead-free terminations made by thick film process.

#### **APPLICATIONS**

• All general purpose applications

#### **FEATURES**

- Halogen Free Epoxy
- RoHS compliant
  - Products with lead-free terminations meet RoHS requirements
  - Pb-glass contained in electrodes, resistor element and glass are exempted by RoHS
- Reducing environmentally hazardous wastes
- High component and equipment reliability
- Saving of PCB space
- None forbidden-materials used in products/production

#### ORDERING INFORMATION - GLOBAL PART NUMBER

Both part numbers are identified by the series name, size, tolerance, packaging type, temperature coefficient, taping reel, resistance value and resistor terminal.

# **GLOBAL PART NUMBER (PREFERRED)**

#### (I) SIZE

0603 / 0805 / 1206 / 2512

#### (2) TOLERANCE

 $F = \pm 1\%$  $| = \pm 5\%$ 

#### (3) PACKAGING TYPE

R = Paper taping reelK = Embossed taping reel

#### (4) TEMPERATURE COEFFICIENT OF RESISTANCE

- = Base on spec

#### (5) TAPING REEL

 $7W = 2 \times standard power$ 

#### (6) RESISTANCE VALUE

There are  $2\sim4$  digits indicated the resistor value. Letter R/K/M is decimal point, no need to mention the last zero after R/K/M, e.g. I K2, not I K20.

Detailed resistance rules show in table of "Resistance rule of global part number".

#### (7) OPTIONAL CODE

Letter L is system default code for ordering only  $^{(Note)}$ 

# Resistance rule of global part number

Resistance coding r	ule Example
OR	0R = Jumper
XRXX (1 to 9.76 Ω)	IR = I Ω IR5 = I.5 Ω 9R76 = 9.76 Ω
XXRX (10 to 97.6 Ω)	IOR = IO Ω 97R6 = 97.6 Ω
XXXR (100 to 976 Ω)	100R = 100 Ω
$\times K \times \times$ (I to 9.76 K $\Omega$ )	IK = 1,000 Ω 9K76 = 9760 Ω
XMXX (1 to 9.76 MΩ)	$IM = 1,000,000 \Omega$ $9M76 = 9,760,000 \Omega$

#### **ORDERING EXAMPLE**

The ordering code of a RC2512 chip resistor, value 47  $\Omega$ , 2W with ±5% tolerance, supplied in 7-inch tape reel is: RC2512JR-7W47RL.

#### NOTE

- All our RSMD products are RoHS compliant and Halogen free. "LFP" of the internal 2D reel label states "Lead-Free Process"
- 2. On customized label, "LFP" or specific symbol can be printed



#### **MARKING**

#### RC0603/0805/1206/2512



E-24 series: 3 digits for 5%

First two digits for significant figure and 3rd digit for number of zeros



Both E-24 and E-96 series: 4 digits for 1%

First three digits for significant figure and 4th digit for number of zeros

For further marking information, please see special data sheet "Chip resistors marking".

#### **CONSTRUCTION**

The resistor is constructed on top of a high-grade ceramic body. Internal metal electrodes are added on each end to make the contacts to the thick film resistive element. The composition of the resistive element is a noble metal embedded into a glass and covered by a second glass to prevent environmental influences. The resistor is laser trimmed to the rated resistance value. The resistor is covered with a protective epoxy coat, finally the two external terminations (matte tin on Ni-barrier) are added. See fig. 3.

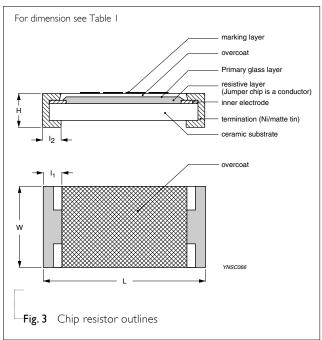
## **DIMENSIONS**

-Table I For outlines see fig. 3

**TYPE**  $L (mm) W (mm) H (mm) I_1 (mm)$ **RC0603** 1.60 ±0.10 0.80 ±0.10 0.45 ±0.10 0.25 ±0.15 0.25 ±0.15 **RC0805** 2.00 ±0.10 1.25 ±0.10 0.50 ±0.10 0.35 ±0.20 0.35 ±0.20 RC1206 3.10 ±0.10 1.60 ±0.10 0.55 ±0.10 0.45 ±0.20 0.40 ±0.20

RC2512 6.35 ±0.10 3.10 ±0.15 0.55 ±0.10 0.60 ±0.20 0.50 ±0.20

#### **OUTLINES**





8

#### **ELECTRICAL CHARACTERISTICS**

#### Table 2

TYPE	Resistance Range	Operating Temperature Range	Power Rating	Max. Working Vol.	Dielectric Withstand Vol.	Max. Overload Vol.	Temperature Coefficient of Resistance
RC0603	$1 \Omega \le R \le 10 K\Omega$	$R \le I M\Omega$ -55 °C to +155 °C 1/2	1/5 W	50 V	100 V	100 V	
RC0805	$- \qquad   \ \Omega \le R \le   \ M\Omega$		1/4 W	150 V	300 V	300 V	1200 222/00
RC1206			1/2 W	200 V	400 V	500 V	±200 ppm/°C
RC2512	$1 \Omega \le R \le 150\Omega$		2 W	200 V	400 V	500 V	

### FOOTPRINT AND SOLDERING PROFILES

For recommended footprint and soldering profiles, please see the special data sheet "Chip resistors mounting".

#### PACKING STYLE AND PACKAGING QUANTITY

Table 3 Packing style and packaging quantity

PACKING STYLE	REEL DIMENSION	RC0603	RC0805	RC1206	RC2512
Paper taping reel (R)	7" (178 mm)	5,000	5,000	5,000	
	10" (254 mm)	10,000	10,000	10,000	
	13" (330 mm)	20,000	20,000	20,000	
Embossed taping reel (K)	7" (178 mm)				4,000

#### NOTE

# FUNCTIONAL DESCRIPTION

# **OPERATING TEMPERATURE RANGE**

Range: -55 °C to +155 °C

#### **POWER RATING**

Each type rated power at 70 °C:

RC0603=1/5 W; RC0805=1/4 W; RC1206=1/2 W;

RC2512=2 W

#### **RATED VOLTAGE**

The DC or AC (rms) continuous working voltage corresponding to the rated power is determined by the following formula:

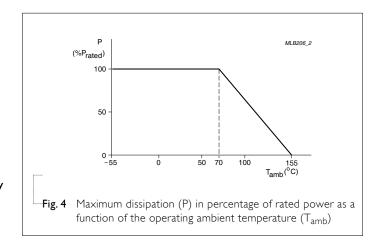
$$V = \sqrt{(P \times R)}$$

Where

V = Continuous rated DC or AC (rms) working voltage (V)

P = Rated power (W)

 $R = Resistance value (\Omega)$ 



<sup>1.</sup> For paper/embossed tape and reel specification/dimensions, please see the special data sheet "Chip resistors packing".

# TESTS AND REQUIREMENTS

**Table 4** Test condition, procedure and requirements

TEST	TEST METHOD	PROCEDURE	REQUIREMENTS
Temperature Coefficient of	IEC 60115-1 4.8	At +25/–55 °C and +25/+125 °C	Refer to table 2
Resistance (T.C.R.)		Formula:	
(1.C.N.)		T.C.R= $\frac{R_2-R_1}{R_1(t_2-t_1)} \times 10^6 \text{ (ppm/°C)}$	
		Where $t_1$ =+25 °C or specified room temperature	
		t <sub>2</sub> =-55 °C or +125 °C test temperature	
		$R_1$ =resistance at reference temperature in ohms	
		R <sub>2</sub> =resistance at test temperature in ohms	
Life/Endurance	IEC 60115-1 4.25.1	At 70±5 °C for 1,000 hours, RCWV applied for 1.5 hours on, 0.5 hour off, still air required	$\pm (1.0\% + 0.05 \ \Omega)$ for 1% tol. $\pm (3.0\% + 0.05 \ \Omega)$ for 5% tol.
High	IEC 60068-2-2	1,000 hours at 155±5 °C, unpowered	$\pm (1.0\% + 0.05 \ \Omega)$ for 1% tol.
Temperature Exposure/ Endurance at Upper Category Temperature			$\pm (2.0\% + 0.05 \ \Omega)$ for 5% tol.
Moisture Resistance	MIL-STD-202G Method-106G	Each temperature / humidity cycle is defined at 8 hours, 3 cycles / 24 hours for IOd. with 25 °C / 65 °C 95% R.H, without steps 7a & 7b, unpowered	$\pm (0.5\% \pm 0.05 \ \Omega)$ for 1% tol. $\pm (2.0\% \pm 0.05 \ \Omega)$ for 5% tol.
		Parts mounted on test-boards, without condensation on parts	
		Measurement at 24±2 hours after test conclusion	
Thermal Shock	MIL-STD-202G Method-107G	-55/+125 °C	$\pm (0.5\% + 0.05 \ \Omega)$ for 1% tol.
		Number of cycles required is 300. Devices unmounted	$\pm (1\% + 0.05 \ \Omega)$ for 5% tol.
		Maximum transfer time is 20 seconds. Dwell time is 15 minutes. Air – Air	
Short Time Overload	IEC60115-1 4.13	2.5 times of rated voltage or maximum overload voltage whichever is less for 5 sec at room temperature	$\pm$ (1.0%+0.05 $\Omega$ ) for 1% tol. $\pm$ (2.0%+0.05 $\Omega$ ) for 5% tol. No visible damage

Chip Resistor Surface Mount RC-High power SERIES 0603 to 2512

TEST	TEST METHOD	PROCEDURE	REQUIREMENTS		
Board Flex/ Bending	IEC 60068-2-21	Chips mounted on a 90mm glass epoxy resin PCB (FR4)	$\pm (1.0\% + 0.05 \ \Omega)$ for 1% No visible damage	1.0%+0.05 Ω) for 1%, 5% tol.	
		5 mm bending	. vo visiolo dall'iago		
		Bending time: 60±5 seconds			
Low Temperature	IEC 60068-2-1	The resistor shall be subjected to a DC rated voltage for 1.5 h-on, 0.5 h-off, at -55±3 °C	$\pm (0.5\% + 0.05 \ \Omega)$ for 1% tol. $\pm (1.0\% + 0.05 \ \Omega)$ for 5% tol.		
Operation		This constitutes shall be repeated for 96 hours	No visible damage		
		However the applied voltage shall not exceed the maximum operating voltage	•		
Insulation Resistance	IEC 60115-1 4.6	Rated continuous overload voltage (RCOV) for I minute	≥10 GΩ		
Dielectric Withstand Voltage	IEC 60115-1 4.7	Maximum voltage (V <sub>rms</sub> ) applied for I minute	No breakdown or flashover		
Resistance to Solvent	IPC/JEDEC J-STD-020D	Isopropylalcohol (C <sub>3</sub> H <sub>7</sub> OH) followed by brushing	No smeared		
Noise	IEC 60115-1 4.12	Maximum voltage (Vrms) applied	Resistors range	Value	
			R < 100 Ω	10 dB	
			$100 \Omega \le R < 1 K\Omega$	20 dB	
			$I K\Omega \le R < 10 K\Omega$	30 dB	
			$10 \text{ K}\Omega \leq R < 100 \text{ K}\Omega$	40 dB	
			$100 \text{ K}\Omega \leq R < 1 \text{ M}\Omega$	46 dB	
Biased Humidity (steady state)	IEC 60115-1 4.37	Steady state for 1000 hours at 40 °C / 95% R.H.	$\pm$ (1.0%+0.05 $\Omega$ ) for 1% tol.		
		RCWV applied for 1.5 hours on and 0.5 hour off	$\pm (2.0\% + 0.05~\Omega)$ for 5% tol.		
Intermittent	IEC 60115-1 4.39	2.5 times of rated voltage or maximum overload	$\pm$ (1.0%+0.05 $\Omega$ ) for 1% tol.		
Overload		voltage whichever is less for 1 second on and 25 seconds off; total 10,000 cycles	$\pm (2.0\% + 0.05~\Omega)$ for 5% tol.		

Chip Resistor Surface Mount RC-High power SERIES 0603 to 2512

TEST	TEST METHOD	PROCEDURE	REQUIREMENTS
Solderability			
- Wetting	IPC/JEDEC J-STD-002B test B	Electrical Test not required	Well tinned (≥95% covered)
		Magnification 50X	No visible damage
		SMD conditions:	
		I <sup>st</sup> step: method B, aging 4 hours at 155 °C dry heat	
		2 <sup>nd</sup> step: lead-free solder bath at 245±3 °C	
		Dipping time: 3±0.5 seconds	
- Leaching	IPC/JEDEC J-STD-002B test D	Lead-free solder, 260 °C, 30 seconds immersion time	No visible damage
- Resistance to	IEC 60068-2-58	Condition B, no pre-heat of samples	$\pm (0.5\% + 0.05 \ \Omega)$ for 1% tol.
Soldering Heat		Lead-free solder, 260 °C, 10 seconds	$\pm$ (1.0%+0.05 $\Omega$ ) for 5% tol.
		immersion time	No visible damage
		Procedure 2 for SMD: devices fluxed and cleaned with isopropanol	

8

8

Chip Resistor Surface Mount RC-High power SERIES 0603 to 2512

REVISION HISTORY

REVISION DATE CHANGE NOTIFICATION DESCRIPTION

Version 0 Dec 14, 2010 - - First issue of this specification

<sup>&</sup>quot;Yageo reserves all the rights for revising the content of this datasheet without further notification, as long as the products itself are unchanged. Any product change will be announced by PCN."

