



N-Channel 30-V (D-S) MOSFET with Schottky Diode

PRODUCT SUMMARY

V _{DS} (V)	r _{DS(on)} (Ω)	I _D (A) ^a	Q _g (Typ)
30	0.00375 at V _{GS} = 10 V	34	35.7 nC
	0.0047 at V _{GS} = 4.5 V	30	

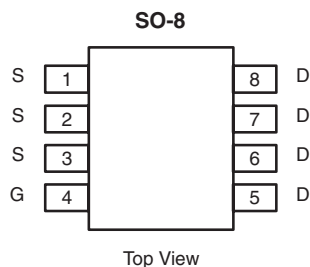
FEATURES

- SkyFET™ Monolithic TrenchFET® Power MOSFET and Schottky Diode
- 100 % R_g and UIS Tested

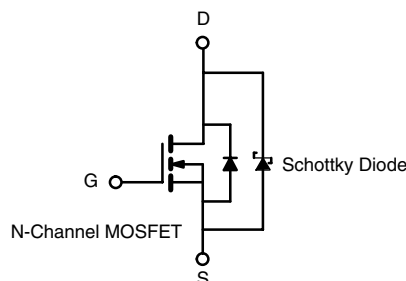
RoHS
COMPLIANT

APPLICATIONS

- Notebook CPU Core
- Buck Converter
- Synchronous Rectifier Switch



Ordering Information: Si4642DY-T1-E3 (Lead (Pb)-free)

ABSOLUTE MAXIMUM RATINGS T_A = 25 °C, unless otherwise noted

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _{GS}	± 20	
Continuous Drain Current (T _J = 150 °C)	I _D	T _C = 25 °C	A
		T _C = 70 °C	
		T _A = 25 °C	
		T _A = 70 °C	
Pulsed Drain Current	I _{DM}	70	A
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C	
		T _A = 25 °C	
Single Pulse Avalanche Current	I _{AS}	45	mJ
Single Pulse Avalanche Energy	E _{AS}	101	
Maximum Power Dissipation	P _D	T _C = 25 °C	W
		T _C = 70 °C	
		T _A = 25 °C	
		T _A = 70 °C	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS

Parameter		Symbol	Typ	Max	Unit
Maximum Junction-to-Ambient ^{b, d}	t ≤ 10 sec	R _{thJA}	29	35	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	13	16	

Notes:

a. Based on T_C = 25 °C.

b. Surface Mounted on 1" x 1" FR4 Board.

c. t = 10 sec.

d. Maximum under Steady State conditions is 80 °C/W.

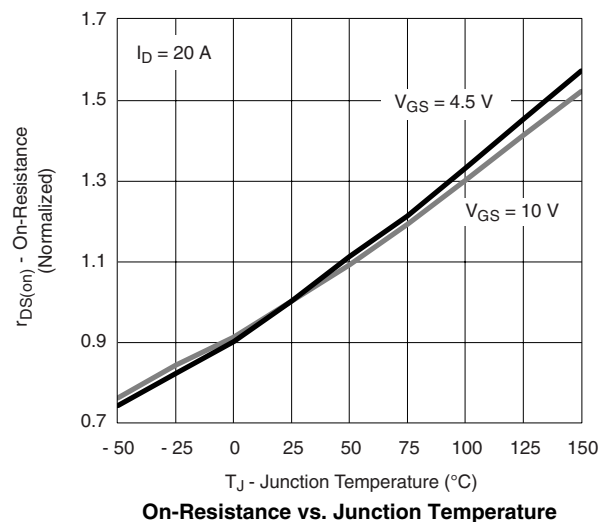
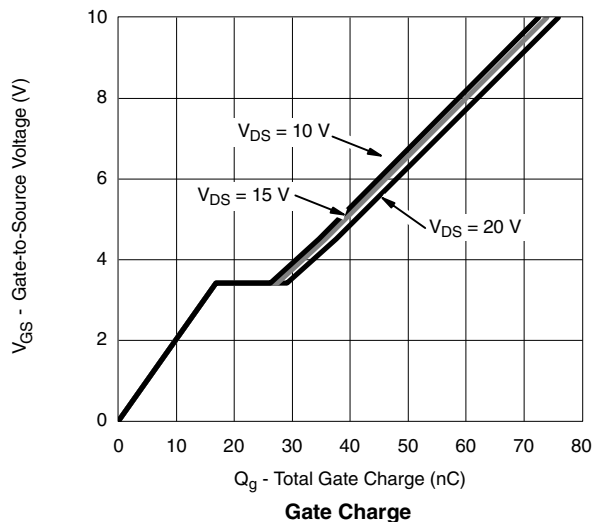
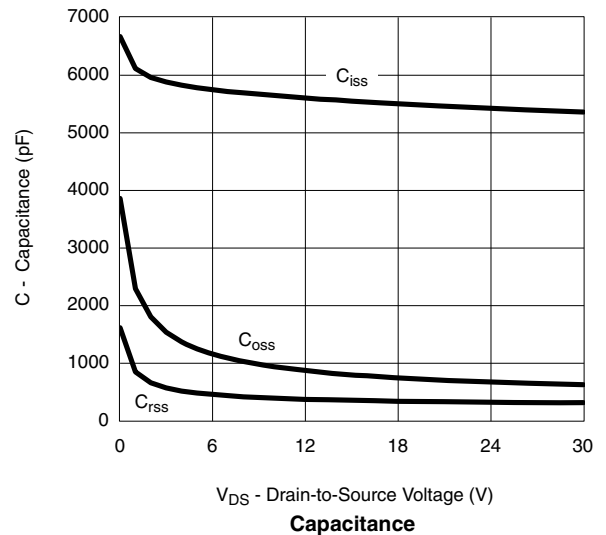
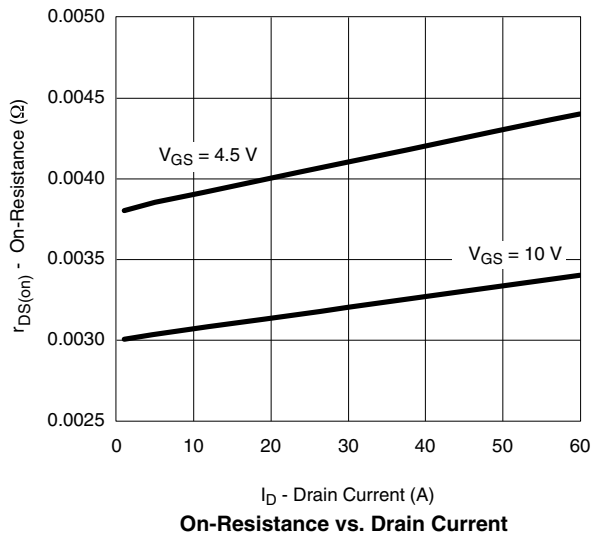
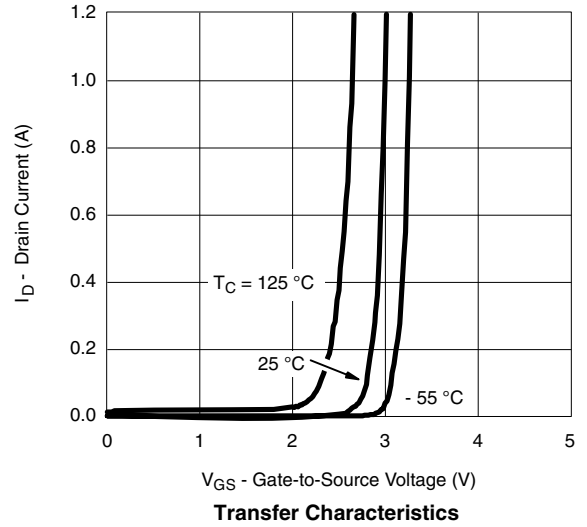
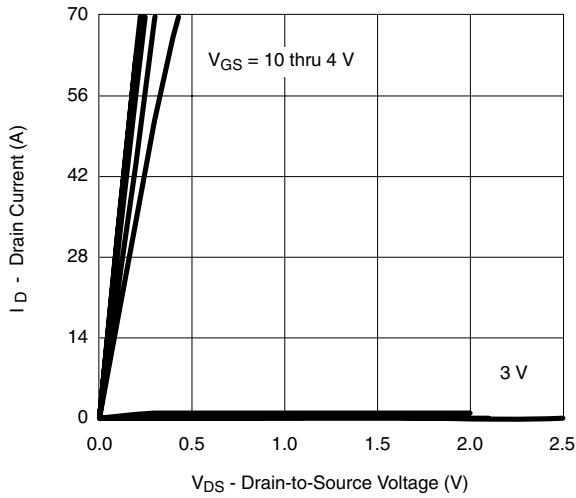
SPECIFICATIONS T _J = 25 °C, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 1 mA	30			V
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 1 mA	1.5		3	
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V		0.05	0.2	mA
		V _{DS} = 30 V, V _{GS} = 0 V, T _J = 100 °C		5.5	50	
On -State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 5 V, V _{GS} = 10 V	40			A
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = 10 V, I _D = 20 A		0.0031	0.00375	Ω
		V _{GS} = 4.5 V, I _D = 15 A		0.0039	0.0047	
Forward Transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 20 A		108		S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{DS} = 15 V, V _{GS} = 0 V, f = 1 MHz		5540		pF
Output Capacitance	C _{oss}			790		
Reverse Transfer Capacitance	C _{rss}			346		
Total Gate Charge	Q _g	V _{DS} = 15 V, V _{GS} = 10 V, I _D = 20 A		74	110	nC
Gate-Source Charge	Q _{gs}	V _{DS} = 15 V, V _{GS} = 4.5 V, I _D = 20 A		35.7	54	
Gate-Drain Charge	Q _{gd}			16.8		
Gate Resistance	R _g			10.7		
Gate Resistance	R _g	f = 1 MHz		1.5	2.3	Ω
Turn-On Delay Time	t _{d(on)}	V _{DD} = 15 V, R _L = 3 Ω I _D ≅ 5 A, V _{GEN} = 4.5 V, R _G = 1 Ω		76	115	ns
Rise Time	t _r			180	270	
Turn-Off Delay Time	t _{d(off)}			53	80	
Fall Time	t _f			50	75	
Turn-On Delay Time	t _{d(on)}	V _{DD} = 15 V, R _L = 3 Ω I _D ≅ 5 A, V _{GEN} = 10 V, R _G = 1 Ω		17	26	
Rise Time	t _r			24	36	
Turn-Off Delay Time	t _{d(off)}			46	70	
Fall Time	t _f			9	15	
Drain-Source Body Diode and Schottky Characteristics						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			7	A
Pulse Diode Forward Current ^a	I _{SM}				70	
Body Diode Voltage	V _{SD}	I _S = 2 A		0.44	0.53	V
Body Diode Reverse Recovery Time	t _{rr}	I _F = 13 A, di/dt = 100 A/μs, T _J = 25 °C		36	55	ns
Body Diode Reverse Recovery Charge	Q _{rr}			34	52	nC
Reverse Recovery Fall Time	t _a			19		ns
Reverse Recovery Rise Time	t _b			17		

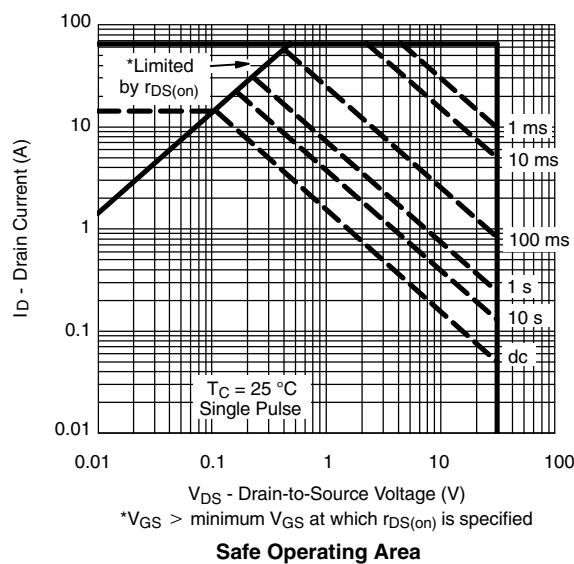
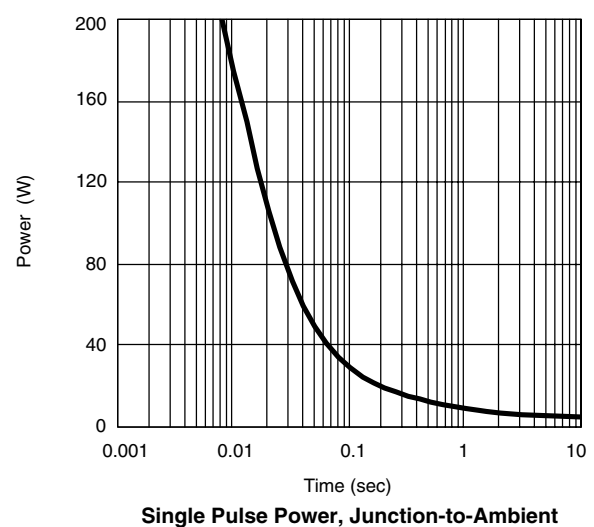
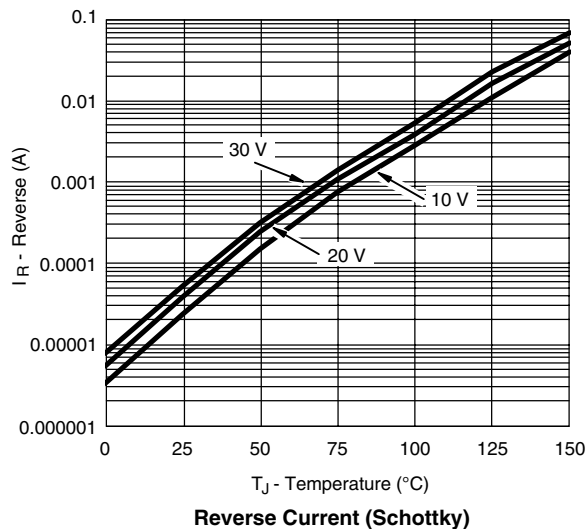
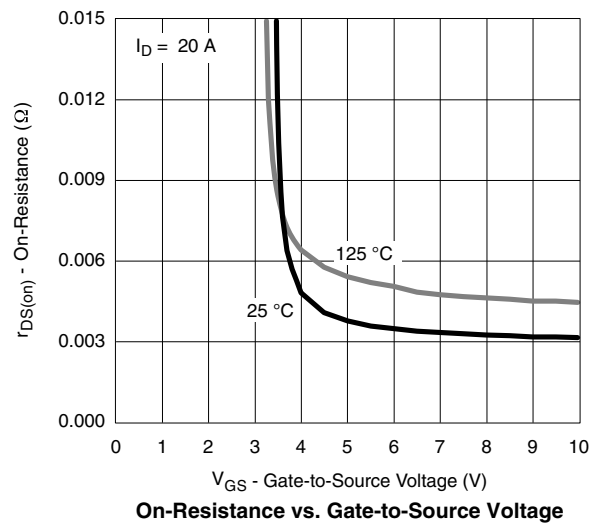
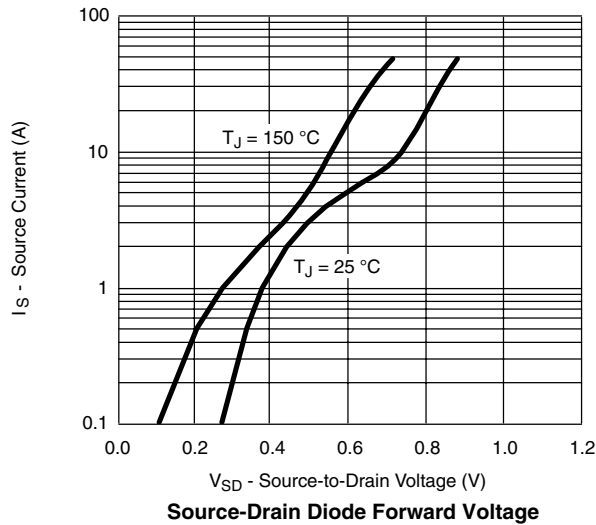
Notes:

a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.

b. Guaranteed by design, not subject to production testing.

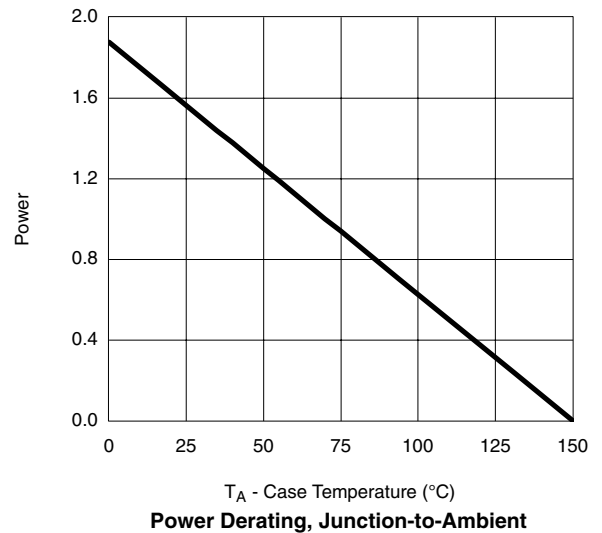
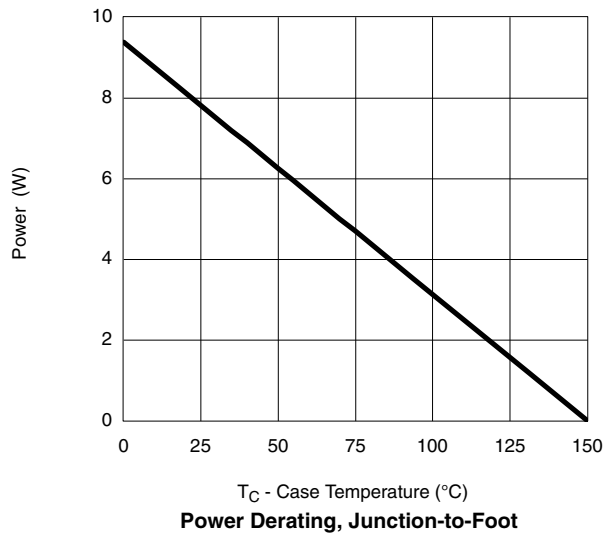
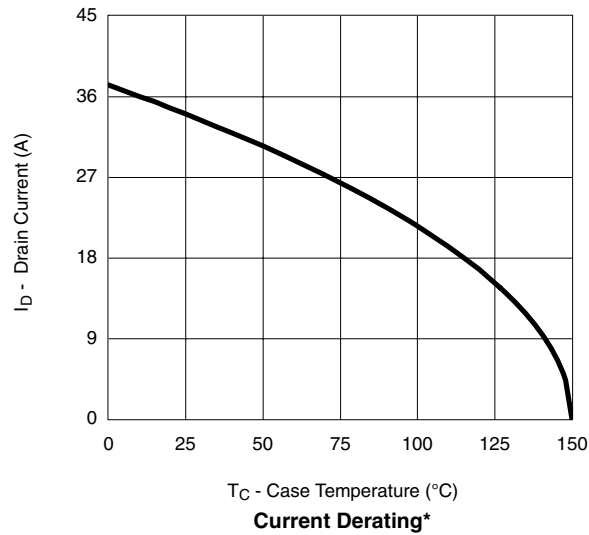
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted

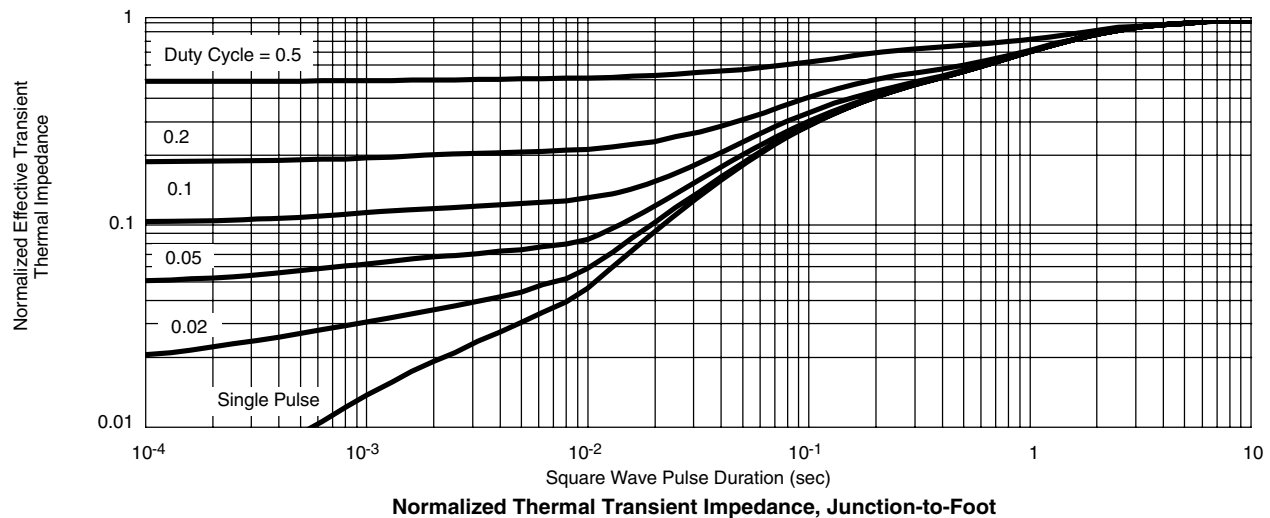
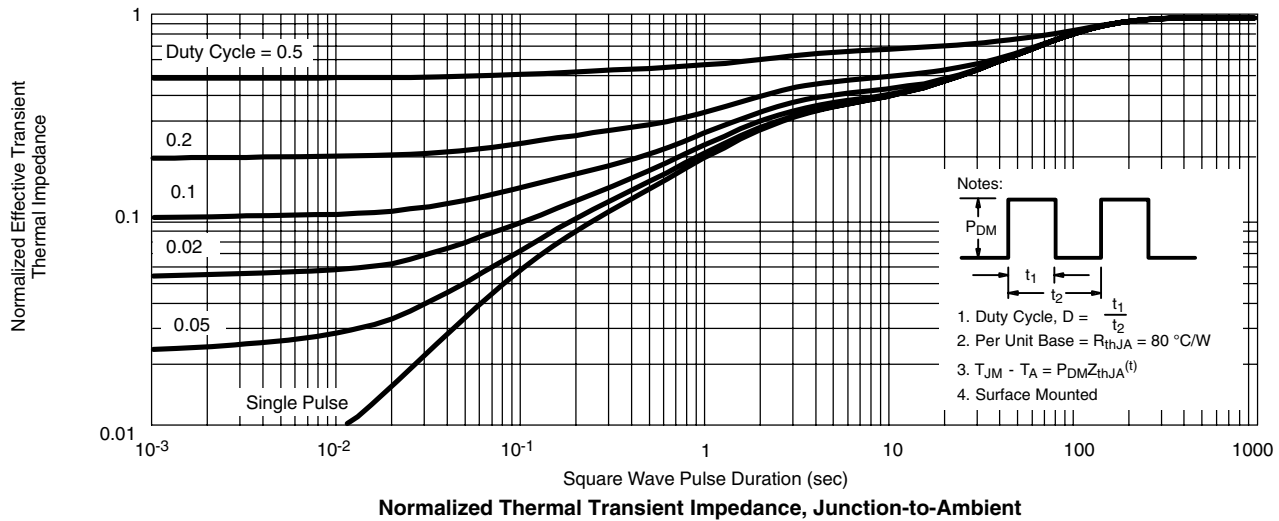
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* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

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