

P-Channel 60-V (D-S) MOSFET

PRODUCT SUMMARY

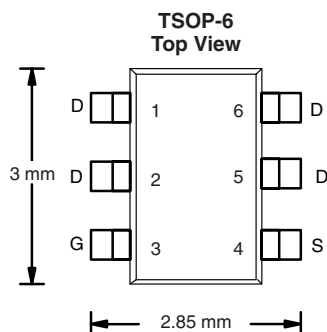
V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A) ^d	Q_g (Typ.)
- 60	0.216 at $V_{GS} = - 10$ V	- 2.9	4.4 nC
	0.288 at $V_{GS} = - 4.5$ V	- 2.5	

FEATURES

- TrenchFET[®] Power MOSFET
- 100 % R_g Tested

APPLICATIONS

- Load Switch

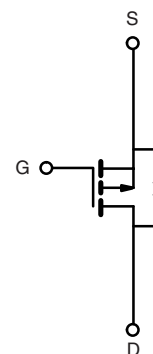

RoHS
COMPLIANT


Marking Code

AS XXX

Lot Traceability
and Date Code

Part # Code



P-Channel MOSFET

Ordering Information: Si3459BDV-T1-E3 (Lead (Pb)-free)

ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless otherwise noted

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	- 60	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ($T_J = 150$ °C)	$T_C = 25$ °C	- 2.9	A
	$T_C = 70$ °C	- 2.3	
	$T_A = 25$ °C	- 2.2 ^{a, b}	
	$T_A = 70$ °C	- 1.8 ^{a, b}	
Pulsed Drain Current	I_{DM}	- 8	
Continuous Source-Drain Diode Current	$T_C = 25$ °C	- 2.9	
	$T_A = 25$ °C	- 1.7 ^{a, b}	
Maximum Power Dissipation	$T_C = 25$ °C	3.3	W
	$T_C = 70$ °C	2.1	
	$T_A = 25$ °C	2 ^{a, b}	
	$T_A = 70$ °C	1.3 ^{a, b}	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 150	°C
Soldering Recommendations (Peak Temperature)		260	

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{a, c}	R_{thJA}	53	62.5	°C/W
Maximum Junction-to-Foot (Drain)	R_{thJF}	32	38	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

b. $t = 5$ s.

c. Maximum under steady state conditions is 110 °C/W.

d. Based on $T_C = 25$ °C.

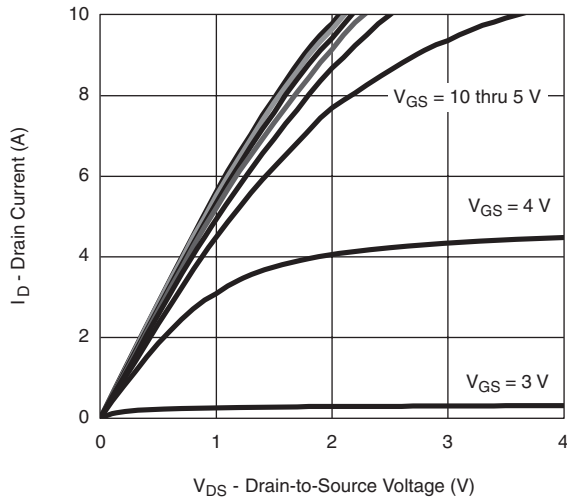
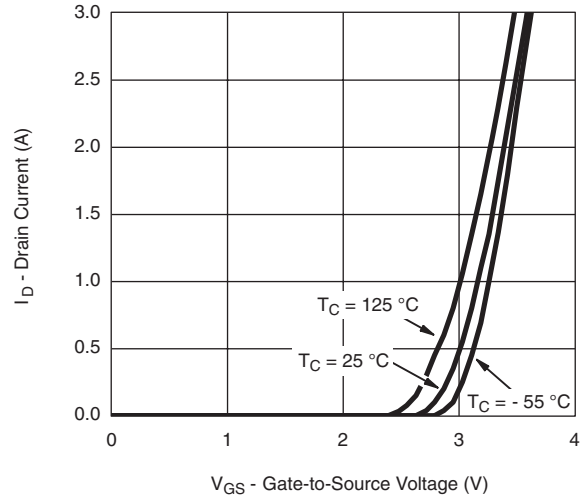
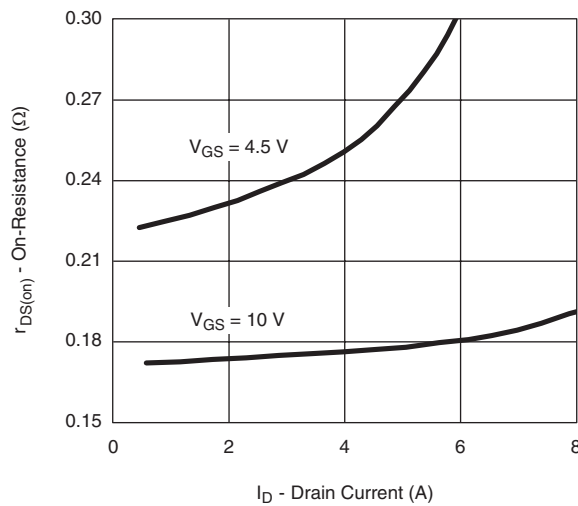
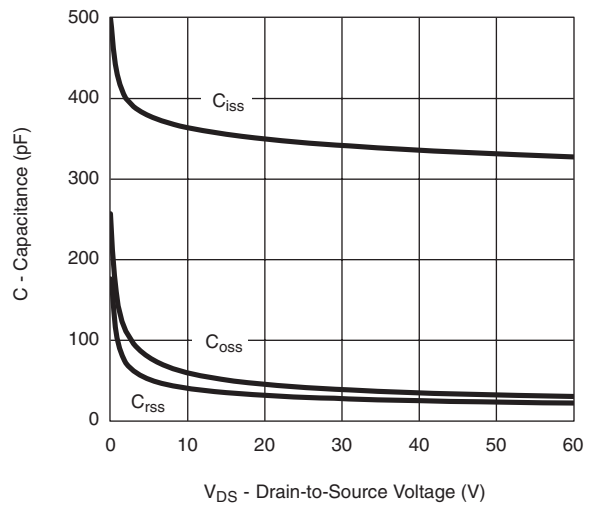
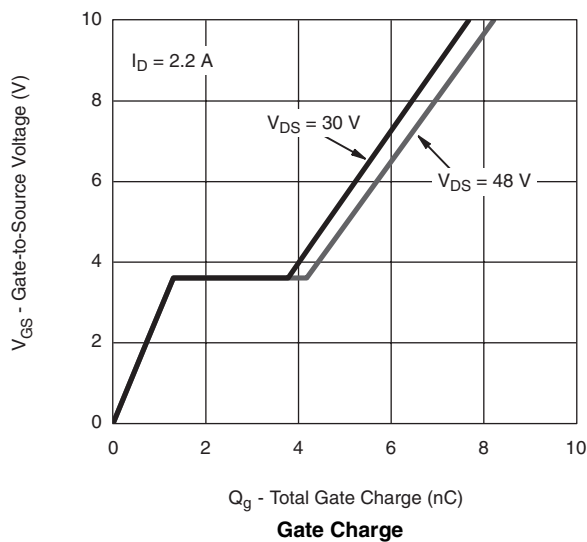
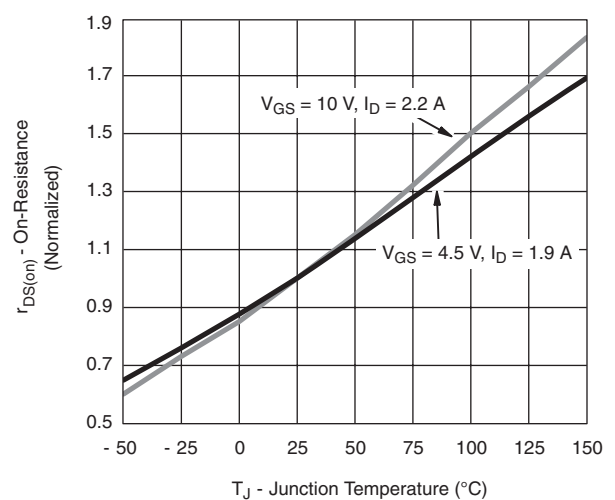
SPECIFICATIONS T _J = 25 °C, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = - 250 μA	- 60			V
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	I _D = - 250 μA		- 65		mV/°C
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J			4		
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = - 250 μA	- 1		- 3	V
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 60 V, V _{GS} = 0 V			- 1	μA
		V _{DS} = - 60 V, V _{GS} = 0 V, T _J = 70 °C			- 10	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ - 5 V, V _{GS} = - 10 V	- 8			A
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = - 10 V, I _D = - 2.2 A		0.180	0.216	Ω
		V _{GS} = - 4.5 V, I _D = - 1.9 A		0.240	0.288	
Forward Transconductance ^a	g _{fs}	V _{DS} = - 15 V, I _D = - 2.2 A		4		S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{DS} = - 30 V, V _{GS} = 0 V, f = 1 MHz		350		pF
Output Capacitance	C _{oss}			40		
Reverse Transfer Capacitance	C _{rss}			30		
Total Gate Charge	Q _g	V _{DS} = - 30 V, V _{GS} = - 10 V, I _D = - 2.2 A		7.7	12	nC
		V _{DS} = - 30 V, V _{GS} = - 4.5 V, I _D = - 2.2 A		4.4	6.6	
Gate-Source Charge	Q _{gs}			1.3		
Gate-Drain Charge	Q _{gd}			2.5		
Gate Resistance	R _g	f = 1 MHz	2	10	20	Ω
Turn-On Delay Time	t _{d(on)}	V _{DD} = - 30 V, R _L = 16.7 Ω I _D ≅ - 1.8 A, V _{GEN} = - 4.5 V, R _g = 1 Ω		45	68	ns
Rise Time	t _r			60	90	
Turn-Off Delay Time	t _{d(off)}			16	25	
Fall Time	t _f			13	20	
Turn-On Delay Time	t _{d(on)}	V _{DD} = - 30 V, R _L = 16.7 Ω I _D ≅ - 1.8 A, V _{GEN} = - 10 V, R _g = 1 Ω		5	10	
Rise Time	t _r			12	20	
Turn-Off Delay Time	t _{d(off)}			18	30	
Fall Time	t _f			10	15	
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			- 2.9	A
Pulse Diode Forward Current	I _{SM}				- 8	
Body Diode Voltage	V _{SD}	I _S = - 1.8 A, V _{GS} = 0 V		- 0.8	- 1.2	V
Body Diode Reverse Recovery Time	t _{rr}	I _F = - 1.8 A, di/dt = 100 A/μs, T _J = 25 °C		28	56	ns
Body Diode Reverse Recovery Charge	Q _{rr}			35	70	nC
Reverse Recovery Fall Time	t _a			23		ns
Reverse Recovery Rise Time	t _b			5		

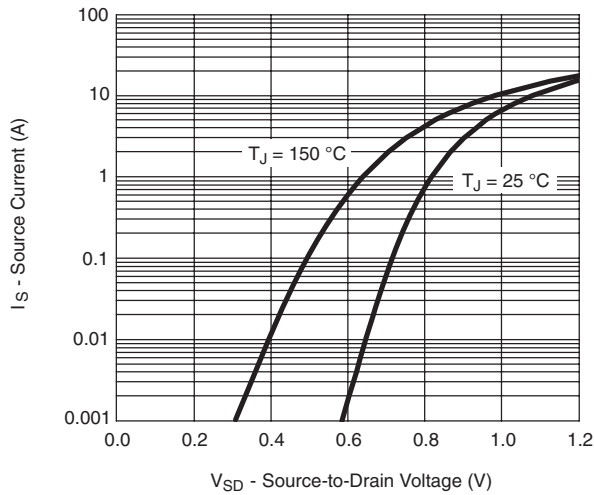
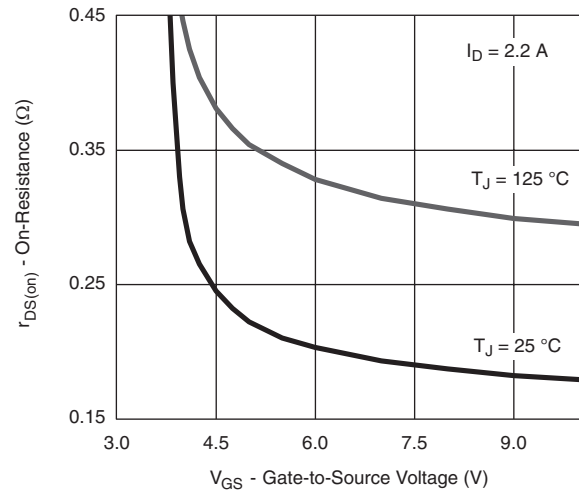
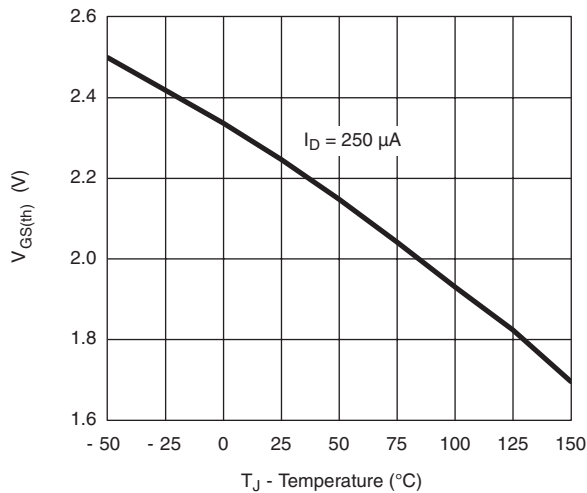
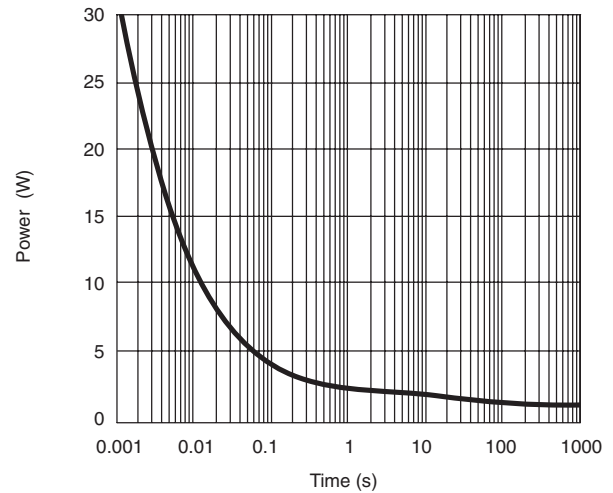
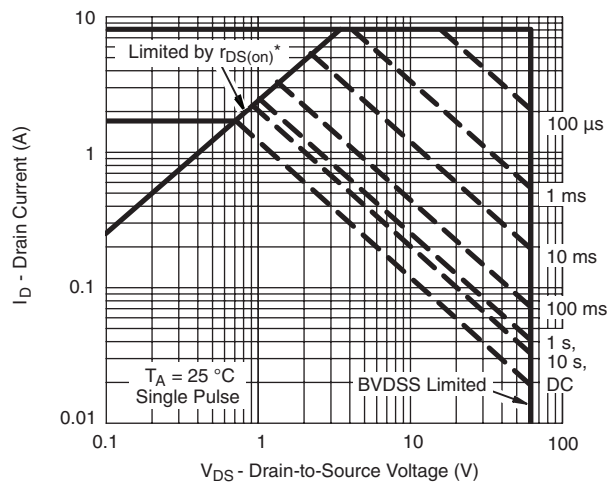
Notes:

a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$

b. Guaranteed by design, not subject to production testing.

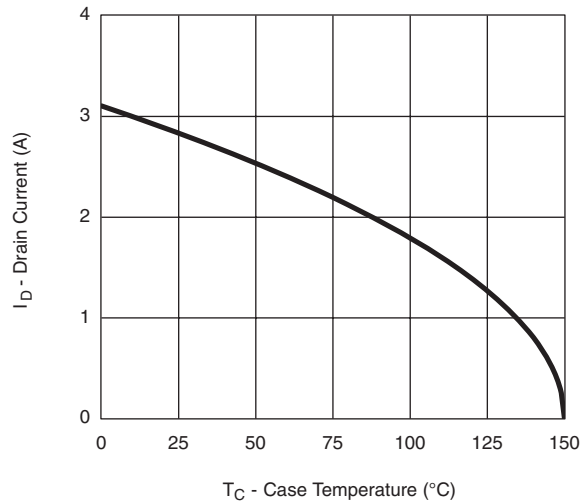
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted**Output Characteristics****Transfer Characteristics****On-Resistance vs. Drain Current and Gate Voltage****Capacitance****Gate Charge****On-Resistance vs. Junction Temperature**

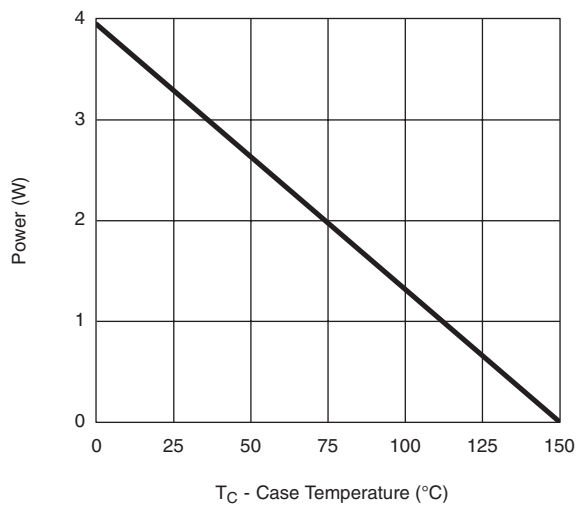
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted**Source-Drain Diode Forward Voltage****On-Resistance vs. Gate-to-Source Voltage****Threshold Voltage****Single Pulse Power*** $V_{GS} >$ minimum V_{GS} at which $r_{DS(on)}$ is specified**Safe Operating Area, Junction-to-Ambient**



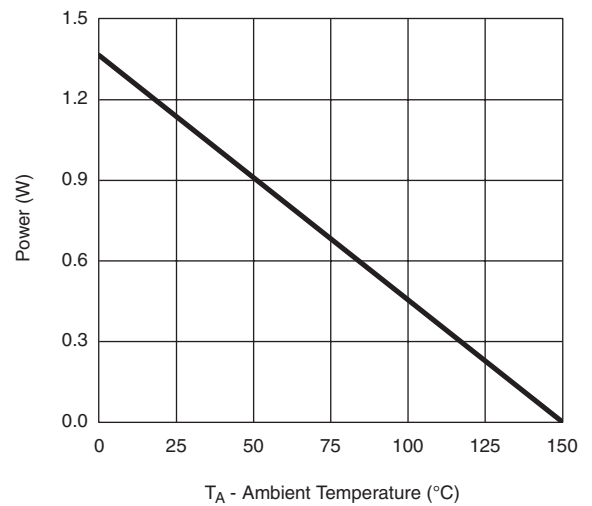
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Current Derating*

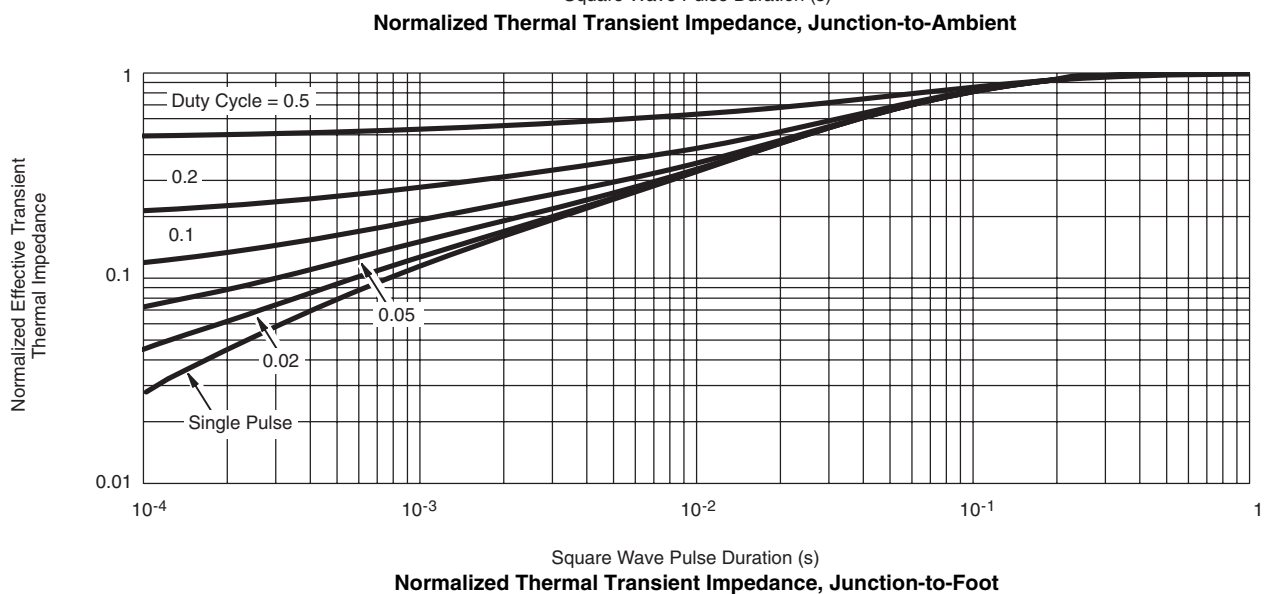
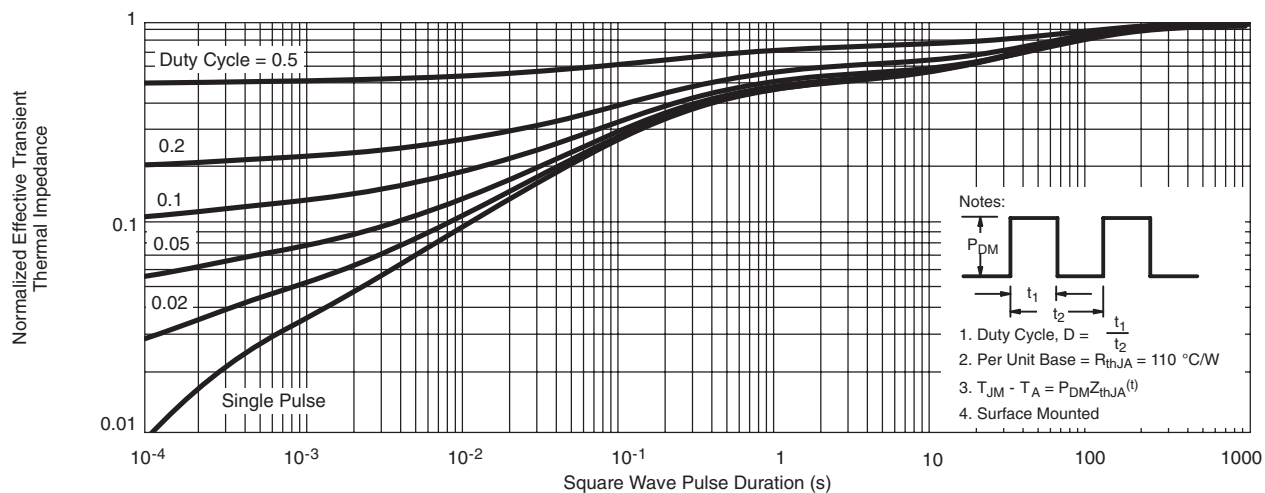


Power Junction-to-Case



Power Junction-to-Ambient

* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

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