

N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY

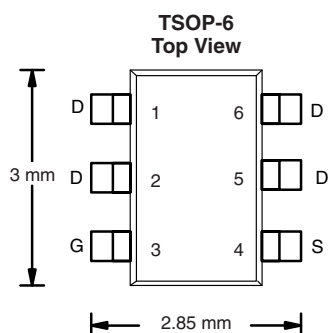
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^a	Q _g (Typ.)
30	0.050 at V _{GS} = 10 V	4.2	2.6
	0.079 at V _{GS} = 4.5 V	3.0	

FEATURES

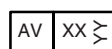
- TrenchFET[®] Power MOSFET
- 100 % R_g Tested

APPLICATIONS

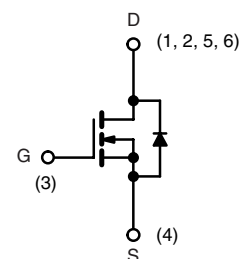
- Load Switch
- Notebook PC


RoHS
COMPLIANT


Marking Code


Lot Traceability
and Date Code

Part # Code



N-Channel MOSFET

Ordering Information: Si3454CDV-T1-E3 (Lead (Pb)-free)

ABSOLUTE MAXIMUM RATINGS T_A = 25 °C, unless otherwise noted

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _{GS}	± 20	
Continuous Drain Current (T _J = 150 °C)	I _D	T _C = 25 °C	A
		T _C = 70 °C	
		T _A = 25 °C	
		T _A = 70 °C	
Pulsed Drain Current	I _{DM}	20	
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C	A
		T _A = 25 °C	
Maximum Power Dissipation	P _D	T _C = 25 °C	W
		T _C = 70 °C	
		T _A = 25 °C	
		T _A = 70 °C	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{b, d}	R _{thJA}	80	100	°C/W
Maximum Junction-to-Foot (Drain)	R _{thJF}	70	85	

Notes:

- Based on T_C = 25 °C.
- Surface Mounted on 1" x 1" FR4 board.
- t = 5 s.
- Maximum under Steady State conditions is 145 °C/W.

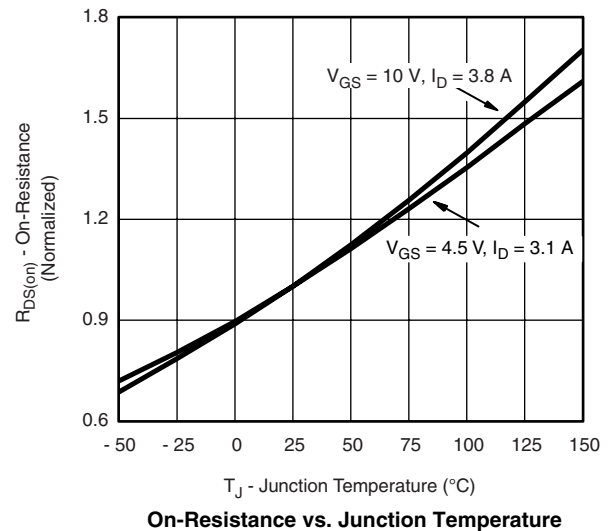
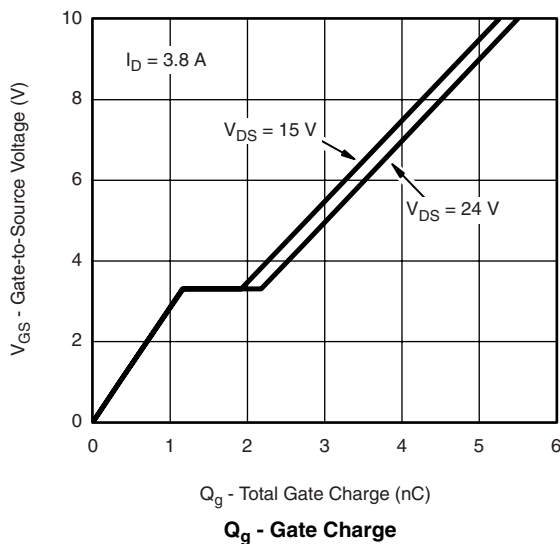
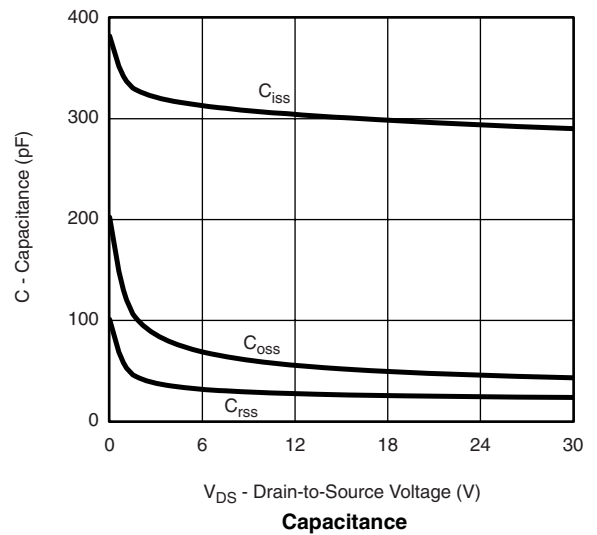
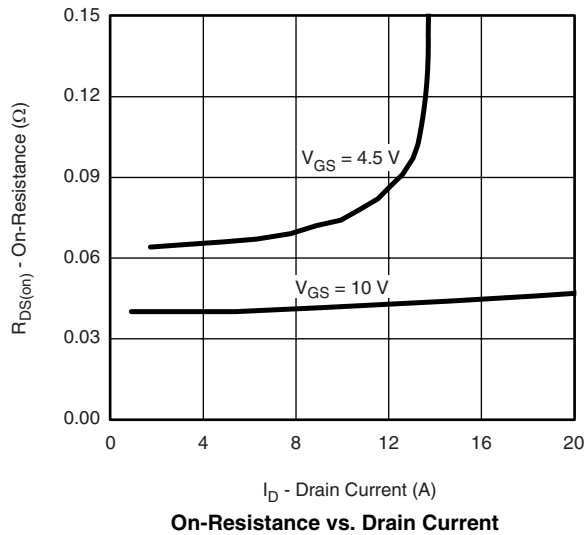
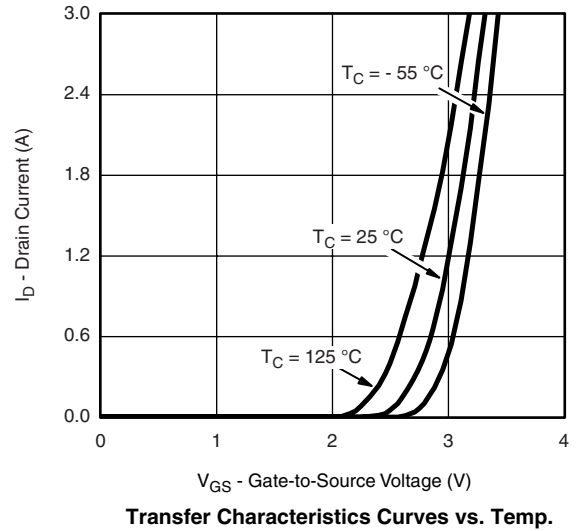
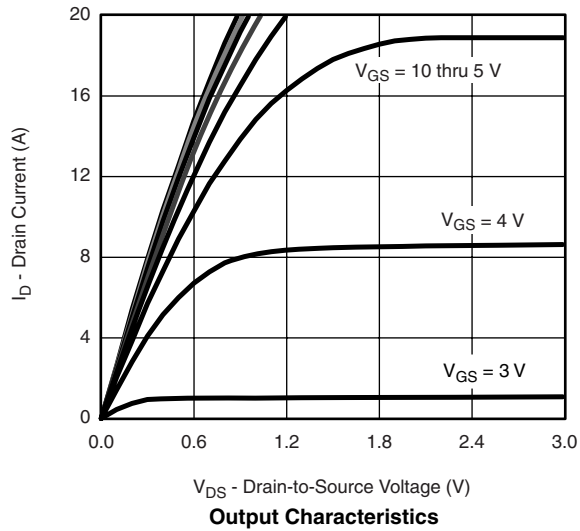
SPECIFICATIONS T _J = 25 °C, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	30			V
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	I _D = 250 μA		27.5		mV/°C
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J			- 5.5		
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	1		3	V
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V			1	μA
		V _{DS} = 30 V, V _{GS} = 0 V, T _J = 85 °C			10	
On-State Drain Current ^a	I _{D(on)}	V _{DS} = ≥ 5 V, V _{GS} = 10 V	30			A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 3.8 A		0.041	0.050	Ω
		V _{GS} = 4.5 V, I _D = 3.1 A		0.066	0.079	
Forward Transconductance	g _{fs}	V _{DS} = 15 V, I _D = 3.8 A		8		S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{DS} = 15 V, V _{GS} = 0 V, f = 1 MHz		305		pF
Output Capacitance	C _{oss}			52		
Reverse Transfer Capacitance	C _{rss}			27		
Total Gate Charge	Q _g	V _{DS} = 15 V, V _{GS} = 10 V, I _D = 3.8 A		5.3	10.6	nC
Gate-Source Charge	Q _{gs}	V _{DS} = 15 V, V _{GS} = 4.5 V, I _D = 3.8 A		2.6	5.2	
Gate-Drain Charge	Q _{gd}			1.2		
Gate Resistance	R _g			0.8		
Gate Resistance	R _g	f = 1 MHz	0.44	2.2	4.4	Ω
Turn-On Delay Time	t _{d(on)}	V _{DD} = 15 V, R _L = 4.8 Ω I _D ≅ 3.1 A, V _{GEN} = 10 V, R _g = 1 Ω		4	8	ns
Rise Time	t _r			8	16	
Turn-Off DelayTime	t _{d(off)}			11	18	
Fall Time	t _f			7	14	
Turn-On Delay Time	t _{d(on)}	V _{DD} = 15 V, R _L = 4.8 Ω I _D ≅ 3.1 A, V _{GEN} = 4.5 V, R _g = 1 Ω		15	20	
Rise Time	t _r			12	18	
Turn-Off DelayTime	t _{d(off)}			8	16	
Fall Time	t _f			9	18	
Drain-Source Body Diode Characteristics						
Continous Source-Drain Diode Current	I _S	T _C = 25 °C			1.25	A
Pulse Diode Forward Current ^a	I _{SM}				20	
Body Diode Voltage	V _{SD}	I _S = 3.1 A		0.8	1.2	V
Body Diode Reverse Recovery Time	t _{rr}	I _F = 3.1 A, dI/dt = 100 A/μs		11.5	17.8	ns
Body Diode Reverse Recovery Charge	Q _{rr}			5	10	nC
Reverse Recovery Fall Time	t _a			7.6		ns
Reverse Recovery Rise Time	t _b			3.9		

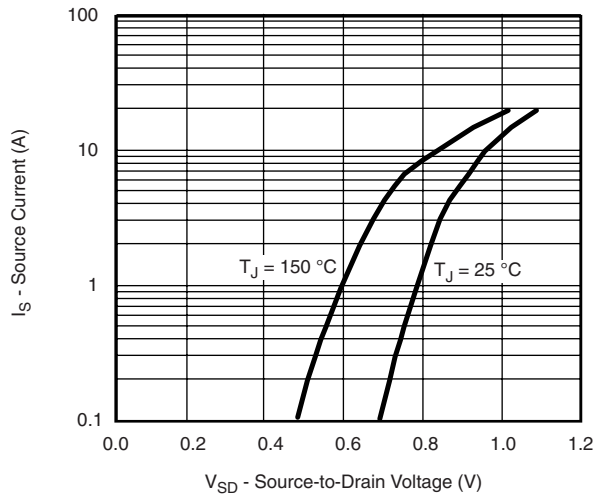
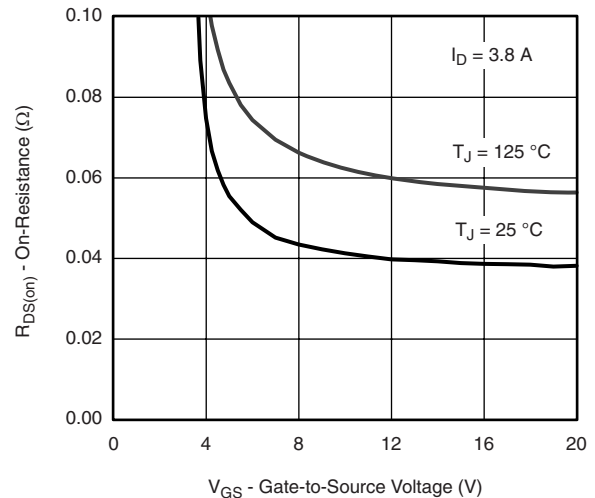
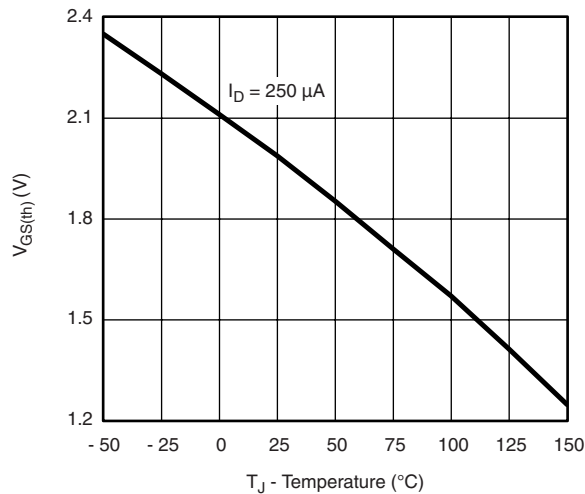
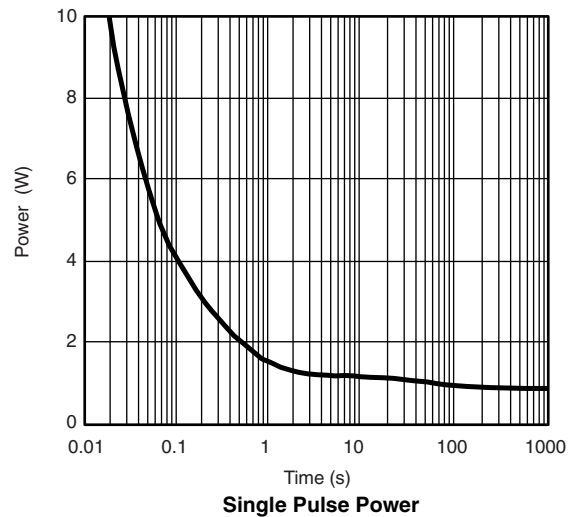
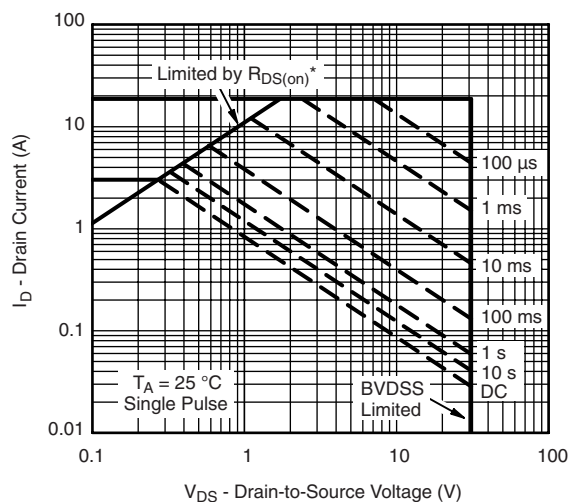
Notes:

a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.

b. Guaranteed by design, not subject to production testing.

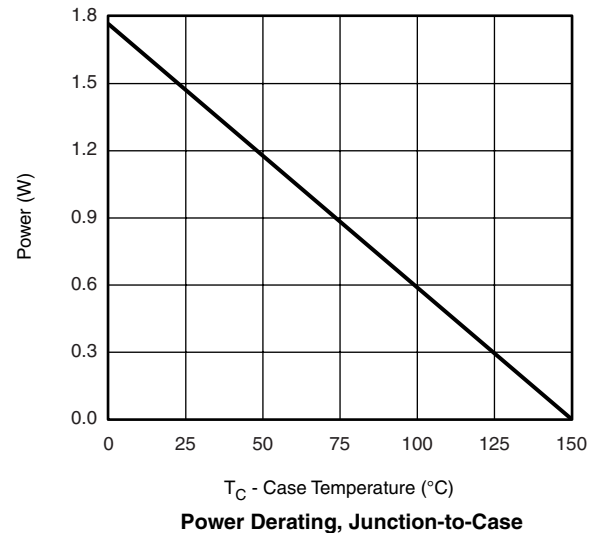
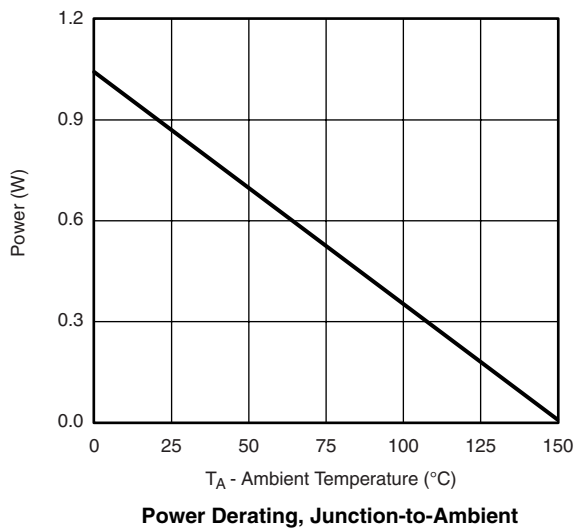
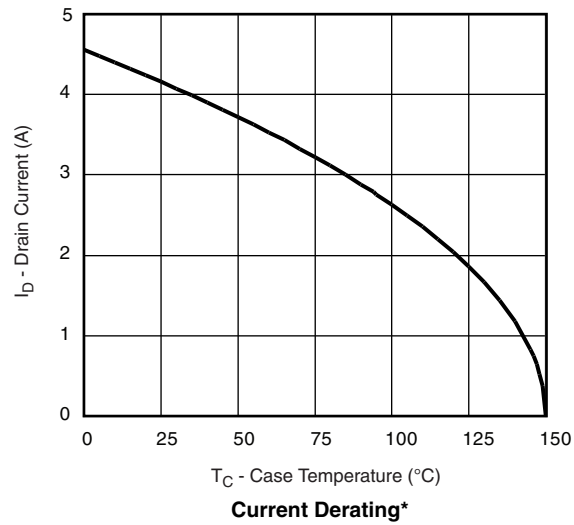
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**TYPICAL CHARACTERISTICS** $T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted

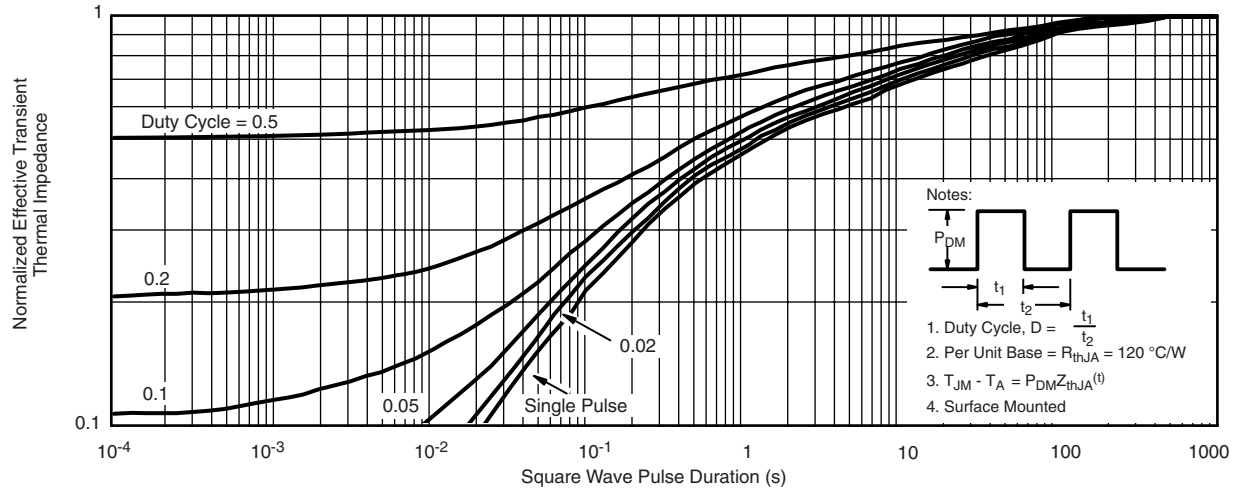
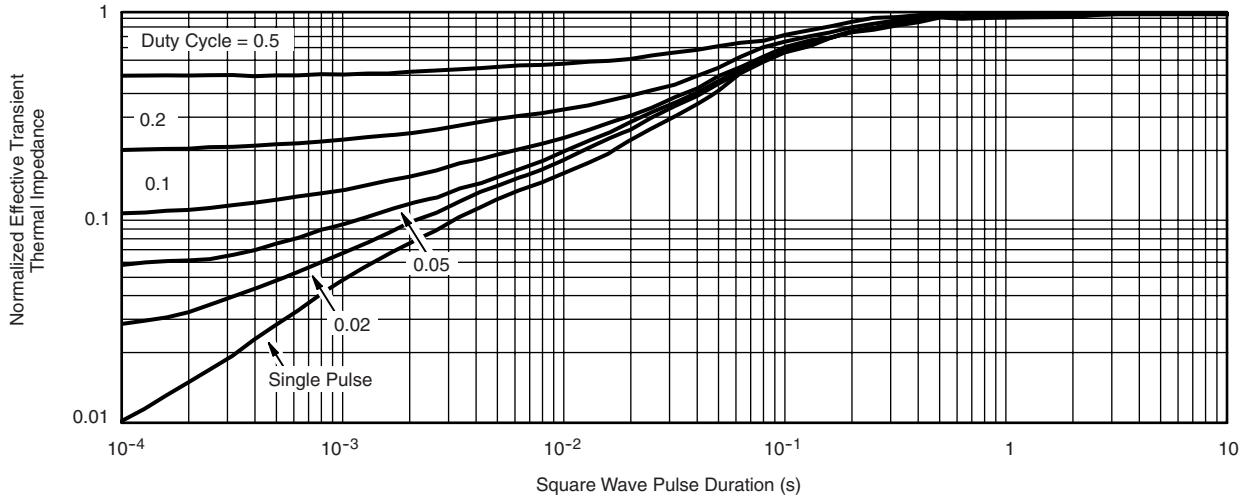
TYPICAL CHARACTERISTICS $T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted**Source-Drain Diode Forward Voltage****On-Resistance vs. Gate-to-Source Voltage****Threshold Voltage****Single Pulse Power*** $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified**Safe Operating Area, Junction-to-Ambient**



TYPICAL CHARACTERISTICS $T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted



* The power dissipation P_D is based on $T_{J(max)} = 150\text{ }^{\circ}\text{C}$, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

TYPICAL CHARACTERISTICS $T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted

Normalized Thermal Transient Impedance, Junction-to-Ambient

Normalized Thermal Transient Impedance, Junction-to-Foot

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