Preferred Device

Dual Series Schottky Barrier Diodes

These Schottky barrier diodes are designed for high speed switching applications, circuit protection, and voltage clamping. Extremely low forward voltage reduces conduction loss. Miniature surface mount package is excellent for hand-held and portable applications where space is limited.

- Extremely Fast Switching Speed
- Low Forward Voltage -0.35 V (Typ) @ $I_F = 10 \text{ mA}$
- This is a Pb-Free Device

MAXIMUM RATINGS (T_{.1} = 125°C unless otherwise noted)

| Rating | Symbol | Value | Unit |
|---|------------------|-------------|-------------|
| Reverse Voltage | V_R | 30 | Volts |
| Forward Power Dissipation @ T _A = 25°C Derate above 25°C | P _F | 240 1.9 | mW mW/°C |
| Forward Current (DC) | IF | 200 Max | mA |
| Junction Temperature | TJ | 125 Max | °C |
| Storage Temperature Range | T _{stg} | -55 to +150 | °C |
| Thermal Resistance Junction–to–Ambient (Note 1) | $R_{\theta JA}$ | 525 | °C/W |

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

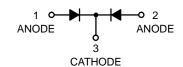
1. FR-5 board with minimum mounting pad.



ON Semiconductor®

http://onsemi.com

30 V DUAL COMMON CATHODE SCHOTTKY BARRIER DIODES



MARKING DIAGRAM



SOT-723 CASE 631AA STYLE 3



5C = Specific Device Code D = Date Code

ORDERING INFORMATION

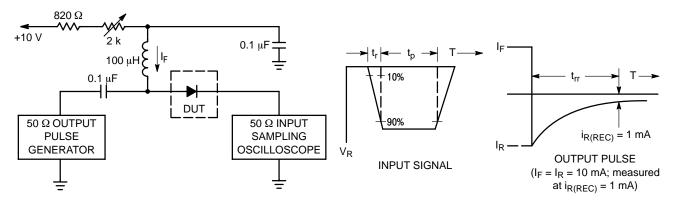
| Device | Package | Shipping [†] |
|-------------|----------------------|-----------------------|
| NSR30CM3T5G | SOT-723 (Pb-Free) | 8000/Tape & Reel |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Preferred devices are recommended choices for future use and best overall value.

$\textbf{ELECTRICAL CHARACTERISTICS} \; (T_{A} = 25^{\circ}\text{C unless otherwise noted}) \; (\text{EACH DIODE})$

| Characteristic | Symbol | Min | Тур | Max | Unit |
|--|--------------------|-----|------|------|------|
| Reverse Breakdown Voltage (I _R = 10 μA) | V _{(BR)R} | 30 | - | - | V |
| Total Capacitance (V _R = 1.0 V, f = 1.0 MHz) | | - | 7.6 | 10 | pF |
| Reverse Leakage (V _R = 25 V) | | - | 0.5 | 2.0 | μΑ |
| Forward Voltage (I _F = 0.1 mA) | | - | 0.22 | 0.24 | V |
| Forward Voltage (I _F = 30 mA) | V _F | - | 0.41 | 0.5 | V |
| Forward Voltage (I _F = 100 mA) | V _F | - | 0.52 | 0.8 | V |
| Reverse Recovery Time $(I_F = I_R = 10 \text{ mA}, I_{R(REC)} = 1.0 \text{ mA}, Figure 1)$ | t _{rr} | - | - | 5.0 | ns |
| Forward Voltage (I _F = 1.0 mA) | V _F | - | 0.29 | 0.32 | V |
| Forward Voltage (I _F = 10 mA) | V _F | - | 0.35 | 0.40 | V |
| Forward Current (DC) | I _F | - | - | 200 | mA |
| Repetitive Peak Forward Current | I _{FRM} | _ | - | 300 | mA |
| Non-Repetitive Peak Forward Current (t < 1.0 s) | I _{FSM} | _ | - | 600 | mA |



Notes: 1. A 2.0 k Ω variable resistor adjusted for a Forward Current (I_F) of 10 mA.

- 2. Input pulse is adjusted so $I_{R(peak)}$ is equal to 10 mA.
- 3. t_p » t_{rr}

Figure 1. Recovery Time Equivalent Test Circuit

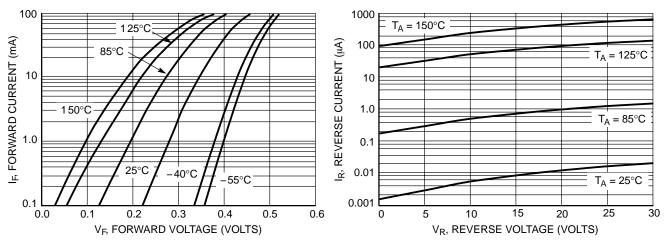


Figure 2. Forward Voltage

Figure 3. Leakage Current

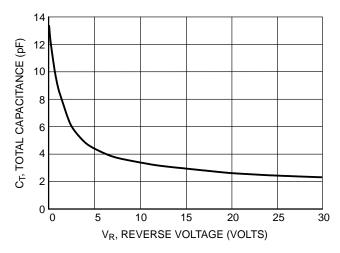
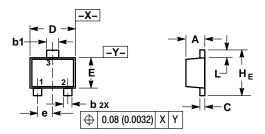


Figure 4. Total Capacitance

PACKAGE DIMENSIONS

SOT-723 CASE 631AA-01 ISSUE B

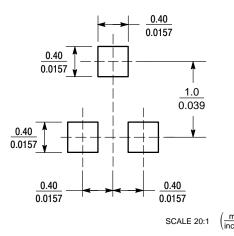


- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI
 - Y14.5M, 1982.
 CONTROLLING DIMENSION: MILLIMETERS.
- MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
 DIMENSIONS D AND E DO NOT INCLUDE MOLD
- FLASH, PROTRUSIONS OR GATE BURRS.

| | MILLIMETERS | | | INCHES | | | |
|-----|-------------|------|------|--------|-----------|--------|--|
| DIM | MIN | NOM | MAX | MIN | NOM | MAX | |
| Α | 0.45 | 0.50 | 0.55 | 0.018 | 0.020 | 0.022 | |
| b | 0.15 | 0.21 | 0.27 | 0.0059 | 0.0083 | 0.0106 | |
| b1 | 0.25 | 0.31 | 0.37 | 0.010 | 0.012 | 0.015 | |
| С | 0.07 | 0.12 | 0.17 | 0.0028 | 0.0047 | 0.0067 | |
| D | 1.15 | 1.20 | 1.25 | 0.045 | 0.047 | 0.049 | |
| E | 0.75 | 0.80 | 0.85 | 0.03 | 0.032 | 0.034 | |
| е | 0.40 BSC | | | C | 0.016 BSC | | |
| ΗE | 1.15 | 1.20 | 1.25 | 0.045 | 0.047 | 0.049 | |
| L | 0.15 | 0.20 | 0.25 | 0.0059 | 0.0079 | 0.0098 | |

STYLE 3: PIN 1. ANODE 2. ANODE 3. CATHODE

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and was are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its partnif rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 61312, Phoenix, Arizona 85082-1312 USA Phone: 480-829-7710 or 800-344-3860 Toll Free USA/Canada Fax: 480-829-7709 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free

Japan: ON Semiconductor, Japan Customer Focus Center 2-9-1 Kamimeguro, Meguro-ku, Tokyo, Japan 153-0051 Phone: 81-3-5773-3850

ON Semiconductor Website: http://onsemi.com

Order Literature: http://www.onsemi.com/litorder

For additional information, please contact your local Sales Representative.