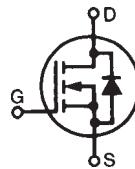


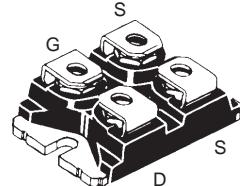
**Polar™ Power MOSFET**  
**HiPerFET™**
**IXFN170N30P**

N-Channel Enhancement Mode  
Avalanche Rated  
Fast Intrinsic Diode



<b>V<sub>DSS</sub></b>	<b>= 300V</b>
<b>I<sub>D25</sub></b>	<b>= 138A</b>
<b>R<sub>DS(on)</sub></b>	<b>≤ 18mΩ</b>
<b>t<sub>rr</sub></b>	<b>≤ 200ns</b>

**miniBLOC, SOT-227 B**  
 **E153432**



G = Gate                      D = Drain  
S = Source

Either Source terminal at miniBLOC can be used as Main or Kelvin Source

Symbol	Test Conditions	Maximum Ratings	
V <sub>DSS</sub>	T <sub>J</sub> = 25°C to 150°C	300	V
V <sub>DGR</sub>	T <sub>J</sub> = 25°C to 150°C, R <sub>GS</sub> = 1MΩ	300	V
V <sub>GSS</sub>	Continuous	±20	V
V <sub>GSM</sub>	Transient	± 30	V
I <sub>D25</sub>	T <sub>C</sub> = 25°C	138	A
I <sub>LRMS</sub>	External lead current limit	100	A
I <sub>DM</sub>	T <sub>C</sub> = 25°C, pulse width limited by T <sub>JM</sub>	500	A
I <sub>A</sub>	T <sub>C</sub> = 25°C	85	A
E <sub>AS</sub>	T <sub>C</sub> = 25°C	5	J
dV/dt	I <sub>S</sub> ≤ I <sub>DM</sub> , V <sub>DD</sub> ≤ V <sub>DSS</sub> , T <sub>J</sub> ≤ 150°C	20	V/ns
P <sub>D</sub>	T <sub>C</sub> = 25°C	890	W
T <sub>J</sub>		-55 ... +150	°C
T <sub>JM</sub>		150	°C
T <sub>stg</sub>		-55 ... +150	°C
T <sub>L</sub>	1.6mm (0.062 in.) from case for 10s	300	°C
V <sub>ISOL</sub>	50/60 Hz, RMS I <sub>ISOL</sub> ≤ 1mA	t = 1min t = 1s	2500 3000
M <sub>d</sub>	Mounting torque Terminal connection torque	1.5/13 1.3/11.5	Nm/lb.in. Nm/lb.in.
Weight		30	g

Symbol	Test Conditions (T <sub>J</sub> = 25°C, unless otherwise specified)	Characteristic Values		
		Min.	Typ.	Max.
BV <sub>DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 3mA	300		V
V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 1mA	2.5	4.5	V
I <sub>GSS</sub>	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V		±200	nA
I <sub>DSS</sub>	V <sub>DS</sub> = V <sub>DSS</sub> V <sub>GS</sub> = 0V		25 1.5	μA mA
R <sub>DS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 85A, Note 1		18	mΩ

**Features**

- Fast intrinsic diode
- Avalanche Rated
- Unclamped Inductive Switching (UIS) rated
- Very low R<sub>th</sub> results high power dissipation
- Low R<sub>DS(ON)</sub> and Q<sub>G</sub>
- Low package inductance

**Advantages**

- Low gate charge results in simple drive requirement
- Improved Gate, Avalanche and dynamic dv/dt ruggedness
- High power density

**Applications**

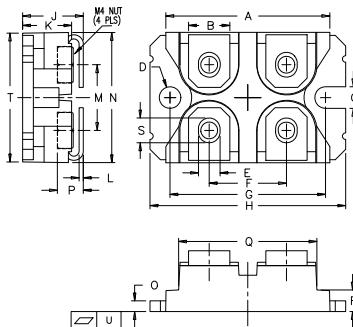
- DC-DC converters
- Battery chargers
- Switched-mode and resonant-mode power supplies
- DC choppers
- AC and DC motor control
- Uninterrupted power supplies
- High speed power switching applications

Symbol	Test Conditions (T <sub>J</sub> = 25°C, unless otherwise specified)	Characteristic Values		
		Min.	Typ.	Max.
<b>g<sub>fs</sub></b>	V <sub>DS</sub> = 10V, I <sub>D</sub> = 60A, Note 1	57	95	S
<b>C<sub>iss</sub></b> <b>C<sub>oss</sub></b> <b>C<sub>rss</sub></b>	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 25V, f = 1MHz	20	nF	
		2450	pF	
		27	pF	
<b>t<sub>d(on)</sub></b> <b>t<sub>r</sub></b> <b>t<sub>d(off)</sub></b> <b>t<sub>f</sub></b>	<b>Resistive Switching Times</b> V <sub>GS</sub> = 10V, V <sub>DS</sub> = 0.5 • V <sub>DSS</sub> , I <sub>D</sub> = 85A R <sub>G</sub> = 1Ω (External)	41	ns	
		29	ns	
		79	ns	
		16	ns	
<b>Q<sub>g(on)</sub></b> <b>Q<sub>gs</sub></b> <b>Q<sub>gd</sub></b>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 0.5 • V <sub>DSS</sub> , I <sub>D</sub> = 85A	258	nC	
		82	nC	
		78	nC	
<b>R<sub>thJC</sub></b>			0.14	°C/W
<b>R<sub>thCS</sub></b>		0.05		°C/W

**Source-Drain Diode****Characteristic Values**(T<sub>J</sub> = 25°C, unless otherwise specified)

Symbol	Test Conditions	Characteristic Values		
		Min.	Typ.	Max.
<b>I<sub>s</sub></b>	V <sub>GS</sub> = 0V		170	A
<b>I<sub>SM</sub></b>	Repetitive, pulse width limited by T <sub>JM</sub>		500	A
<b>V<sub>SD</sub></b>	I <sub>F</sub> = 85A, V <sub>GS</sub> = 0V, Note 1		1.3	V
<b>t<sub>rr</sub></b> <b>Q<sub>RM</sub></b> <b>I<sub>RM</sub></b>	I <sub>F</sub> = 85A, -di/dt = 150A/μs V <sub>R</sub> = 100V	1.85	200	ns
		21		μC
				A

Note 1: Pulse test, t ≤ 300μs; duty cycle, d ≤ 2%.

**SOT-227B Outline**

SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	1.240	1.255	31.50	31.88
B	.307	.323	7.80	8.20
C	.161	.169	4.09	4.29
D	.161	.169	4.09	4.29
E	.161	.169	4.09	4.29
F	.587	.595	14.91	15.11
G	1.186	1.193	30.12	30.30
H	1.496	1.505	38.00	38.23
J	.460	.481	11.68	12.22
K	.351	.378	8.92	9.60
L	.030	.033	0.76	0.84
M	.496	.506	12.60	12.85
N	.990	1.001	25.15	25.42
O	.078	.084	1.98	2.13
P	.195	.235	4.95	5.97
Q	1.045	1.059	26.54	26.90
R	.155	.174	3.94	4.42
S	.186	.191	4.72	4.85
T	.968	.987	24.59	25.07
U	-.002	.004	-0.05	0.1

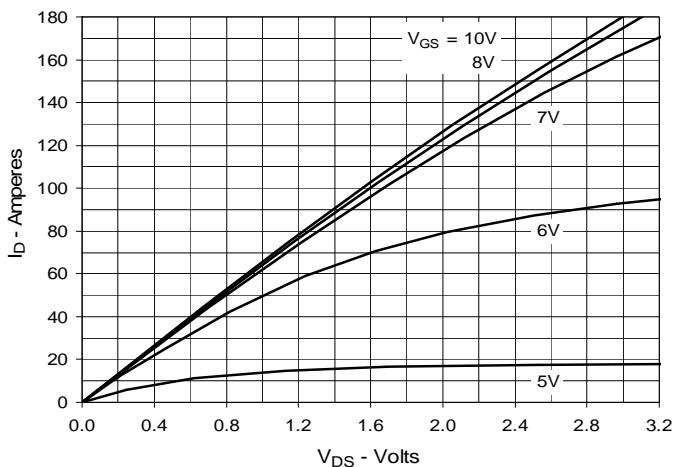
**PRELIMINARY TECHNICAL INFORMATION**

The product presented herein is under development. The Technical Specifications offered are derived from data gathered during objective characterizations of preliminary engineering lots; but also may yet contain some information supplied during a pre-production design evaluation. IXYS reserves the right to change limits, test conditions, and dimensions without notice.

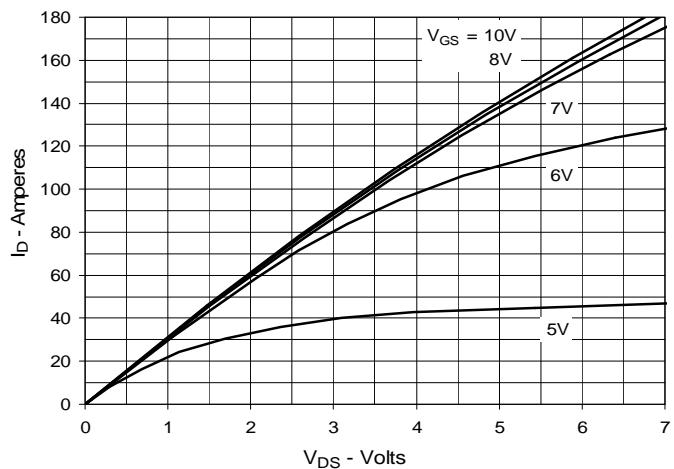
IXYS reserves the right to change limits, test conditions, and dimensions.

IXYS MOSFETs and IGBTs are covered by one or more of the following U.S. patents: 4,835,592 4,931,844 5,049,961 5,237,481 6,162,665 6,404,065 B1 6,683,344 6,727,585 7,005,734 B2 7,157,338B2 5,017,508 5,063,307 5,381,025 6,259,123 B1 6,534,343 6,710,405 B2 6,759,692 7,063,975 B2 4,881,106 5,034,796 5,187,117 5,486,715 6,306,728 B1 6,583,505 6,710,463 6,771,478 B2 7,071,537

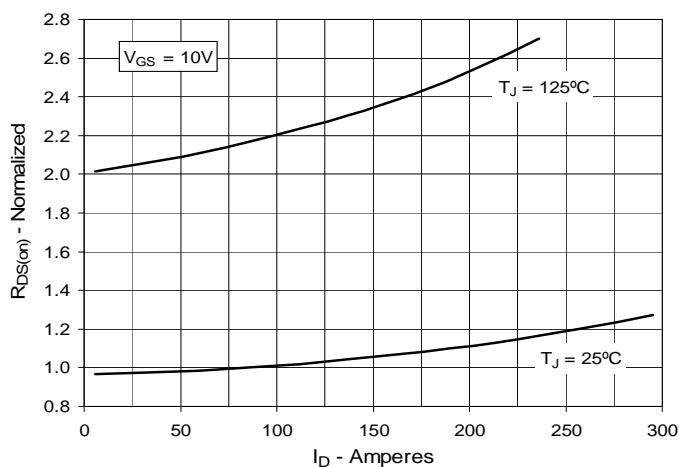
**Fig. 1. Output Characteristics  
@ 25°C**



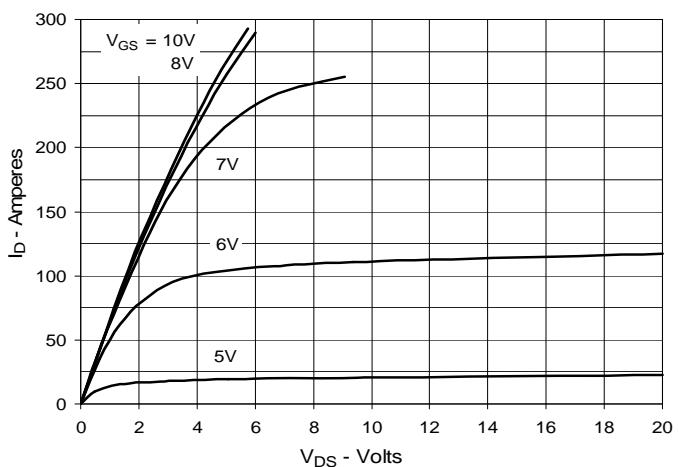
**Fig. 3. Output Characteristics  
@ 125°C**



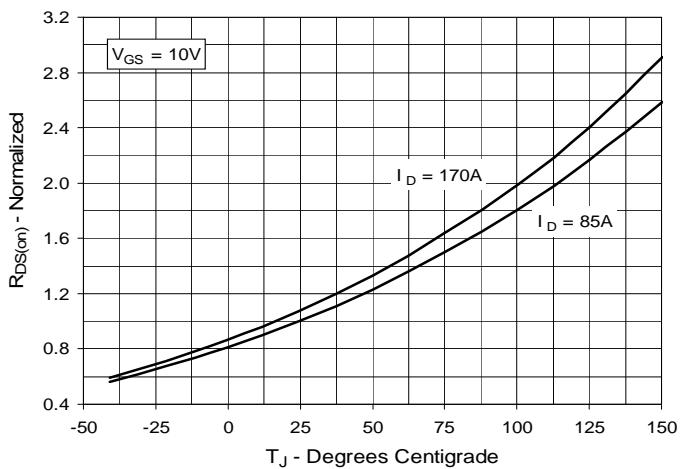
**Fig. 5.  $R_{DS(on)}$  Normalized to  $I_D = 85A$  Value  
vs. Drain Current**



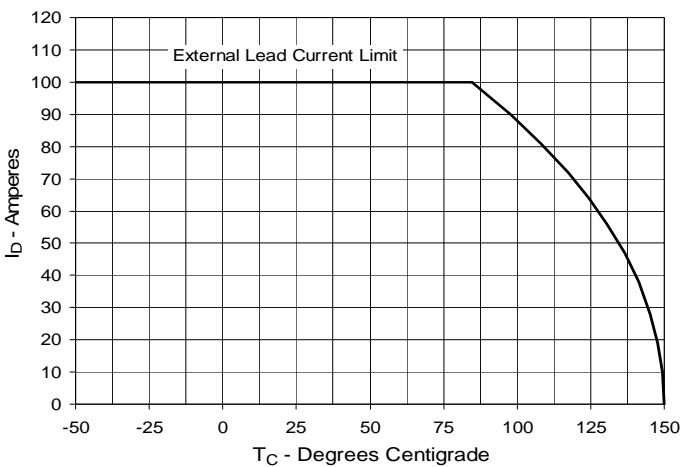
**Fig. 2. Extended Output Characteristics  
@ 25°C**

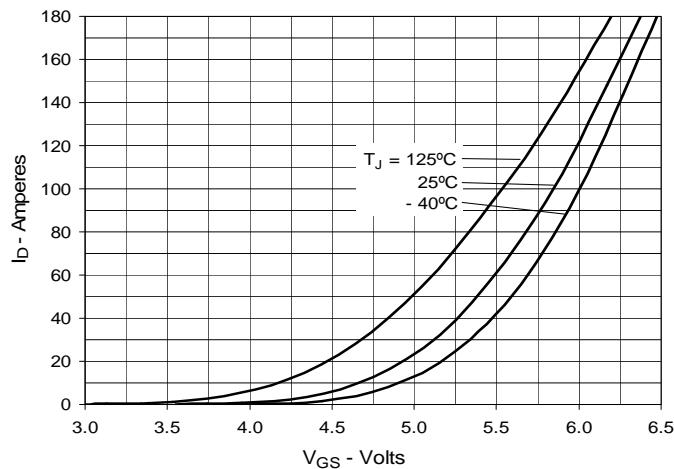
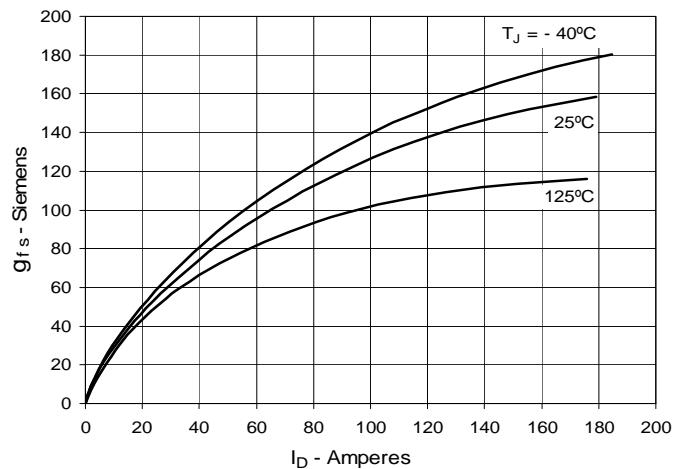
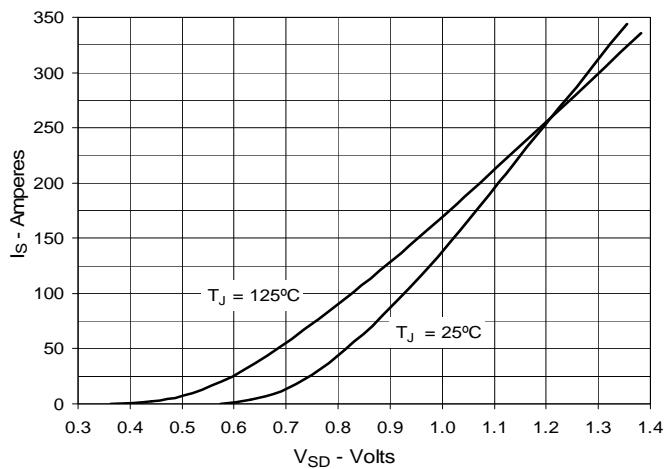
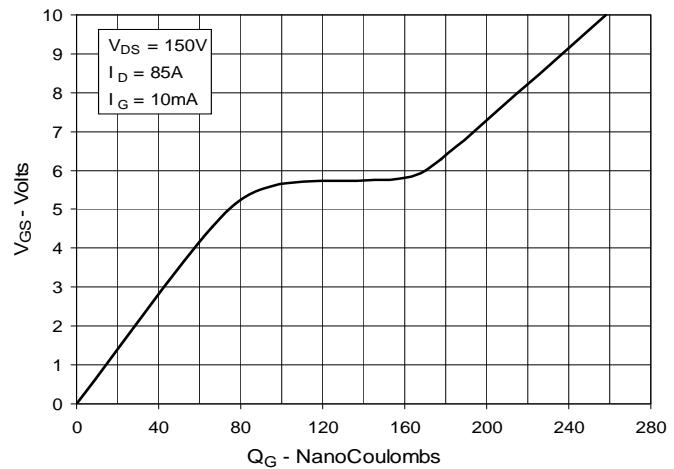
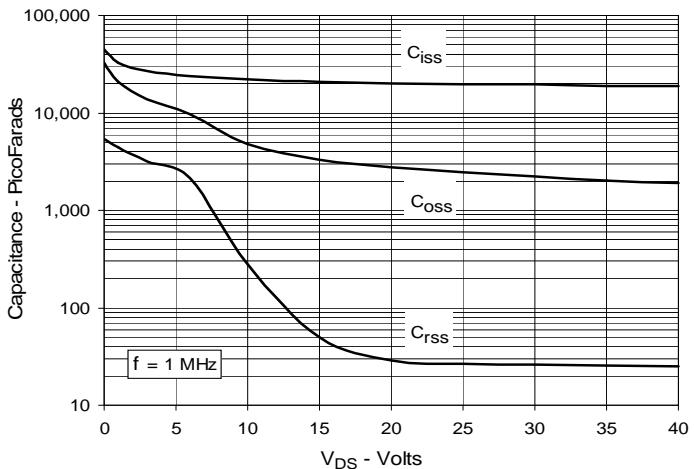
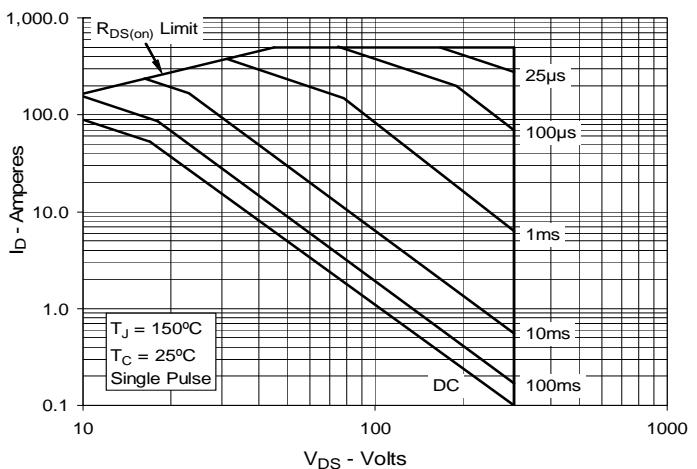


**Fig. 4.  $R_{DS(on)}$  Normalized to  $I_D = 85A$  Value  
vs. Junction Temperature**



**Fig. 6. Maximum Drain Current vs.  
Case Temperature**



**Fig. 7. Input Admittance****Fig. 8. Transconductance****Fig. 9. Forward Voltage Drop of Intrinsic Diode****Fig. 10. Gate Charge****Fig. 11. Capacitance****Fig. 12. Forward-Bias Safe Operating Area**

IXYS reserves the right to change limits, test conditions, and dimensions.

**Fig. 12. Maximum Transient Thermal Impedance**