PD - 95059

International **ICR** Rectifier

IRFPC60PbF

 $V_{DSS} = 600V$

 $R_{DS(on)} = 0.40\Omega$

HEXFET[®] Power MOSFET

- Dynamic dv/dt Rating
- Repetitive Avalanche Rated
- Isolated Central Mounting Hole
- Fast Switching
- · Ease of Paralleling
- Simple Drive Requirements

Absolute Maximum Ratings

Lead-Free

Description

Third Generation HEXFETs from International Rectifier provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-247 package is preferred for commercial-industrial applications where higher power levels preclude the use of TO-220 devices. The TO-247 is similar but superior to the earlier TO-218 package because of its isolated mounting hole. It also provides greater creepage distance between pins to meet the requirements of most safety specifications.



	Parameter	
Ip @ Tc = 25°C	Continuous Drain Current, VGS @	10

	Parameter	Max.	Units	
I _D @ T _C = 25°C	Continuous Drain Current, VGS @ 10 V 16			
$I_D @ T_C = 100^{\circ}C$	Continuous Drain Current, VGS @ 10 V	10	A	
IDM	Pulsed Drain Current ①	64		
P _D @ T _C = 25°C	@ $T_C = 25^{\circ}C$ Continuous Drain Current, V_{GS} @ 10 V @ $T_C = 100^{\circ}C$ Continuous Drain Current, V_{GS} @ 10 V M Pulsed Drain Current ① D @ $T_C = 25^{\circ}C$ Power Dissipation Linear Derating Factor Gate-to-Source Voltage As Single Pulse Avalanche Energy ② R Avalanche Current ① AR Repetitive Avalanche Energy ① V/dt Peak Diode Recovery dv/dt ③ J Operating Junction and	280	W	
	Linear Derating Factor	2.2	W/°C	
VGS	Gate-to-Source Voltage	±20	V	
EAS	Single Pulse Avalanche Energy 2	1000	mJ	
IAR	Avalanche Current ①	16	A	
EAR	Repetitive Avalanche Energy ①	28	mJ	
dv/dt	Peak Diode Recovery dv/dt ③	3.0	V/ns	
TJ TSTG		-55 to +150	°C	
	Soldering Temperature, for 10 seconds	300 (1.6mm from case)		
	Mounting Torque, 6-32 or M3 screw	10 lbf•in (1.1 N•m)		

Thermal Resistance

	Parameter	Min.	Тур.	Max.	Units
Reuc	Junction-to-Case		—	0.45	
Recs	Case-to-Sink, Flat, Greased Surface	1 -	0.24	-	°C/W
Reja	Junction-to-Ambient		-	40	

	Parameter	Min.	Тур.	Max.	Units	Test Conditions	
V(BR)DSS	Drain-to-Source Breakdown Voltage	600	-	-	V	V _{GS} =0V, I _D = 250µA	
ΔV(BR)DSS/ΔTJ	Breakdown Voltage Temp. Coefficient	-	0.83		V/°C	Reference to 25°C, Ip= 1mA	
RDS(on)	Static Drain-to-Source On-Resistance	-	-	0.40	Ω	V _{GS} =10V, I _D =9.6A ④	
V _{GS(th)}	Gate Threshold Voltage	2.0		4.0	V	VDS=VGS, ID= 250µA	
g ts	Forward Transconductance	13	-	-	S	VDS=50V, ID=9.6A @	
loss	Drain-to-Source Leakage Current	-	-	100	μА	V _{DS} =600V, V _{GS} =0V	
			-	500		VDS=480V, VGS=0V, TJ=125°C	
	Gate-to-Source Forward Leakage	-	-	100	nA	V _{GS} =20V	
lgss	Gate-to-Source Reverse Leakage	-	-	-100		V _{GS} =-20V	
Qg	Total Gate Charge		-	210		I _D =16A	
Qgs	Gate-to-Source Charge		-	26	nC	V _{DS} =360V	
Qgd	Gate-to-Drain ("Miller") Charge	-	-	110		V _{GS} =10V See Fig. 6 and 13 @	
t _{d(on)}	Turn-On Delay Time	-	19	-		V _{DD} =300V	
tr	Rise Time	-	54	-	ns	ID=16A	
td(off)	Turn-Off Delay Time	-	110	-	113	R _G =4.5Ω	
te	Fall Time	-	56	-		$R_D=18\Omega$ See Figure 10 @	
LD	Internal Drain Inductance	-	5.0	-	nH	Between lead, 6 mm (0.25in.)	
Ls	Internal Source Inductance	-	13	-	101	from package and center of die contact	
Ciss	Input Capacitance	-	3900	-		V _{GS} =0V	
Coss	Output Capacitance	-	440	-	pF	V _{DS} =25V	
Crss	Reverse Transfer Capacitance	-	98			f=1.0MHz See Figure 5	

Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

Source-Drain Ratings and Characteristics

	Parameter	Min.	Тур.	Max.	Units	Test Conditions	
ls	Continuous Source Current (Body Diode)	-	-	16		MOSFET symbol showing the	
ISM	Pulsed Source Current (Body Diode) ①	-	-	64	A	integral reverse p-n junction diode.	
VSD	Diode Forward Voltage	- 1	-	1.8	V	T_=25°C, Is=16A, VGS=0V @	
trr	Reverse Recovery Time	-	610	920	ns	T_=25°C, IF=16A	
Qrr	Reverse Recovery Charge	- 1	6.6	9.9	μC	di/dt=100A/µs ④	
ton	Forward Turn-On Time	Intrinsic turn-on time is neglegible (turn-on is dominated by Ls+Lp					

Notes:

- Repetitive rating; pulse width limited by max. junction temperature (See Figure 11)
- ③ I_{SD}≤16A, di/dt≤140A/ μ s, V_{DD}≤V(BR)DSS, T_J≤150°C
- ② V_{DD}=50V, starting T_J=25°C, L=7.2mH Rg=25Ω, I_{AS}=16A (See Figure 12)
- ④ Pulse width \leq 300 µs; duty cycle \leq 2%.

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Vs. Temperature

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Fig 10a. Switching Time Test Circuit



Fig 10b. Switching Time Waveforms



Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

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Fig 12a. Unclamped Inductive Test Circuit



Fig 12b. Unclamped Inductive Waveforms



Fig 13a. Basic Gate Charge Waveform



Fig 12c. Maximum Avalanche Energy Vs. Drain Current





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IRFPC60PbF

Peak Diode Recovery dv/dt Test Circuit



Diode Recovery dv/dt

⁾ Forward Drop

V_{DD}

↑ I_{SD}

*** V_{GS} = 5.0V for Logic Level and 3V Drive Devices **Fig** 14 For N Channel HEXFETS

Body Diode

Ripple ≤ 5%

Re-Applied Voltage

4

Inductor Curent

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TO-247AC Package Outline

Dimensions are shown in millimeters (inches)



TO-247AC Part Marking Information



Data and specifications subject to change without notice.

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