

FDP5690/FDB5690

60V N-Channel PowerTrench™MOSFET

General Description

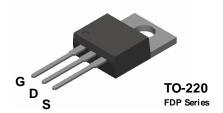
This N-Channel MOSFET has been designed specifically to improve the overall efficiency of DC/DC converters using either synchronous or conventional switching PWM controllers.

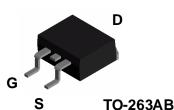
These MOSFETs feature faster switching and lower gate charge than other MOSFETs with comparable $R_{\scriptscriptstyle DS(\rm on)}$ specifications resulting in DC/DC power supply designs with higher overall efficiency.

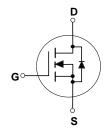
Features

- 32 A, 60 V. $R_{DS(ON)} = 0.027~\Omega$ @ $V_{GS} = 10~V$ $R_{DS(ON)} = 0.032~\Omega$ @ $V_{GS} = 6~V$.
- Critical DC electrical parameters specified at evevated temperature.
- Rugged internal source-drain diode can eliminate the need for an external Zener diode transient suppressor.
- High performance trench technology for extremely low $R_{_{DS/DM}}. \label{eq:RDS}$
- 175°C maximum junction temperature rating.

FDB Series







Absolute Maximum Ratings T_C = 25°C unless otherwise noted

Symbol	Parameter	FDP5690	FDB5690	Units
V _{DSS}	Drain-Source Voltage	60		V
V _{GSS}	Gate-Source Voltage ±20		20	V
I _D	Maximum Drain Current - Continuous	Continuous 32		Α
	- Pulsed	100		
P _D	Total Power Dissipation @ T _C = 25°C	58		W
	Derate above 25°C	0.4		W/°C
T _J , T _{STG}	Operating and Storage Junction Temperature Range	-65 to +175		°C

Thermal Characteristics

R _e JC	Thermal Resistance, Junction-to-Case	2.6	°C/W
R_{eJA}	Thermal Resistance, Junction-to-Ambient	62.5	°C/W

Package Marking and Ordering Information

Device Marking	Device	Reel Size	Tape Width	Quantity
FDB5690	FDB5690	13"	24mm	800
FDP5690	FDP5690	Tube	N/A	45

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Drain-Sc	ource Avalanche Ratings (No	ote1)				
W _{DSS}	Single Pulse Drain-Source Avalanche Energy	$V_{DD} = 30 \text{ V}, I_{D} = 32 \text{A}$			80	mJ
I _{AR}	Maximum Drain-Source Avalanche	Current			32	Α
Off Char	acteristics					
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$	60			V
<u>A</u> BVdss ΔTJ	Breakdown Voltage Temperature Coefficient	$I_D = 250 \mu A$, Referenced to 25°C		61		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 48 \text{ V}, V_{GS} = 0 \text{ V}$			1	μΑ
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 20 V, V _{DS} = 0 V			100	nA
I_{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -20 V, V _{DS} = 0 V			-100	nA
On Char	acteristics (Note 1)					
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$	2	2.4	4	V
<u>A</u> VGS(th) ΔTJ	Gate Threshold Voltage Temperature Coefficient	$I_D = 250 \mu A$, Referenced to 25°C		-6.4		mV/°C
$R_{\text{DS(on)}}$	Static Drain-Source On-Resistance	$V_{GS} = 10 \text{ V}, I_D = 16 \text{ A}, V_{GS} = 10 \text{ V}, I_D = 16 \text{ A}, T_J = 125 ^{\circ}\text{C}$ $V_{GS} = 6 \text{ V}, I_D = 15 \text{ A}$		0.021 0.042 0.024	0.027 0.055 0.032	Ω
I _{D(on)}	On-State Drain Current	$V_{GS} = 10 \text{ V}, V_{DS} = 5 \text{ V}$	50			Α
G FS	Forward Transconductance	$V_{DS} = 5 \text{ V}, I_{D} = 16 \text{ A}$		32		S
Dvnamio	Characteristics					
C _{iss}	Input Capacitance	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$		1120		pF
C _{oss}	Output Capacitance	f = 1.0 MHz		160		pF
C _{rss}	Reverse Transfer Capacitance			80		pF
Switchin	g Characteristics (Note 1)					
t _{d(on)}	Turn-On Delay Time	$V_{DD} = 30 \text{ V}, I_D = 1 \text{ A},$		10	18	ns
t _r	Turn-On Rise Time	$V_{GS} = 10 \text{ V}, R_{GEN} = 6 \Omega$		9	18	ns
t _{d(off)}	Turn-Off Delay Time			24	39	ns
t _f	Turn-Off Fall Time			10	18	ns
Q_g	Total Gate Charge	V _{DS} = 15 V,		23	33	nC
Q_{gs}	Gate-Source Charge	$I_D = 16 \text{ A}, V_{GS} = 10 \text{ V}$		3.9		nC
Q_{gd}	Gate-Drain Charge			6.8		nC
Drain-Sc	ource Diode Characteristics	and Maximum Ratings				
I _S	Maximum Continuous Drain-Source	_			32	Α
V _{SD}	Drain-Source Diode Forward Voltage	$V_{GS} = 0 \text{ V}, I_S = 16 \text{ A}$ (Note 1)		0.92	1.2	V

^{1.} Pulse Test: Pulse Width $\leq 300~\mu s$, Duty Cycle $\leq 2.0\%$

Typical Characteristics

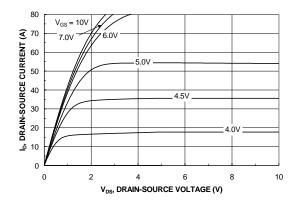


Figure 1. On-Region Characteristics.

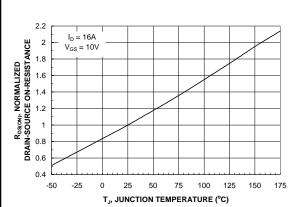


Figure 3. On-Resistance Variation with Temperature.

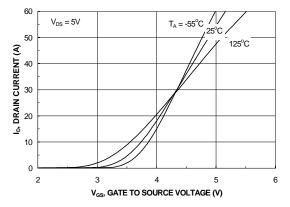


Figure 5. Transfer Characteristics.

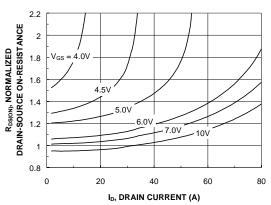


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.

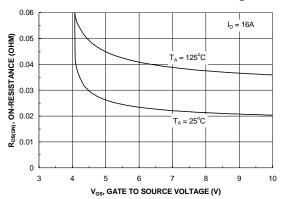


Figure 4. On-Resistance Variation with Gate-to-Source Voltage.

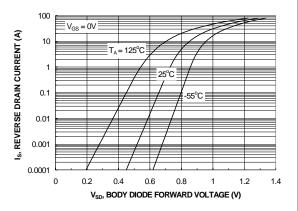
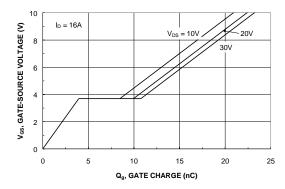


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.





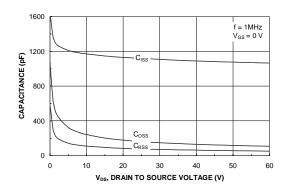


Figure 7. Gate-Charge Characteristics.

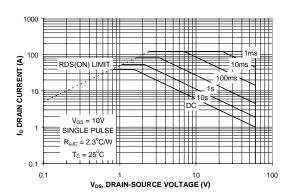


Figure 8. Capacitance Characteristics.

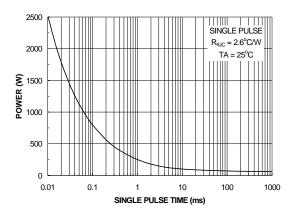
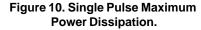


Figure 9. Maximum Safe Operating Area.



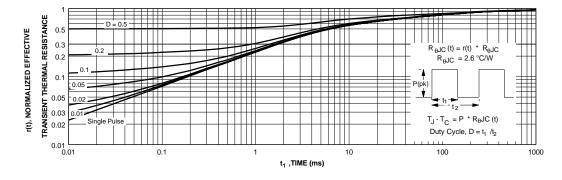


Figure 11. Transient Thermal Response Curve.

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PRODUCT STATUS DEFINITIONS

Definition of Terms

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