



N-CHANNEL ENHANCEMENT MODE MOSFET

Features

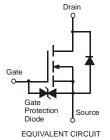
- Low On-Resistance: RDS(ON)
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Lead Free By Design/RoHS Compliant (Note 2)
- ESD Protected Up To 2kV
- "Green" Device (Note 4)

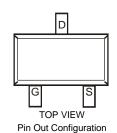
Mechanical Data

- Case: SOT-323
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Finish Matte Tin annealed over Alloy 42 leadframe. Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.006 grams (approximate)

SOT-323







Maximum Ratings @T_A = 25°C unless otherwise specified

Charac	teristic	Symbol	Value	Units	
Drain-Source Voltage		V_{DSS}	60	V	
Gate-Source Voltage		V _{GSS}	±20	V	
Drain Current (Note 1)	Continuous Pulsed (Note 3)		300 800	mA	

Thermal Characteristics @T_A = 25°C unless otherwise specified

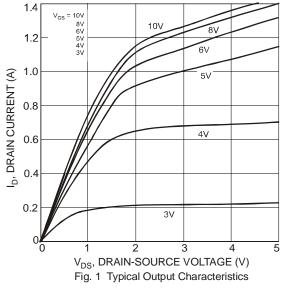
Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 1)	P_{D}	200	mW
Thermal Resistance, Junction to Ambient	$R_{ hetaJA}$	625	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +150	°C

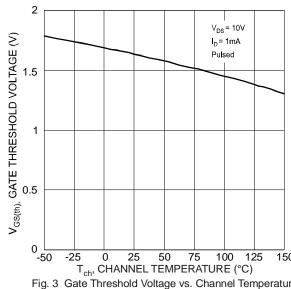
Electrical Characteristics @T_A = 25°C unless otherwise specified

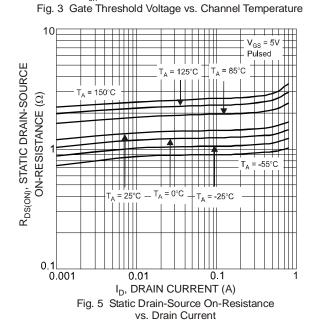
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 5)							
Drain-Source Breakdown Voltage	BV _{DSS}	60	_		V	$V_{GS} = 0V, I_{D} = 10\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}			1.0	μΑ	$V_{DS} = 60V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}		_	±10	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 5)							
Gate Threshold Voltage	V _{GS(th)}	1.0	1.6	2.5	>	$V_{DS} = 10V, I_{D} = 1mA$	
			_	2.0	\cap	$V_{GS} = 10V, I_D = 0.5A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	_	3.0		$V_{GS} = 4.5V, I_D = 0.2A$	
Forward Transfer Admittance	Y _{fs}	80	_	_	ms	$V_{DS} = 10V, I_D = 0.2A$	
DYNAMIC CHARACTERISTICS							
Input Capacitance	C _{iss}			50	рF		
Output Capacitance	Coss			25	pF	$V_{DS} = 25V, V_{GS} = 0V, f = 1.0MHz$	
Reverse Transfer Capacitance	C _{rss}	_		5.0	рF		

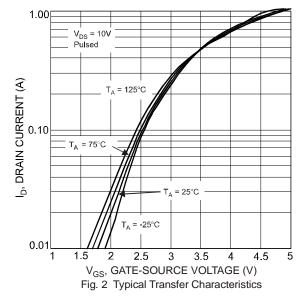
- Notes:
- 1. Device mounted on FR-4 PCB.
- No purposefully added lead.
 Pulse width <10uS. Duty Cycle
- Pulse width ≤10μS, Duty Cycle ≤1%.
 Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
- Short duration pulse test used to minimize self-heating effect.











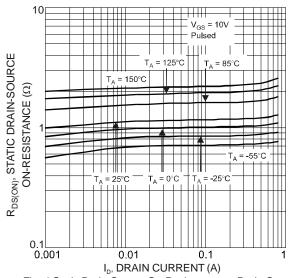


Fig. 4 Static Drain-Source On-Resistance vs. Drain Current

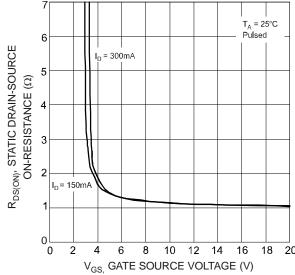
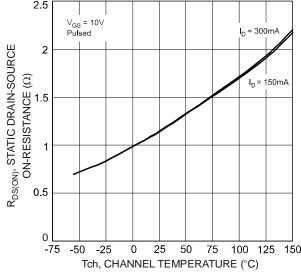


Fig. 6 Static Drain-Source On-Resistance vs. Gate-Source Voltage





Tch, CHANNEL TEMPERATURE (°C)
Fig. 7 Static Drain-Source On-State Resistance
vs. Channel Temperature

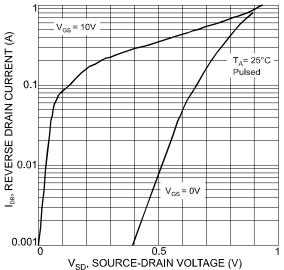


Fig. 9 Reverse Drain Current vs. Source-Drain Voltage

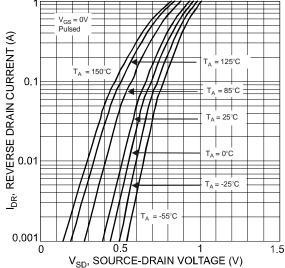


Fig. 8 Reverse Drain Current vs. Source-Drain Voltage

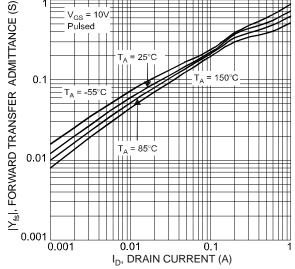


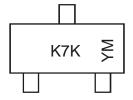
Fig.10 Forward Transfer Admittance vs. Drain Current

Ordering Information (Note 6)

ſ	Part Number	Case	Packaging
	DMN601WK-7	SOT-323	3000/Tape & Reel

Notes: 6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



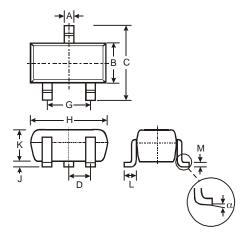
DM = Product Type Marking Code YM = Date Code Marking Y = Year (ex: S = 2005) M = Month (ex: 9 = September)

Date Code Key

Date Code Hoy												
Year	2005		2006	2007		2008	2009)	2010	2011		2012
Code	S		T	U		V	W		Χ	Y		Z
Month	Jan	Feb	Mar	Apr	Mav	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

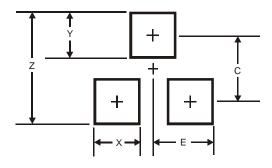


Package Outline Dimensions



SOT-323					
Dim	Min	Max	Тур		
Α	0.25	0.40	0.30		
В	1.15	1.35	1.30		
С	2.00	2.20	2.10		
D	-	-	0.65		
G	1.20	1.40	1.30		
Н	1.80	2.20	2.15		
J	0.0	0.10	0.05		
K	0.90	1.00	1.00		
L	0.25	0.40	0.30		
M	0.10	0.18	0.11		
α	0°	8°	-		
All Dimensions in mm					

Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.8
X	0.7
Υ	0.9
С	1.9
E	1.0



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