

FQP9N25C/FQPF9N25C

250V N-Channel MOSFET

General Description

These N-Channel enhancement mode power field effect transistors are produced using Fairchild's proprietary, planar stripe, DMOS technology.

This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switching DC/DC converters, switch mode power supplies, DC-AC converters for uninterrupted power supplies and motor controls.

Features

- 8.8A, 250V, $R_{DS(on)}$ = 0.43 Ω @V_{GS} = 10 V Low gate charge (typical 26.5 nC)
- Low Crss (typical 45.5 pF)
- · Fast switching
- · 100% avalanche tested
- · Improved dv/dt capability



Absolute Maximum Ratings T_C = 25°C unless otherwise noted

Symbol	Parameter		FQP9N25C	FQPF9N25C	Units
V_{DSS}	Drain-Source Voltage		250		V
I _D	Drain Current - Continuous (T _C = 25°C)		8.8	8.8 *	Α
	- Continuous (T _C = 100°C))	5.6	5.6 *	Α
I_{DM}	Drain Current - Pulsed	(Note 1)	35.2	35.2 *	Α
V _{GSS}	Gate-Source Voltage		± 30		V
E _{AS}	Single Pulsed Avalanche Energy (No		285		mJ
I _{AR}	Avalanche Current	(Note 1)	8	3.8	Α
E _{AR}	Repetitive Avalanche Energy	(Note 1)	7	'.4	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		5.5		V/ns
P _D	Power Dissipation (T _C = 25°C)		74	38	W
	- Derate above 25°C		0.59	0.3	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150		°C
T _L	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds		300		°C

^{*} Drain current limited by maximum junction temperature.

Thermal Characteristics

Symbol	Parameter	FQP9N25C	FQPF9N25C	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	1.69	3.29	°C/W
$R_{\theta JS}$	Thermal Resistance, Case-to-Sink Typ.	0.5		°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	62.5	62.5	°C/W

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Cha	aracteristics					
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	250			V
ΔBV _{DSS} / ΔΤ _J	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25	°C	0.30		V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 250 V, V _{GS} = 0 V			10	μА
		V _{DS} = 200 V, T _C = 125°C			100	μΑ
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 30 V, V _{DS} = 0 V			100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -30 V, V _{DS} = 0 V			-100	nA
On Cha	racteristics					
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250 μA	2.0		4.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 4.4 A		0.35	0.43	Ω
9 _{FS}	Forward Transconductance	V _{DS} = 40 V, I _D = 4.4 A (Not	e 4)	7.0		S
C _{iss} C _{oss} C _{rss}	Input Capacitance Output Capacitance Reverse Transfer Capacitance	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$ f = 1.0 MHz		545 115 45.5	710 150 60	pF pF
	,			40.0	00	ρı
	Ing Characteristics Turn-On Delay Time			15	40	no
t _{d(on)}	Turn-On Rise Time	$V_{DD} = 125 \text{ V}, I_{D} = 8.8 \text{ A},$		15 85	40 180	ns ns
t _r	Turn-Off Delay Time	$R_G = 25 \Omega$		90	190	ns
t _{d(off)}	Turn-Off Fall Time	(Note	4, 5)	65	140	ns
Q _g	Total Gate Charge	\/ - 200 \/ I - 0 0 A		26.5	35	nC
Q_{gs}	Gate-Source Charge	$V_{DS} = 200 \text{ V}, I_{D} = 8.8 \text{ A},$ $V_{GS} = 10 \text{ V}$		3.5		nC
Q _{gd}	Gate-Drain Charge	VGS - 10 V (Note		13.5		nC
gu	2.0 2.0	,				
Drain-S	Source Diode Characteristics a	nd Maximum Ratings				
I _S	Maximum Continuous Drain-Source Diode Forward Current				8.8	Α
I _{SM}	Maximum Pulsed Drain-Source Diode F				35.2	Α
V_{SD}	Drain-Source Diode Forward Voltage	$V_{GS} = 0 \text{ V}, I_S = 8.8 \text{ A}$			1.5	V
t _{rr}	Reverse Recovery Time	$V_{GS} = 0 \text{ V}, I_{S} = 8.8 \text{ A},$		218		ns
Q _{rr}	Reverse Recovery Charge	$dI_F / dt = 100 A/\mu s$ (Not	te 4)	1.58		μC

- **Notes:**1. Repetitive Rating : Pulse width limited by maximum junction temperature 2. L = 5.9mH, I_{AS} = 8.8A, V_{DD} = 50V, R_{G} = 25 Ω , Starting T_{J} = 25°C 3. $I_{SD} \le$ 8.8A, di/dt \le 300A/ μ s, $V_{DD} \le$ BV $_{DSS}$, Starting T_{J} = 25°C 4. Pulse Test : Pulse width \le 300 μ s, Duty cycle \le 2% 5. Essentially independent of operating temperature

Typical Characteristics

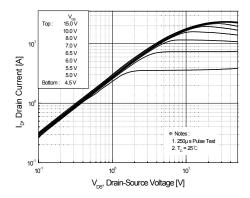


Figure 1. On-Region Characteristics

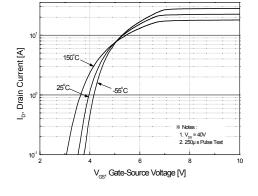


Figure 2. Transfer Characteristics

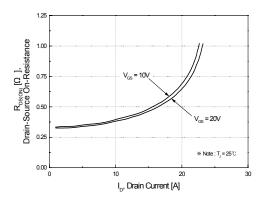


Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

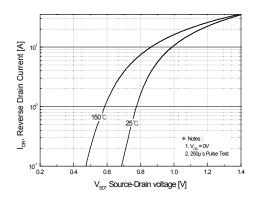


Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

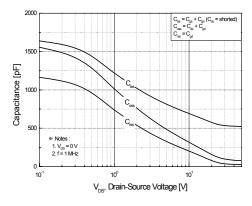


Figure 5. Capacitance Characteristics

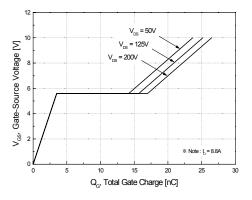


Figure 6. Gate Charge Characteristics

Typical Characteristics (Continued)

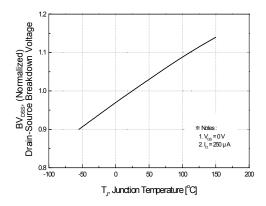


Figure 7. Breakdown Voltage Variation vs Temperature

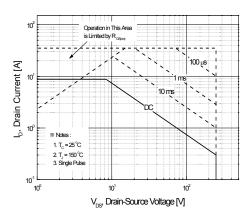


Figure 9-1. Maximum Safe Operating Area for FQP9N25C

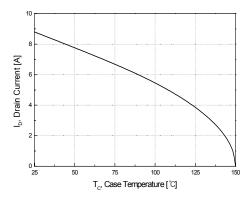


Figure 10. Maximum Drain Current vs Case Temperature

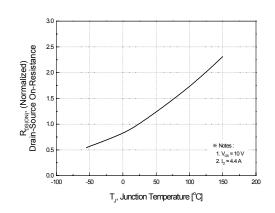


Figure 8. On-Resistance Variation vs Temperature

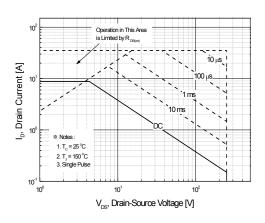


Figure 9-2. Maximum Safe Operating Area for FQPF9N25C

Typical Characteristics (Continued)

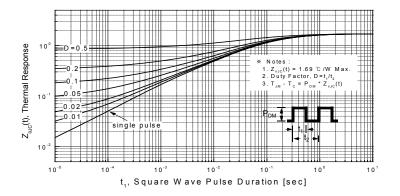


Figure 11-1. Transient Thermal Response Curve for FQP9N25C

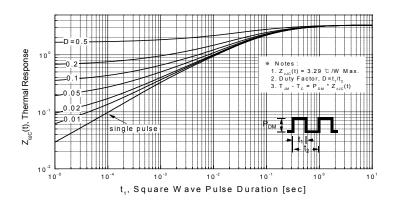
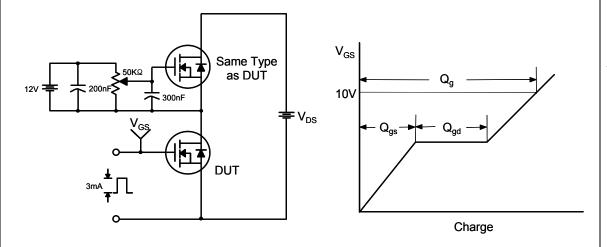


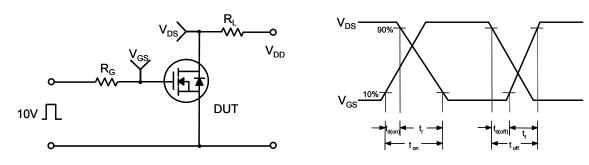
Figure 11-2. Transient Thermal Response Curve for FQPF9N25C

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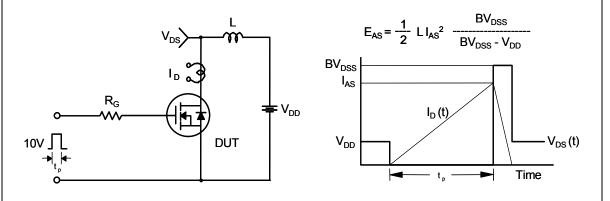
Gate Charge Test Circuit & Waveform



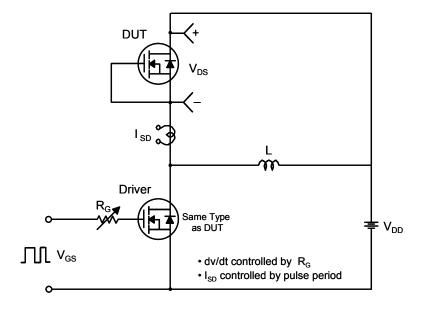
Resistive Switching Test Circuit & Waveforms

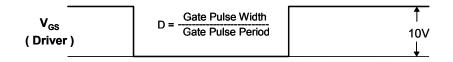


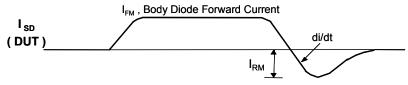
Unclamped Inductive Switching Test Circuit & Waveforms



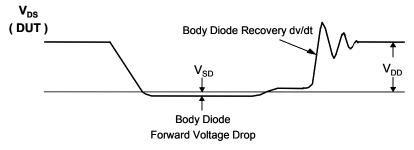
Peak Diode Recovery dv/dt Test Circuit & Waveforms

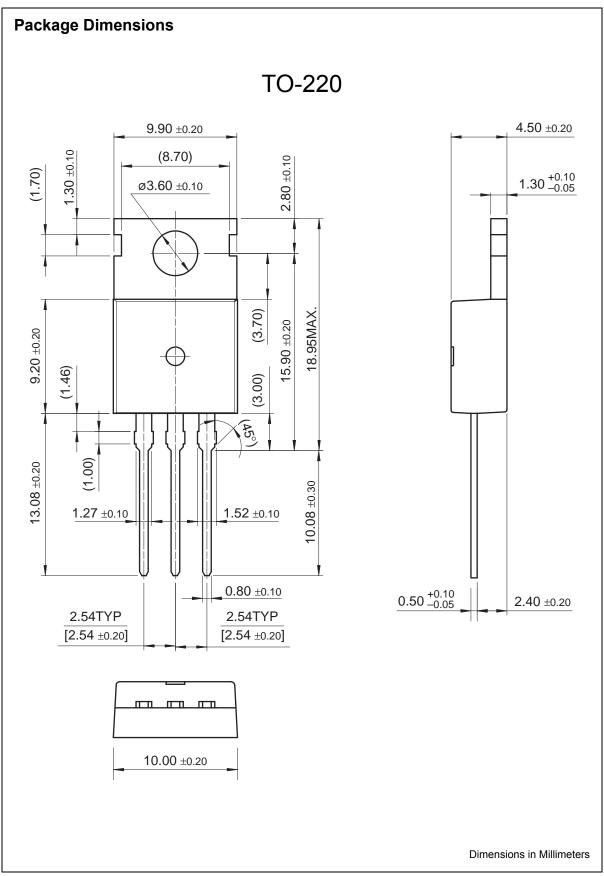


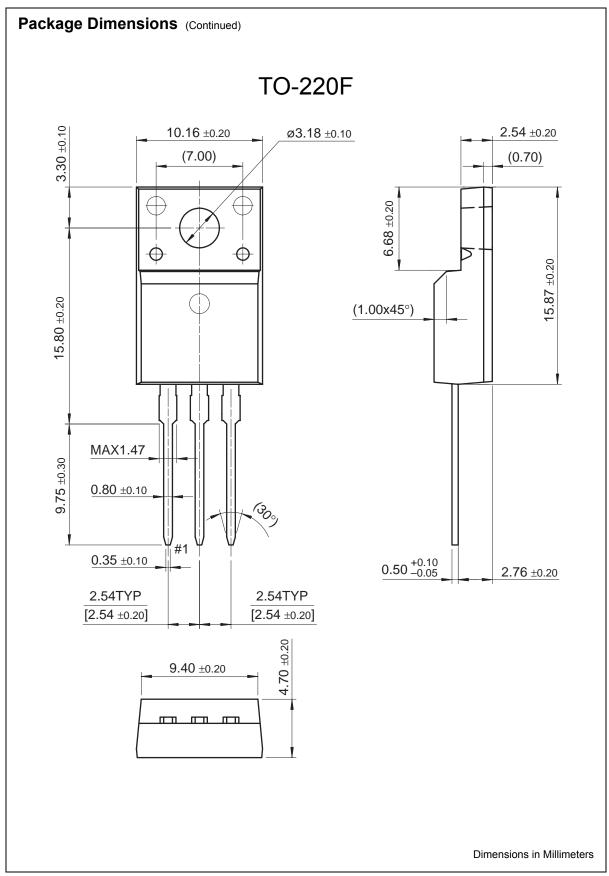




Body Diode Reverse Current







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