



OPTICALLY COUPLED ISOLATOR PHOTOTRANSISTOR OUTPUT

APPROVALS

- UL recognised, File No. E91231

DESCRIPTION

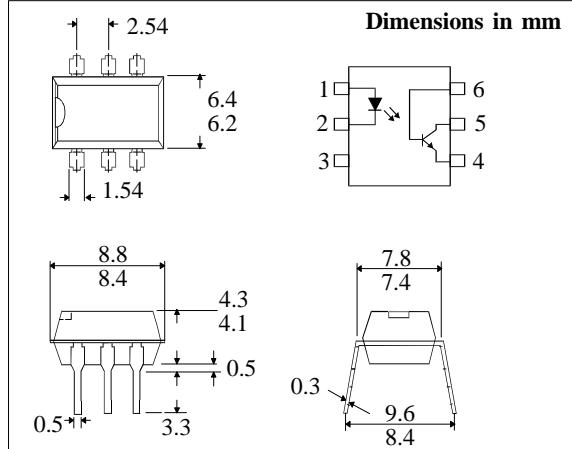
The MCT210 optically coupled isolator consists of an infrared light emitting diode and NPN silicon photo transistor in a standard 6 pin dual in line plastic package.

FEATURES

- Options :-
10mm lead spread - add G after part no.
Surface mount - add SM after part no.
Tape&reel - add SMT&R after part no.
- High Isolation Voltage (5.3kV_{RMS}, 7.5kV_{PK})
- All electrical parameters 100% tested
- Custom electrical selections available

APPLICATIONS

- DC motor controllers
- Industrial systems controllers
- Measuring instruments
- Signal transmission between systems of different potentials and impedances



ABSOLUTE MAXIMUM RATINGS (25°C unless otherwise specified)

Storage Temperature	-55°C to + 150°C
Operating Temperature	-55°C to + 100°C
Lead Soldering Temperature (1/16 inch (1.6mm) from case for 10 secs)	260°C

INPUT DIODE

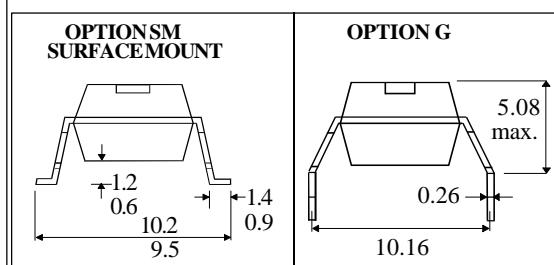
Forward Current	60mA
Reverse Voltage	6V
Power Dissipation	105mW

OUTPUT TRANSISTOR

Collector-emitter Voltage BV _{CEO}	30V
Collector-base Voltage BV _{CBO}	30V
Emitter-collector Voltage BV _{ECC}	6V
Power Dissipation	160mW

POWER DISSIPATION

Total Power Dissipation	200mW
(derate linearly 2.67mW/°C above 25°C)	



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ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ Unless otherwise noted)

PARAMETER		MIN	TYP	MAX	UNITS	TEST CONDITION
Input	Forward Voltage (V_F) Reverse Voltage (V_R) Reverse Current (I_R)	6	1.2	1.5	V V μA	$I_F = 40\text{mA}$ $I_R = 10\mu\text{A}$ $V_R = 6\text{V}$
Output	Collector-emitter Breakdown (BV_{CEO}) (note 2) Collector-base Breakdown (BV_{CBO}) Emitter-collector Breakdown (BV_{ECO}) Collector-emitter Dark Current (I_{CEO})	30 30 6 50			V V nA	$I_C = 1\text{mA}$ $I_C = 10\mu\text{A}$ $I_E = 100\mu\text{A}$ $V_{CE} = 10\text{V}$
Coupled	Current Transfer Ratio (CTR)	50 150			% %	$3.2\text{mA } I_F \text{ to } 32\text{mA } I_F$, $0.4\text{V } V_{CE}$ $10\text{mA } I_F$, $5\text{V } V_{CE}$
	Collector-emitter Saturation Voltage $V_{CE(SAT)}$		0.4		V	$32\text{mA } I_F$, $16\text{mA } I_C$
	Input to Output Isolation Voltage V_{ISO}	5300 7500			V_{RMS} V_{PK}	See note 1 See note 1
	Input-output Isolation Resistance R_{ISO}	5×10^{10}			Ω	$V_{IO} = 500\text{V}$ (note 1)
	Rise Time t_r Fall Time t_f		4 5		μs μs	$V_{CC} = 5\text{V}$, fig 1 $I_C = 2\text{mA}$, $R_L = 100\Omega$

Note 1 Measured with input leads shorted together and output leads shorted together.

Note 2 Special Selections are available on request. Please consult the factory.

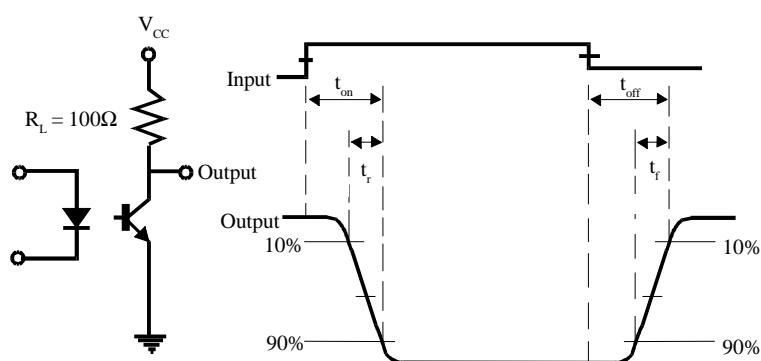


FIG 1

