Preferred Devices

Complementary Power Transistors

For Isolated Package Applications

... for general purpose power amplification and switching such as output or driver stages in applications such as switching regulators, converters and power amplifiers.

- Low Collector-Emitter Saturation Voltage -V_{CE(sat)} = 1.0 V (Max) @ 8.0 A
- Fast Switching Speeds
- Complementary Pairs Simplifies Designs

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V _{CEO}	80	Vdc
Emitter-Base Voltage	V _{EB}	5	Vdc
Collector Current - Continuous - Peak	I _C	10 20	Adc
Total Power Dissipation @ T _C = 25°C Derate above 25°C	P _D	36 1.67	Watts W/°C
Total Power Dissipation @ T _A = 25°C Derate above 25°C	P _D	2.0 0.016	Watts W/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	- 55 to 150	°C

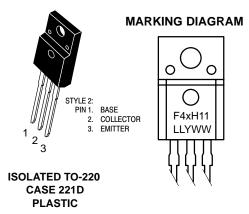
THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit	
Thermal Resistance, Junction to Case	$R_{\theta JC}$	3.5	°C/W	
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	62.5	°C/W	



http://onsemi.com

SILICON POWER TRANSISTORS 10 AMPERES 80 VOLTS 36 WATTS



F4xH11 = Specific Device Code

= 4 or 5

LL = Location Code

Y = Year WW = Work Week

ORDERING INFORMATION

Device	Package	Shipping		
MJF44H11	TO-220	50 Units/Rail		
MJF45H11	TO-220	50 Units/Rail		

Preferred devices are recommended choices for future use and best overall value.

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Characteristic		Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS				1		1
Collector-Emitter Sustaining Voltage ($I_C = 30 \text{ mA}, I_B = 0$)		V _{CEO(sus)}	80	-	-	Vdc
Collector Cutoff Current $(V_{CE} = Rated V_{CEO}, V_{BE} = 0)$		I _{CES}	-	-	1.0	μΑ
Emitter Cutoff Current (V _{EB} = 5 Vdc)		I _{EBO}	-	-	10	μΑ
ON CHARACTERISTICS						•
Collector-Emitter Saturation Voltage (I _C = 8 Adc, I _B = 0.4 Adc)		V _{CE(sat)}	-	-	1.0	Vdc
Base-Emitter Saturation Voltage $(I_C = 8 \text{ Adc}, I_B = 0.8 \text{ Adc})$		V _{BE(sat)}	-	-	1.5	Vdc
DC Current Gain (V _{CE} = 1 Vdc, I _C = 2 Adc)		h _{FE}	60	-	-	-
DC Current Gain (V _{CE} = 1 Vdc, I _C = 4 Adc)			40	-	-	
DYNAMIC CHARACTERISTICS						•
Collector Capacitance (V _{CB} = 10 Vdc, f _{test} = 1 MHz)	MJF44H11 MJF45H11	C _{cb}	- -	130 230	-	pF
Gain Bandwidth Product ($I_C = 0.5 \text{ Adc}, V_{CE} = 10 \text{ Vdc}, f = 20 \text{ MHz}$)	MJF44H11 MJF45H11	f _T	<u>-</u> -	50 40	-	MHz
SWITCHING TIMES						•
Delay and Rise Times $(I_C = 5 \text{ Adc}, I_{B1} = 0.5 \text{ Adc})$	MJF44H11 MJF45H11	t _d + t _r	- -	300 135	- -	ns
Storage Time $(I_C = 5 \text{ Adc}, I_{B1} = I_{B2} = 0.5 \text{ Adc})$	MJF44H11 MJF45H11	t _s	-	500 500	-	ns
Fall Time $(I_C = 5 \text{ Adc}, I_{B1} = I_{B2} = 0.5 \text{ Adc})$	MJF44H11 MJF45H11	t _f	-	140 100	- -	ns

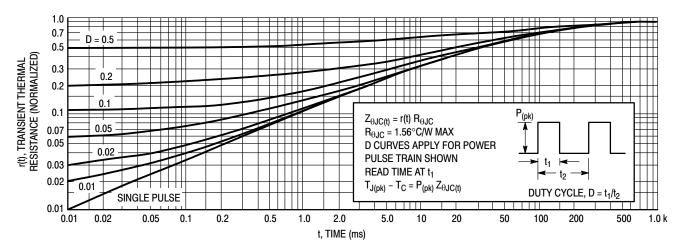


Figure 1. Thermal Response

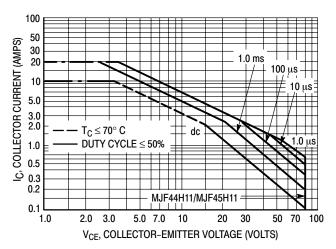


Figure 2. Maximum Rated Forward Bias Safe Operating Area

There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate I_C - V_{CE} limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Figure 2 is based on $T_{J(pk)} = 150^{\circ}C$; T_C is variable depending on conditions. Second breakdown pulse limits are valid for duty cycles to 10% provided $T_{J(pk)} \leq 150^{\circ}C$. $T_{J(pk)}$ may be calculated from the data in Figure 1. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.

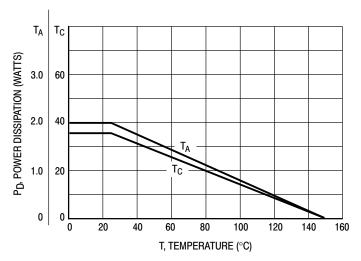


Figure 3. Power Derating

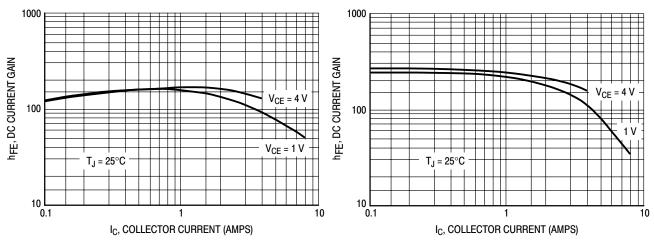


Figure 4. MJF44H11 DC Current Gain

Figure 5. MJF45H11 DC Current Gain

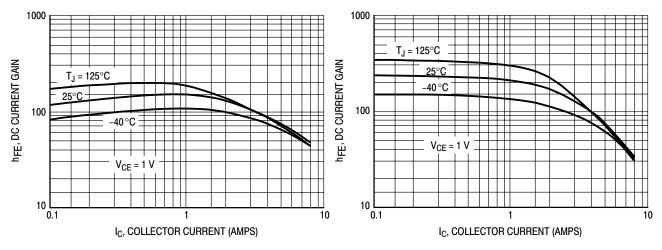


Figure 6. MJF44H11 Current Gain versus Temperature

Figure 7. MJF45H11 Current Gain versus Temperature

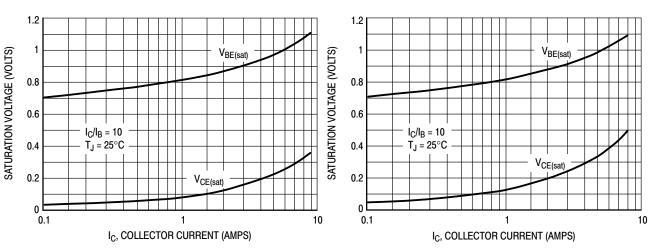


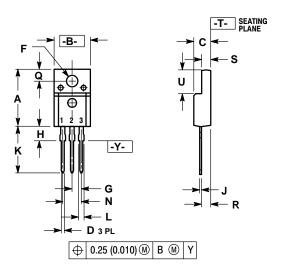
Figure 8. MJF44H11 On-Voltages

Figure 9. MJF45H11 On-Voltages

PACKAGE DIMENSIONS

TO-220 FULLPAK TRANSISTOR

CASE 221D-03 ISSUE G



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH
 3. 221D-01 THRU 221D-02 OBSOLETE, NEW STANDARD 221D-03.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.625	0.635	15.88	16.12
В	0.408	0.418	10.37	10.63
С	0.180	0.190	4.57	4.83
D	0.026	0.031	0.65	0.78
F	0.116	0.119	2.95	3.02
G	0.100 BSC		2.54 BSC	
Н	0.125	0.135	3.18	3.43
J	0.018	0.025	0.45	0.63
K	0.530	0.540	13.47	13.73
L	0.048	0.053	1.23	1.36
N	0.200 BSC		5.08 BSC	
Q	0.124	0.128	3.15	3.25
R	0.099	0.103	2.51	2.62
S	0.101	0.113	2.57	2.87
U	0.238	0.258	6.06	6.56

- STYLE 2:
 PIN 1. BASE
 2. COLLECTOR
 3. EMITTER





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