## **Field Effect Transistor**

Silicon N Channel MOS Type ( $\pi$ -MOS III.5) High Speed, High Current DC-DC Converter, Relay Drive and Motor Drive Applications

### **Features**

- Low Drain-Source ON Resistance
  - $R_{DS(ON)} = 0.15\Omega$  (Typ.)
- High Forward Transfer Admittance
  - $|Y_{fs}| = 21S$  (Typ.)
- Low Leakage Current
  - $I_{DSS} = 300 \mu A \text{ (Max.)} @ V_{DS} = 500 V$
- Enhancement-Mode
  - $V_{th} = 1.5 \sim 3.5 V @ V_{DS} = 10 V$ ,  $I_{D} = 1 mA$

### **Absolute Maximum Ratings (Ta = 25°C)**

CHARACTERISTIC		SYMBOL	RATING	UNIT
Drain-Source Voltage		V <sub>DSS</sub>	500	V
Drain-Gate Voltage ( $R_{GS} = 20k\Omega$ )		V <sub>DGR</sub>	500	V
Gate-Source Voltage		V <sub>GSS</sub>	±30	V
Drain Current	DC	I <sub>D</sub>	25	А
	Pulse	I <sub>DP</sub>	100	
Drain Power Dissipation (Tc = 25°C)		P <sub>D</sub>	200	W
Channel Temperature		T <sub>ch</sub>	150	°C
Storage Temperature Range		T <sub>stg</sub>	-55 ~ 150	°C

### **Thermal Characteristics**

CHARACTERISTIC	SYMBOL	MAX.	UNIT
Thermal Resistance, Channel to Case	R <sub>th(ch-c)</sub>	0.625	°C/W
Thermal Resistance, Channel to Ambient	R <sub>th(ch-a)</sub>	35.7	°C/W

This transistor is an electrostatic sensitive device. Please handle with care.

# 1. GATE 2. DRAIN (HEAT SINK) 3. SOURCE JEDEC —

2-21F1B

Weight: 9.75g

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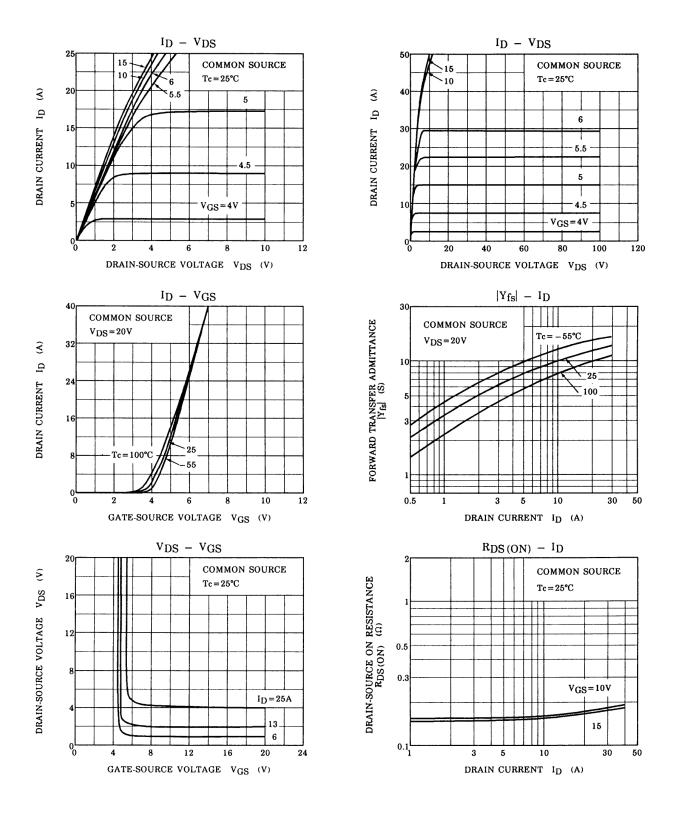
# Electrical Characteristics (Ta = 25°C)

CHARA	ACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Gate Leakage Cu	urrent	I <sub>GSS</sub>	$V_{GS} = \pm 25V, V_{DS} = 0V$	_	_	±100	nA
Drain Cut-off Cu	Drain Cut-off Current		$V_{DS} = 500V, V_{GS} = 0V$	-	-	300	μA
Drain-Source Br	eakdown Voltage	V <sub>(BR) DSS</sub>	$I_D = 10$ mA, $V_{GS} = 0$ V	500	-	-	V
Gate Threshold \	Voltage	V <sub>th</sub>	$V_{DS} = 10V$ , $I_D = 1mA$	1.5	-	3.5	V
Drain-Source Of	N Resistance	R <sub>DS (ON)</sub>	$I_D = 13A, V_{GS} = 10V$	-	0.15	0.20	Ω
Forward Transfer	r Admittance	IY <sub>fs</sub> I	$V_{DS} = 10V, I_D = 13A$	10	21	-	S
Input Capacitano	ce	C <sub>iss</sub>		-	3700	5000	_
Reverse Transfer Capacitance		C <sub>rss</sub>	$V_{DS} = 10V, V_{GS} = 0V,$ f = 1MHz	_	400	750	pF
Output Capacitance		C <sub>oss</sub>		_	920	1300	
Switching Time	Rise Time	t <sub>r</sub>		-	185	370	ns
	Turn-on Time	t <sub>on</sub>	$V_{GS}$ $\square$ $I_{D}=13A$ $V_{GS}$ $\square$	_	240	480	
	Fall Time	t <sub>f</sub>	G Rt = 160	-	250	500	
	Turn-off Time	t <sub>off</sub>		-	590	1180	
			$V_{GS_0}^{10V} \downarrow I_{D} = 13A$ $V_{GS_0}^{10V} \downarrow R_L = 16\Omega$ $V_{IN} : t_r, t_f < 5 \text{ns}, V_{DD} = 200V$ $Duty \le 1\%, t_w = 10 \mu \text{s}$				
Total Gate Charge (Gate-Source Plus Gate-Drain)		Qg	V <sub>DD</sub> = 400V, V <sub>GS</sub> = 10V,	-	150	250	0
Gate-Source Charge		Q <sub>gs</sub>	$I_D = 25A$	-	70	-	nC
Gate-Drain ("Miller") Charge		Q <sub>gd</sub>		-	80	-	

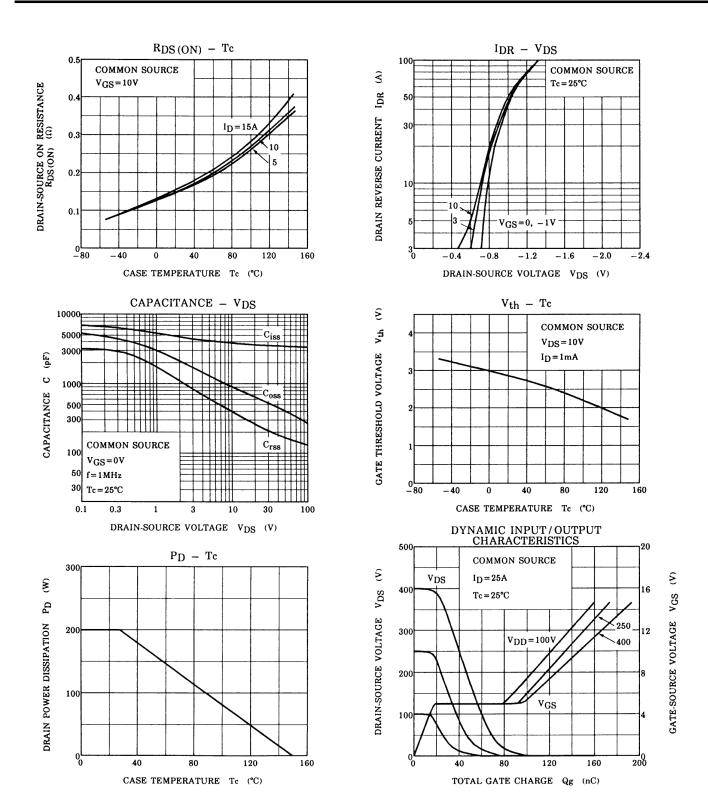
# Source-Drain Diode Ratings and Characteristics (Ta = 25°C)

CHARACTERISTICS	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Continuous Drain Reverse Current	I <sub>DR</sub>	_	-	-	25	Α
Pulse Drain Reverse Current	I <sub>DRP</sub>	-	-	-	100	А
Diode Forward Voltage	V <sub>DSF</sub>	$I_{DR} = 25A$ , $V_{GS} = 0V$	-	-	-1.6	V
Reverse Recovery Time	t <sub>rr</sub>	$I_{DR} = 25A$ , $V_{GS} = 0V$	-	780	-	ns
Reverse Recovered Charge	Q <sub>rr</sub>	dl <sub>DR</sub> /dt = 100A/μs	_	9.8	-	μC

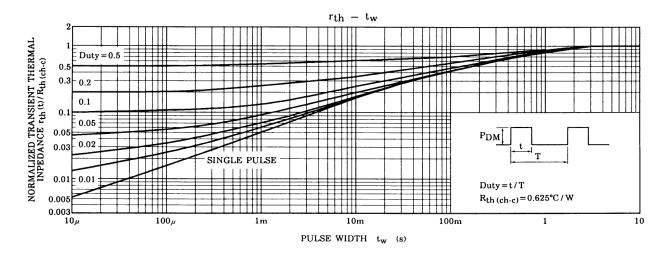
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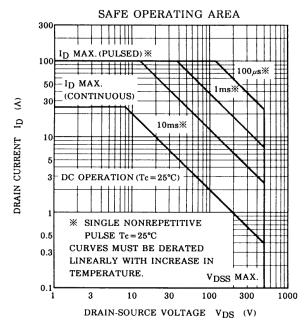


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