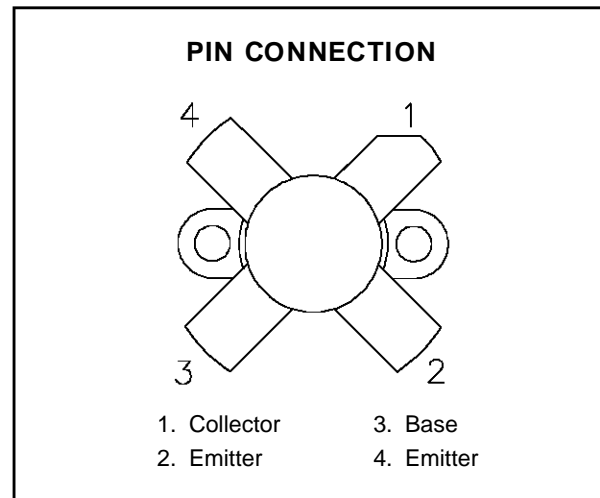
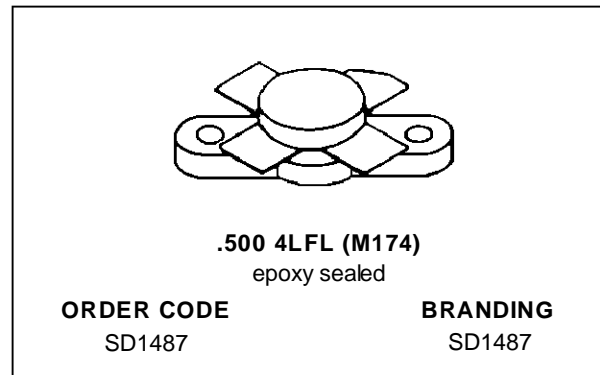


**RF & MICROWAVE TRANSISTORS  
HF SSB APPLICATIONS**

- 30 MHz
- 12.5 VOLTS
- IMD -30 dB
- COMMON EMITTER
- GOLD METALLIZATION
- P<sub>OUT</sub> = 100 W MIN. WITH 12.0 dB GAIN


**DESCRIPTION**

The SD1487 is a 12.5 V Class C epitaxial silicon NPN planar transistor designed primarily for HF communications. This device utilizes state-of-the-art diffused emitter ballasting to achieve extreme ruggedness under severe operating conditions.

**ABSOLUTE MAXIMUM RATINGS (T<sub>case</sub> = 25°C)**

Symbol	Parameter	Value	Unit
V <sub>CBO</sub>	Collector-Base Voltage	36	V
V <sub>CEO</sub>	Collector-Emitter Voltage	18	V
V <sub>EBO</sub>	Emitter-Base Voltage	4.0	V
I <sub>C</sub>	Device Current	20	A
P <sub>DISS</sub>	Power Dissipation	290	W
T <sub>J</sub>	Junction Temperature	+200	°C
T <sub>STG</sub>	Storage Temperature	- 65 to +150	°C

**THERMAL DATA**

R <sub>TH(j-c)</sub>	Junction-Case Thermal Resistance	0.6	°C/W
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# SD1487

## ELECTRICAL SPECIFICATIONS (T<sub>case</sub> = 25°C)

### STATIC

Symbol	Test Conditions		Value			Unit
			Min.	Typ.	Max.	
BV <sub>CBO</sub>	I <sub>C</sub> = 100mA	I <sub>E</sub> = 0mA	36	—	—	V
BV <sub>CES</sub>	I <sub>C</sub> = 100mA	V <sub>BE</sub> = 0V	36	—	—	V
BV <sub>CEO</sub>	I <sub>C</sub> = 100mA	I <sub>B</sub> = 0mA	18	—	—	V
BV <sub>EBO</sub>	I <sub>E</sub> = 20mA	I <sub>C</sub> = 0mA	4.0	—	—	V
I <sub>CES</sub>	V <sub>CE</sub> = 15V	I <sub>E</sub> = 0mA	—	—	20	mA
h <sub>FE</sub>	V <sub>CE</sub> = 5V	I <sub>C</sub> = 5A	10	—	200	—

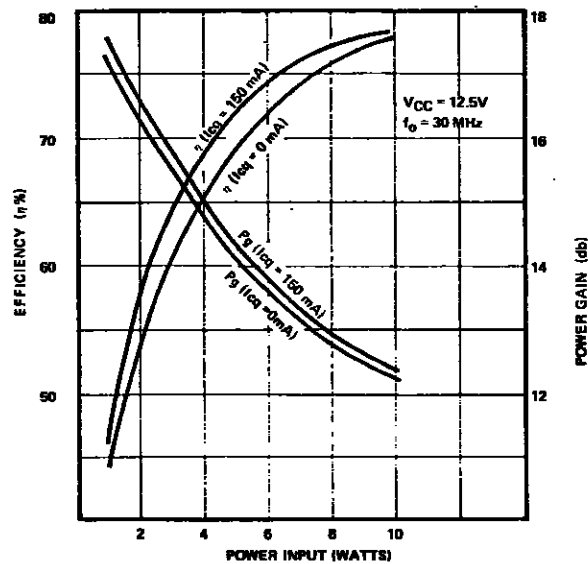
### DYNAMIC

Symbol	Test Conditions			Value			Unit
				Min.	Typ.	Max.	
P <sub>OUT</sub>	f = 30 MHz	V <sub>CE</sub> = 12.5 V	I <sub>CQ</sub> = 150mA	100	—	—	W
G <sub>P</sub>	f = 30 MHz	V <sub>CE</sub> = 12.5 V	I <sub>CQ</sub> = 150mA	11	13	—	dB
IMD <sub>3</sub> *	P <sub>OUT</sub> = 100WPEP	V <sub>CE</sub> = 12.5 V	I <sub>CQ</sub> = 150mA	—	—	-30	dBc
C <sub>OB</sub>	f = 1 MHz	V <sub>CB</sub> = 12.5 V		—	400	—	pF

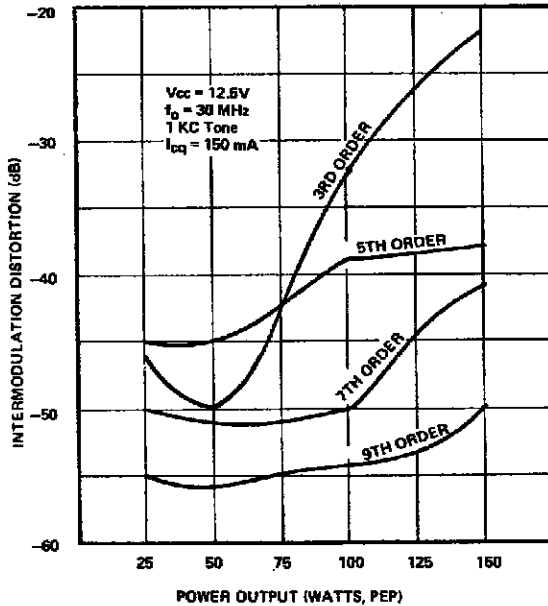
\*Note: f = 30 + 30.001MHz

### TYPICAL PERFORMANCE

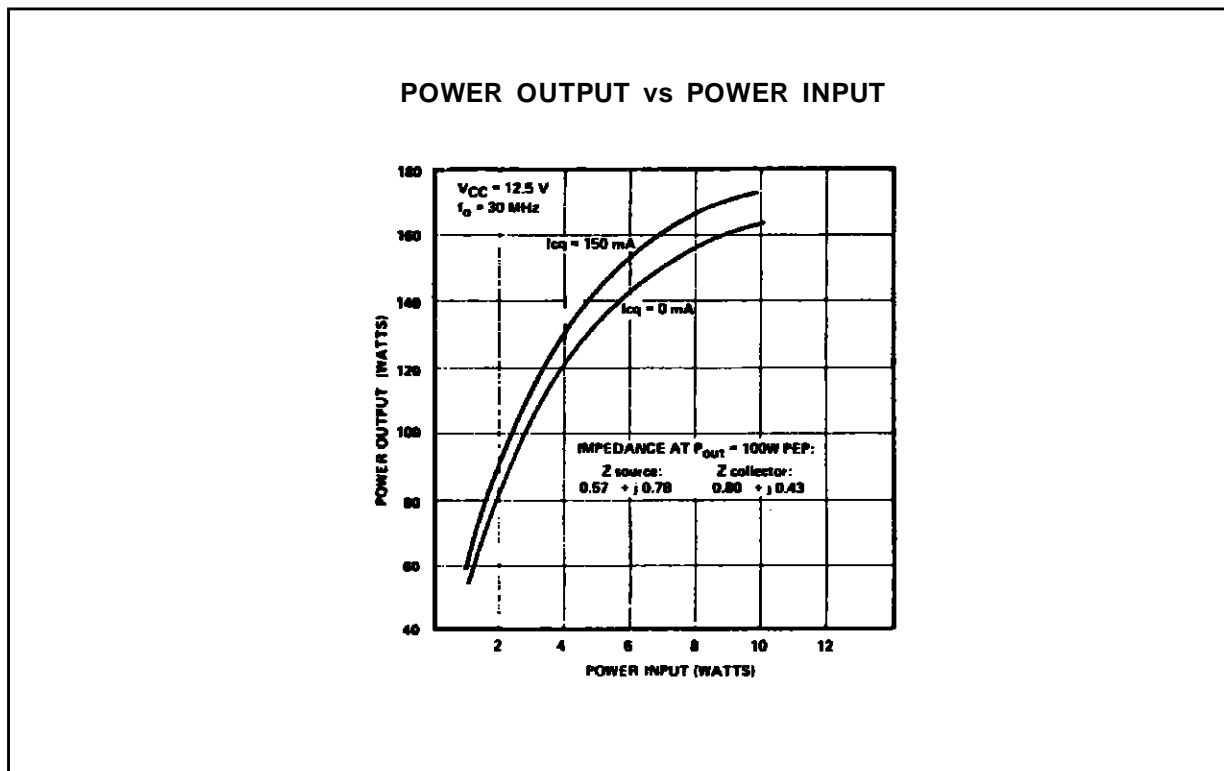
**POWER GAIN & COLLECTOR EFFICIENCY vs POWER INPUT**



**IMD vs POWER OUTPUT, PEP**



## TYPICAL PERFORMANCE (cont'd)



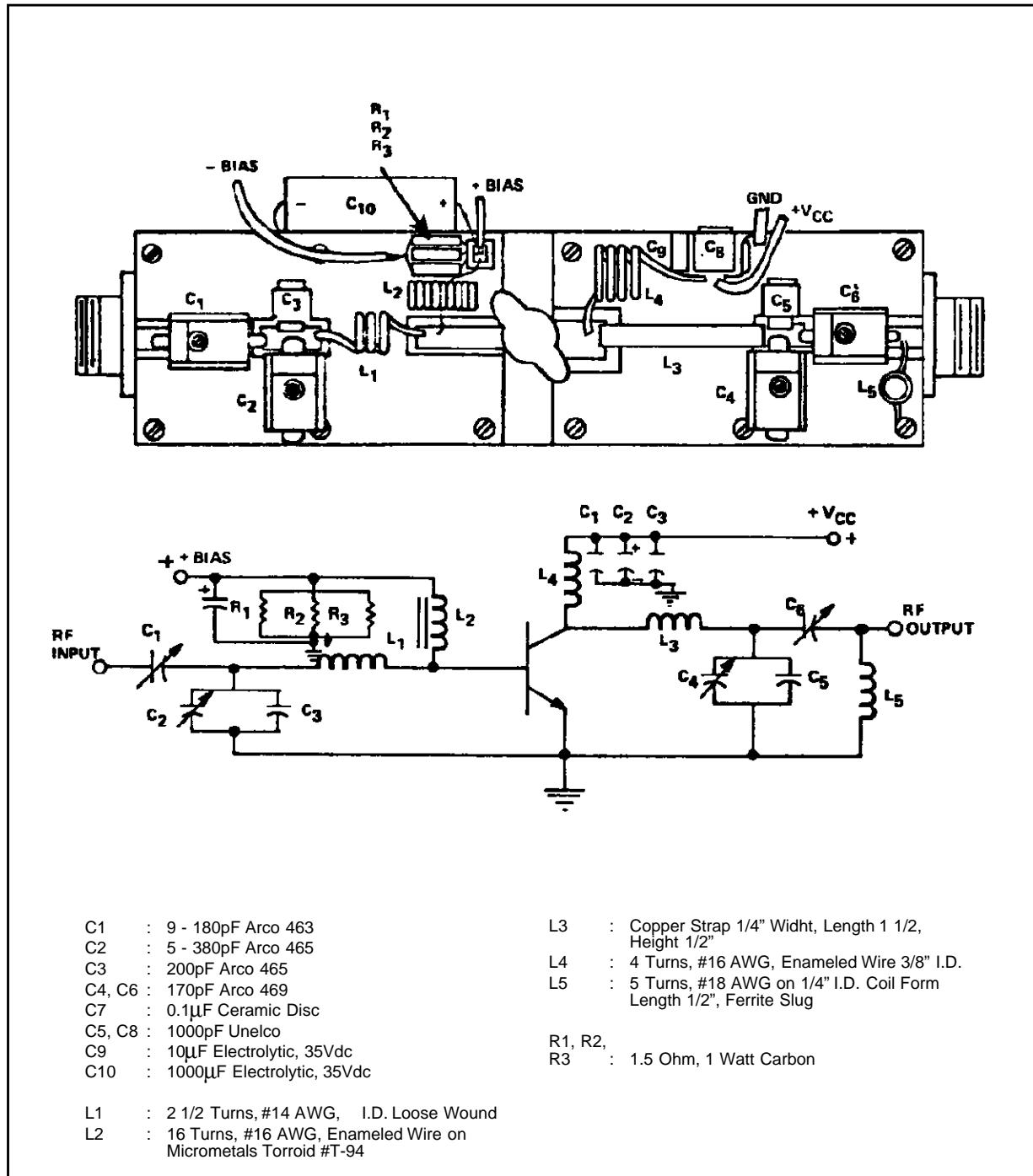
## IMPEDANCE DATA

FREQ.	$Z_{IN} (\Omega)$	$Z_{CL} (\Omega)$
30 MHz	$0.57 + j 0.78$	$0.80 + j 0.43$

$P_{OUT} = 100 \text{ W PEP}$

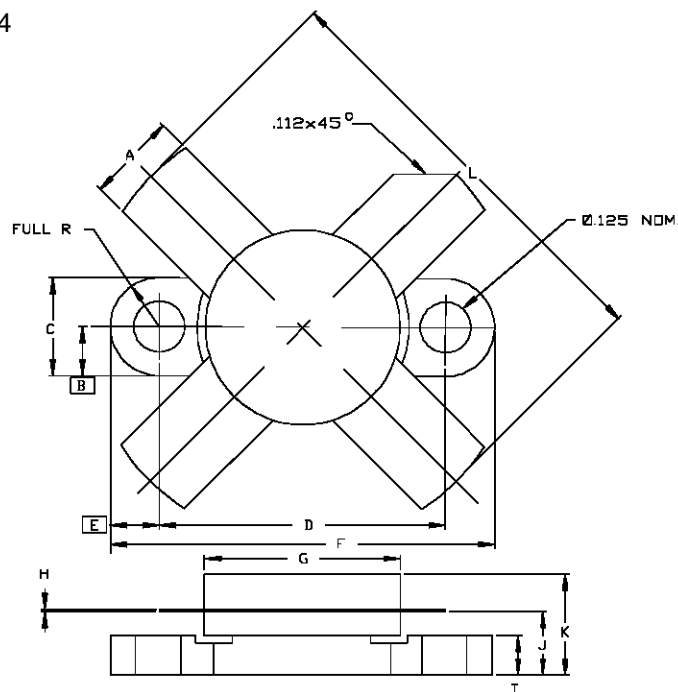
$V_{CE} = 12.5 \text{ V}$

## TEST CIRCUIT



## PACKAGE MECHANICAL DATA

Ref.: Dwg. No.12-0174



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	MINIMUM Inches/mm	MAXIMUM Inches/mm	
A	.220/5,59	.230/5,84	K
B	.125/3,18		L
C	.245/6,22	.255/6,48	
D	.720/18,28	.730/18,54	
E	.125/3,18		
F	.970/24,64	.980/24,89	
G	.495/12,57	.505/12,83	
H	.003/0,08	.007/0,18	
I	.090/2,29	.110/2,79	
J	.160/4,06	.175/4,45	

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