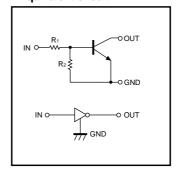
Digital transistors (built-in resistors)

DTC143EM / DTC143EE / DTC143EUA / DTC143EKA / DTC143ESA

●Features

- 1) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- 3) Only the on/off conditions need to be set for operation, making device design easy.

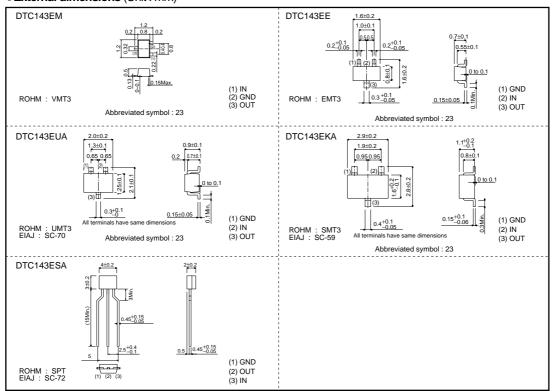
●Equivalent circuit



Structure

NPN digital transistor (Built-in resistor type)

●External dimensions (Unit: mm)



● Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits(DTC143E□)					
		М	E	UA	KA	SA	Unit
Supply voltage	Vcc		V				
Input voltage	Vin	-10 to +30					V
Output current	lo	100					
	IC(Max.)	100					- mA
Power dissipation	Pd	15	50	200		300	mW
Junction temperature	Tj	150					°C
Storage temperature	Tstg	-55 to +150					°C

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions		
Input voltage	VI(off)	-	-	0.5	V	Vcc=5V, Io=100μA		
	VI(on)	3	-	-	V	Vo=0.3V, Io=20mA		
Output voltage	VO(on)	-	0.1	0.3	V	lo/l≔10mA/0.5mA		
Input current	lı .	-	-	1.8	mA	Vi=5V		
Output current	IO(off)	-	-	0.5	μΑ	Vcc=50V, V⊫0V		
DC current gain	Gı	30	-	-	-	Vo=5V, Io=10mA		
Input resistance	R ₁	3.29	4.7	6.11	kΩ	-		
Resistance ratio	R ₂ /R ₁	0.8	1	1.2	-	-		
Transition frequency	f⊤	-	250	-	MHz	Vc=10V, I=-5mA, f=100MHz *		

^{*}Transition frequency of the device

Packaging specifications

Туре	Package	VMT3	EMT3	UMT3	SMT3	SPT		
	Packaging type	Taping	Taping	Taping	Taping	Taping		
	Code	T2L	TL	T106	T146	TP		
	Basic ordering unit (pieces)	8000	3000	3000	3000	5000		
DTC143EM		0	-	-	-	-		
DTC143EE		-	0	-	-	-		
DTC143EUA		-		0		-		
DTC143EKA		-	-	-	0	-		
DTC143ESA		-	-	-	-	0		

•Electrical characteristic curves

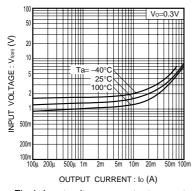


Fig.1 Input voltage vs. output current (ON characteristics)

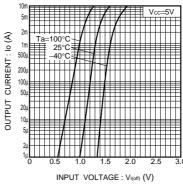


Fig.2 Output current vs. input voltage (OFF characteristics)

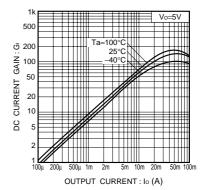


Fig.3 DC current gain vs. output current

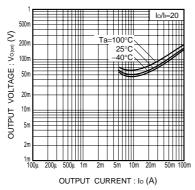


Fig.4 Output voltage vs. output current

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