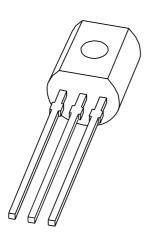
## **DISCRETE SEMICONDUCTORS**

# DATA SHEET



# PSS9015B PNP general purpose transistor

Product specification Supersedes data of 2002 Sep 20 2004 Aug 10





# PNP general purpose transistor

**PSS9015B** 

#### **FEATURES**

• Low collector capacitance.

#### **APPLICATIONS**

- General purpose switching and amplification
- Low frequency, low noise amplifier.

#### **DESCRIPTION**

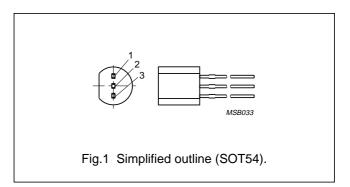
PNP transistor in a SOT54 plastic package. NPN complement: PSS9014.

#### **MARKING**

TYPE NUMBER	MARKING CODE
PSS9015B	S9015B

#### **PINNING**

PIN	DESCRIPTION
1	collector
2	base
3	emitter



#### **LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	_	-50	V
$V_{CEO}$	collector-emitter voltage	open base	_	<b>-45</b>	٧
$V_{EBO}$	emitter-base voltage	open collector	_	<b>-</b> 5	٧
I <sub>C</sub>	collector current (DC)		_	-100	mA
I <sub>CM</sub>	peak collector current		_	-200	mA
I <sub>BM</sub>	peak base current		_	-200	mA
P <sub>tot</sub>	total power dissipation	up to T <sub>amb</sub> = 25 °C; note 1	_	500	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T <sub>amb</sub>	operating ambient temperature		-65	+150	°C

#### Note

1. Device mounted on a printed-circuit board; single sided copper; tinplated; standard footprint.

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#### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th j-a</sub>	thermal resistance from junction to	in free air; note 1	240	K/W
	ambient			

#### Note

1. Device mounted on a printed-circuit board; single sided copper; tinplated; standard footprint.

#### **CHARACTERISTICS**

 $T_{amb}$  = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I <sub>CBO</sub>	collector-base cut-off current	$V_{CB} = -30 \text{ V}; I_E = 0$	_	_	-50	nA
		$V_{CB} = -30 \text{ V}; I_E = 0;$ $T_{amb} = 150 \text{ °C}$	_	_	-5	μΑ
I <sub>CEO</sub>	collector-emitter cut-off current	$V_{CE} = -30 \text{ V}; I_{B} = 0$	_	_	-100	nA
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB} = -5 \text{ V}; I_C = 0$	_	_	-100	nA
h <sub>FE</sub>	DC current gain	$I_C = -1 \text{ mA}; V_{CE} = -5 \text{ V}$	100	200	300	
V <sub>CEsat</sub>	saturation voltage	$I_C = -100 \text{ mA}; I_B = -5 \text{ mA}; \text{ note } 1$	_	_	-700	mV
V <sub>BEsat</sub>	saturation voltage	$I_C = -100 \text{ mA}; I_B = -5 \text{ mA}; \text{ note } 1$	_	_	-1000	mV
V <sub>BEon</sub>	base-emitter turn-on voltage	$I_C = -2 \text{ mA}; V_{CE} = -5 \text{ V}$	-600	_	-750	mV
f <sub>T</sub>	transition frequency	I <sub>C</sub> = -10 mA; V <sub>CE</sub> = -10 V; f = 100 MHz	100	-	_	MHz
C <sub>c</sub>	collector capacitance	$V_{CB} = -10 \text{ V}; I_E = I_e = 0;$ f = 1 MHz	_	_	7	pF
F	noise figure	$V_{CE} = -5 \text{ V}; I_{C} = -0.2 \text{ mA};$ $R_{S} = 1 \text{ k}\Omega; f = 1 \text{ kHz}; B = 200 \text{ Hz}$	_	_	10	dB

#### Note

1. Pulse test:  $t_p \le 300 \ \mu s; \ \delta \le 0.02.$ 

# PNP general purpose transistor

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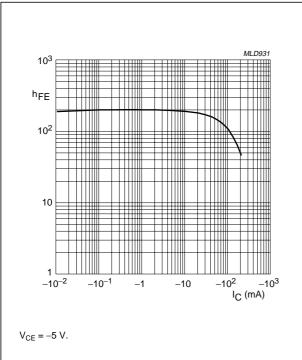


Fig.2 DC current gain as a function of collector current; typical values.

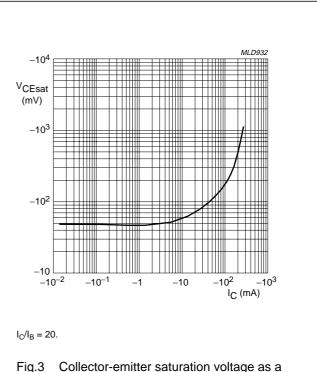
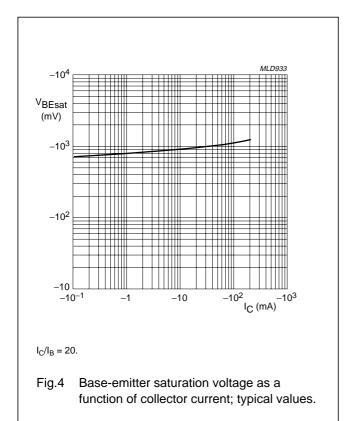
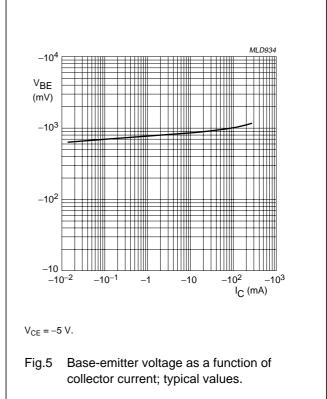


Fig.3 Collector-emitter saturation voltage as a function of collector current; typical values.





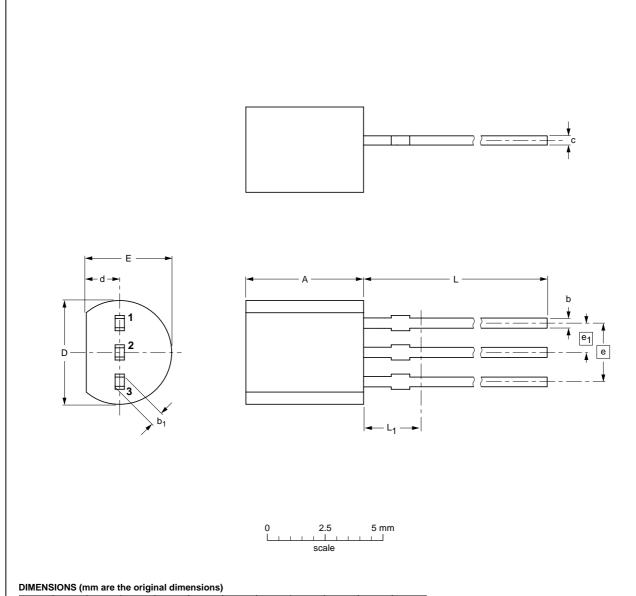
# PNP general purpose transistor

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#### **PACKAGE OUTLINE**

#### Plastic single-ended leaded (through hole) package; 3 leads

SOT54



UNIT	A	b	b <sub>1</sub>	С	D	d	E	е	e <sub>1</sub>	L	L <sub>1</sub> <sup>(1)</sup> max.	
mm	5.2 5.0	0.48 0.40	0.66 0.55	0.45 0.38	4.8 4.4	1.7 1.4	4.2 3.6	2.54	1.27	14.5 12.7	2.5	

#### Note

1. Terminal dimensions within this zone are uncontrolled to allow for flow of plastic and terminal irregularities.

OUTLINE	OUTLINE REFERENCES					ISSUE DATE
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT54		TO-92	SC-43A			<del>97-02-28</del> 04-06-28

### PNP general purpose transistor

PSS9015B

#### **DATA SHEET STATUS**

LEVEL	DATA SHEET STATUS <sup>(1)</sup>	PRODUCT STATUS(2)(3)	DEFINITION
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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Printed in The Netherlands

R75/02/pp7

Date of release: 2004 Aug 10

Document order number: 9397 750 13687

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