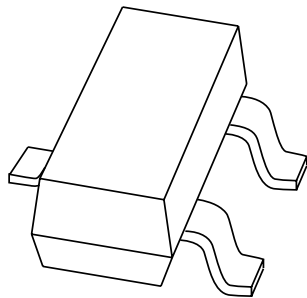


# DATA SHEET



## **PLVA2600A series** Low-voltage avalanche regulator double diodes

Product specification  
Supersedes data of 1996 May 06

1999 May 10

# Low-voltage avalanche regulator double diodes

## PLVA2600A series

### FEATURES

- Very low dynamic impedance at low currents: approximately 1/20 of conventional series
- Hard breakdown knee
- Low noise: approximately 1/10 of conventional series
- Total power dissipation: max. 250 mW
- Small tolerances of  $V_Z$
- Working voltage range: nom. 5.0 to 6.8 V
- Non-repetitive peak reverse power dissipation: max. 30 W.

### APPLICATIONS

- Low current, low power, low noise applications
- CMOS RAM back-up circuits
- Voltage stabilizers
- Voltage limiters
- Smoke detector relays.

### DESCRIPTION

The PLVA2600A series consists of two high performance voltage regulator diodes with common anodes, in small SOT23 plastic SMD packages.

The series consists of PLVA2650A to PLVA2668A.

### MARKING

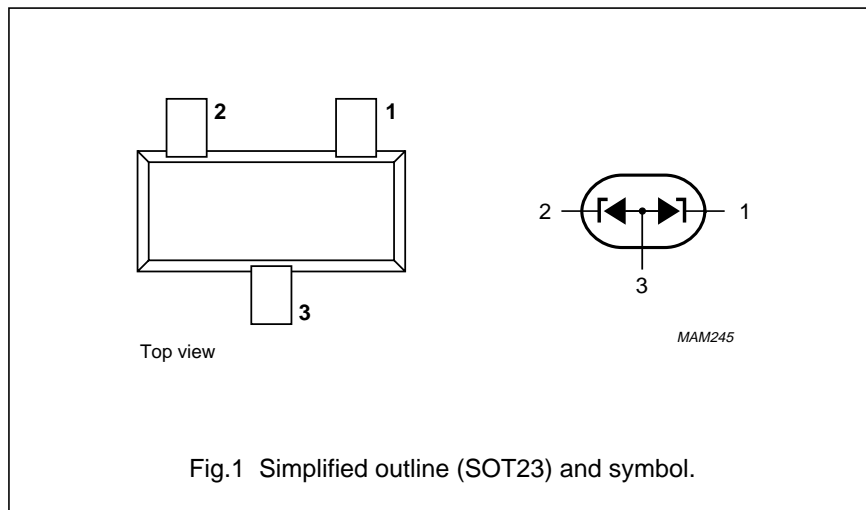
TYPE NUMBER	MARKING CODE <sup>(1)</sup>
PLVA2650A	*9J
PLVA2653A	*9K
PLVA2656A	*9L
PLVA2659A	*9M
PLVA2662A	*9N
PLVA2665A	*9O
PLVA2668A	*9P

### Note

1. \* = p : Made in Hong Kong.  
\* = t : Made in Malaysia.

### PINNING

PIN	DESCRIPTION
1	cathode (k1)
2	cathode (k2)
3	common anode



# Low-voltage avalanche regulator double diodes

## PLVA2600A series

### LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$I_F$	continuous forward current		–	250	mA
$I_{ZRM}$	repetitive peak working current	$t_p = 100 \mu\text{s}; \delta = 10\%$	–	250	mA
$P_{ZSM}$	non-repetitive peak reverse power dissipation	$t_p = 100 \mu\text{s}; T_j = 150 \text{ }^\circ\text{C}$	–	30	W
$P_{tot}$	total power dissipation	single diode loaded; $T_{amb} = 25 \text{ }^\circ\text{C}; \text{note 1}$	–	250	mW
		double diode loaded; $T_{amb} = 25 \text{ }^\circ\text{C}; \text{note 1}$	–	180	mW
$T_{stg}$	storage temperature		–65	+150	$^\circ\text{C}$
$T_j$	junction temperature		–	150	$^\circ\text{C}$

### Note

1. Device mounted on an FR4 printed circuit-board.

# Low-voltage avalanche regulator double diodes

## PLVA2600A series

### ELECTRICAL CHARACTERISTICS

$T_j = 25\text{ }^\circ\text{C}$  unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$V_F$	forward voltage	$I_F = 10\text{ mA}$	–	–	0.9	V
$V_Z$	working voltage	$I_Z = 250\text{ }\mu\text{A}$	4.80	5.00	5.20	V
			5.10	5.30	5.50	V
			5.40	5.60	5.80	V
			5.70	5.90	6.10	V
			6.00	6.20	6.40	V
			6.30	6.50	6.70	V
			6.60	6.80	7.00	V
	working voltage	$I_Z = 10\text{ }\mu\text{A}$	–	4.30	–	V
			–	5.20	–	V
			–	5.51	–	V
			–	5.85	–	V
			–	6.19	–	V
			–	6.49	–	V
			–	6.80	–	V
$R_Z$	dynamic resistance	1 kHz superimposed; $I_{ZAC}$ is 10% of $I_{ZDC}$ ; $I_Z = 250\text{ }\mu\text{A}$	–	–	700	$\Omega$
			–	–	250	$\Omega$
			–	–	100	$\Omega$
$S_Z$	temperature coefficient	$I_Z = 250\text{ }\mu\text{A}$	–	0.20	–	mV/K
			–	1.60	–	mV/K
			–	1.90	–	mV/K
			–	2.40	–	mV/K
			–	2.65	–	mV/K
			–	2.90	–	mV/K
			–	3.40	–	mV/K
$I_R$	reverse current	$V_R = 80\%$ ; $V_Z$ nominal	–	–	20000	nA
			–	–	5000	nA
			–	–	1000	nA
			–	–	500	nA
			–	–	100	nA
			–	–	50	nA
			–	–	10	nA

# Low-voltage avalanche regulator double diodes

## PLVA2600A series

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT					
$I_R$	reverse current	$V_R = 50\%$ ; $V_Z$ nominal	–	34	–	nA					
	PLVA2650A										
	PLVA2653A										
	PLVA2656A										
	PLVA2659A										
	PLVA2662A										
	PLVA2665A										
	PLVA2668A										
	reverse current	$V_R = 90\%$ ; $V_Z$ nominal	–	21	–	$\mu$ A					
	PLVA2650A										
	PLVA2653A										
	PLVA2656A										
	PLVA2659A										
	PLVA2662A										
PLVA2665A											
PLVA2668A											
$\Delta V_Z$	line regulation	$I_{LO} = 10 \mu\text{A}$ ; $I_{Hi} = 1 \text{ mA}$	–	–	0.1	V					
	PLVA2659A to PLVA2668A										
	PLVA2656A						$I_{LO} = 50 \mu\text{A}$ ; $I_{Hi} = 1 \text{ mA}$	–	–	0.1	V
	PLVA2650A						$I_{LO} = 100 \mu\text{A}$ ; $I_{Hi} = 1 \text{ mA}$	–	–	0.4	V
PLVA2653A	$I_{LO} = 100 \mu\text{A}$ ; $I_{Hi} = 1 \text{ mA}$	–	–	0.2	V						
$V_n$	noise voltage density	$f = 1 \text{ kHz}$ ; $B = 1 \text{ kHz}$ ; $I_Z = 250 \mu\text{A}$	–	–	1.0	$\frac{\mu\text{V}}{\sqrt{\text{Hz}}}$					

### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j-tp}$	thermal resistance from junction to tie-point		360	K/W
$R_{th\ j-a}$	thermal resistance from junction to ambient	note 1	500	K/W

#### Note

1. Device mounted on an FR4 printed circuit-board.

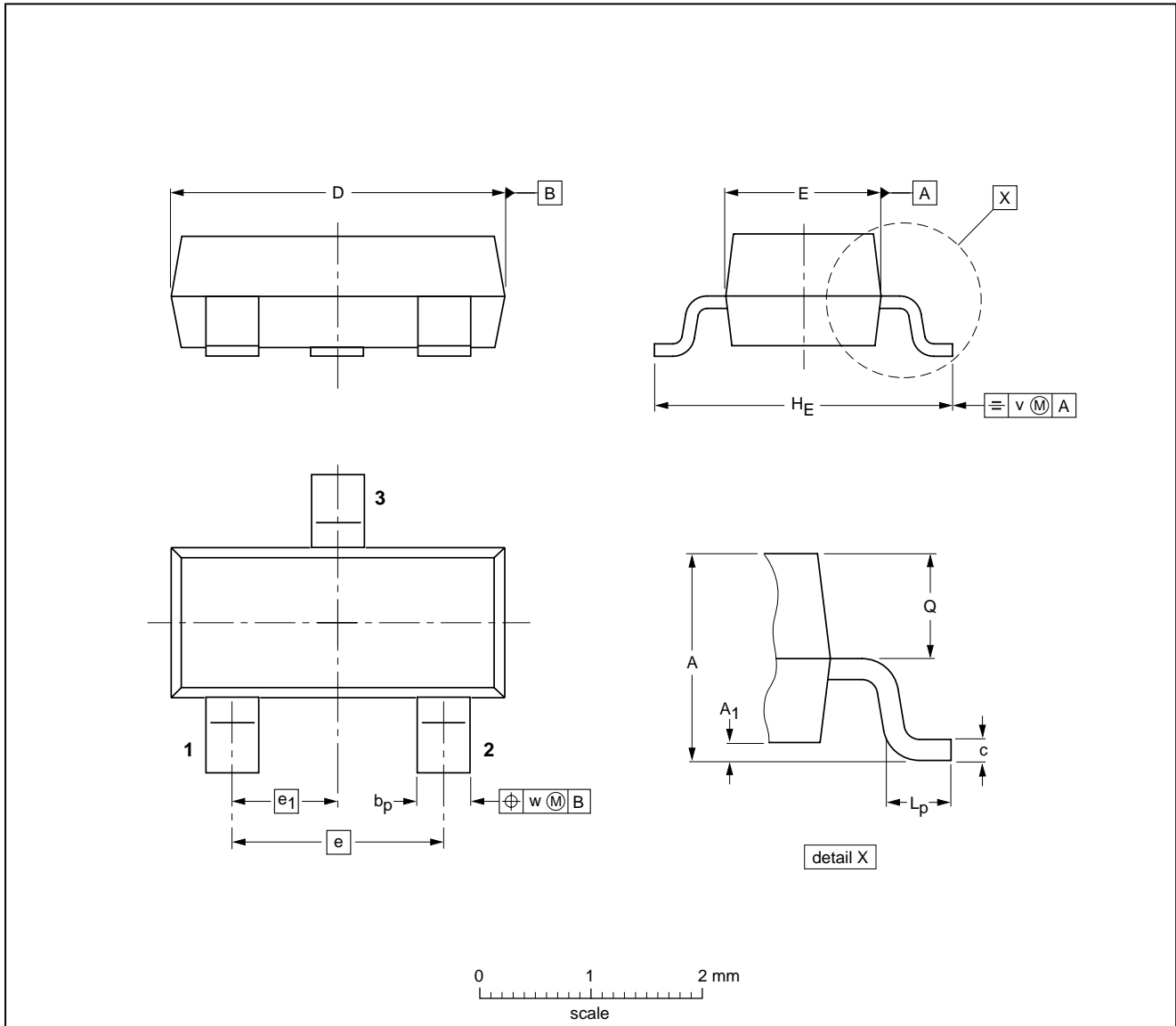
# Low-voltage avalanche regulator double diodes

## PLVA2600A series

### PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT23



DIMENSIONS (mm are the original dimensions)

UNIT	A	A <sub>1</sub> max.	b <sub>p</sub>	c	D	E	e	e <sub>1</sub>	H <sub>E</sub>	L <sub>p</sub>	Q	v	w
mm	1.1 0.9	0.1	0.48 0.38	0.15 0.09	3.0 2.8	1.4 1.2	1.9	0.95	2.5 2.1	0.45 0.15	0.55 0.45	0.2	0.1

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT23						97-02-28

# Low-voltage avalanche regulator double diodes

## PLVA2600A series

### DEFINITIONS

<b>Data sheet status</b>	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
<b>Limiting values</b>	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
<b>Application information</b>	
Where application information is given, it is advisory and does not form part of the specification.	

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# Philips Semiconductors – a worldwide company

**Argentina:** see South America

**Australia:** 34 Waterloo Road, NORTH RYDE, NSW 2113,  
Tel. +61 2 9805 4455, Fax. +61 2 9805 4466

**Austria:** Computerstr. 6, A-1101 WIEN, P.O. Box 213,  
Tel. +43 1 60 101 1248, Fax. +43 1 60 101 1210

**Belarus:** Hotel Minsk Business Center, Bld. 3, r. 1211, Volodarski Str. 6,  
220050 MINSK, Tel. +375 172 20 0733, Fax. +375 172 20 0773

**Belgium:** see The Netherlands

**Brazil:** see South America

**Bulgaria:** Philips Bulgaria Ltd., Energoproject, 15th floor,  
51 James Bourchier Blvd., 1407 SOFIA,  
Tel. +359 2 68 9211, Fax. +359 2 68 9102

**Canada:** PHILIPS SEMICONDUCTORS/COMPONENTS,  
Tel. +1 800 234 7381, Fax. +1 800 943 0087

**China/Hong Kong:** 501 Hong Kong Industrial Technology Centre,  
72 Tat Chee Avenue, Kowloon Tong, HONG KONG,  
Tel. +852 2319 7888, Fax. +852 2319 7700

**Colombia:** see South America

**Czech Republic:** see Austria

**Denmark:** Sydhavnsgade 23, 1780 COPENHAGEN V,  
Tel. +45 33 29 3333, Fax. +45 33 29 3905

**Finland:** Sinikalliontie 3, FIN-02630 ESPOO,  
Tel. +358 9 615 800, Fax. +358 9 6158 0920

**France:** 51 Rue Carnot, BP317, 92156 SURESNES Cedex,  
Tel. +33 1 4099 6161, Fax. +33 1 4099 6427

**Germany:** Hammerbrookstraße 69, D-20097 HAMBURG,  
Tel. +49 40 2353 60, Fax. +49 40 2353 6300

**Hungary:** see Austria

**India:** Philips INDIA Ltd, Band Box Building, 2nd floor,  
254-D, Dr. Annie Besant Road, Worli, MUMBAI 400 025,  
Tel. +91 22 493 8541, Fax. +91 22 493 0966

**Indonesia:** PT Philips Development Corporation, Semiconductors Division,  
Gedung Philips, Jl. Buncit Raya Kav.99-100, JAKARTA 12510,  
Tel. +62 21 794 0040 ext. 2501, Fax. +62 21 794 0080

**Ireland:** Newstead, Clonskeagh, DUBLIN 14,  
Tel. +353 1 7640 000, Fax. +353 1 7640 200

**Israel:** RAPAC Electronics, 7 Kehilat Saloniki St, PO Box 18053,  
TEL AVIV 61180, Tel. +972 3 645 0444, Fax. +972 3 649 1007

**Italy:** PHILIPS SEMICONDUCTORS, Piazza IV Novembre 3,  
20124 MILANO, Tel. +39 02 67 52 2531, Fax. +39 02 67 52 2557

**Japan:** Philips Bldg 13-37, Kohnan 2-chome, Minato-ku,  
TOKYO 108-8507, Tel. +81 3 3740 5130, Fax. +81 3 3740 5077

**Korea:** Philips House, 260-199 Itaewon-dong, Yongsan-ku, SEOUL,  
Tel. +82 2 709 1412, Fax. +82 2 709 1415

**Malaysia:** No. 76 Jalan Universiti, 46200 PETALING JAYA, SELANGOR,  
Tel. +60 3 750 5214, Fax. +60 3 757 4880

**Mexico:** 5900 Gateway East, Suite 200, EL PASO, TEXAS 79905,  
Tel. +9-5 800 234 7381, Fax +9-5 800 943 0087

**Middle East:** see Italy

**Netherlands:** Postbus 90050, 5600 PB EINDHOVEN, Bldg. VB,  
Tel. +31 40 27 82785, Fax. +31 40 27 88399

**New Zealand:** 2 Wagener Place, C.P.O. Box 1041, AUCKLAND,  
Tel. +64 9 849 4160, Fax. +64 9 849 7811

**Norway:** Box 1, Manglerud 0612, OSLO,  
Tel. +47 22 74 8000, Fax. +47 22 74 8341

**Pakistan:** see Singapore

**Philippines:** Philips Semiconductors Philippines Inc.,  
106 Valero St. Salcedo Village, P.O. Box 2108 MCC, MAKATI,  
Metro MANILA, Tel. +63 2 816 6380, Fax. +63 2 817 3474

**Poland:** Ul. Lukiska 10, PL 04-123 WARSZAWA,  
Tel. +48 22 612 2831, Fax. +48 22 612 2327

**Portugal:** see Spain

**Romania:** see Italy

**Russia:** Philips Russia, Ul. Usatcheva 35A, 119048 MOSCOW,  
Tel. +7 095 755 6918, Fax. +7 095 755 6919

**Singapore:** Lorong 1, Toa Payoh, SINGAPORE 319762,  
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**Slovakia:** see Austria

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**South Africa:** S.A. PHILIPS Pty Ltd., 195-215 Main Road Martindale,  
2092 JOHANNESBURG, P.O. Box 58088 Newville 2114,  
Tel. +27 11 471 5401, Fax. +27 11 471 5398

**South America:** Al. Vicente Pinzon, 173, 6th floor,  
04547-130 SÃO PAULO, SP, Brazil,  
Tel. +55 11 821 2333, Fax. +55 11 821 2382

**Spain:** Balmes 22, 08007 BARCELONA,  
Tel. +34 93 301 6312, Fax. +34 93 301 4107

**Sweden:** Kottbygatan 7, Akalla, S-16485 STOCKHOLM,  
Tel. +46 8 5985 2000, Fax. +46 8 5985 2745

**Switzerland:** Allmendstrasse 140, CH-8027 ZÜRICH,  
Tel. +41 1 488 2741 Fax. +41 1 488 3263

**Taiwan:** Philips Semiconductors, 6F, No. 96, Chien Kuo N. Rd., Sec. 1,  
TAIPEI, Taiwan Tel. +886 2 2134 2886, Fax. +886 2 2134 2874

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209/2 Sanpavuth-Bangna Road Prakanong, BANGKOK 10260,  
Tel. +66 2 745 4090, Fax. +66 2 398 0793

**Turkey:** Yukari Dudullu, Org. San. Blg., 2.Cad. Nr. 28 81260 Umraniye,  
ISTANBUL, Tel. +90 216 522 1500, Fax. +90 216 522 1813

**Ukraine:** PHILIPS UKRAINE, 4 Patrice Lumumba str., Building B, Floor 7,  
252042 KIEV, Tel. +380 44 264 2776, Fax. +380 44 268 0461

**United Kingdom:** Philips Semiconductors Ltd., 276 Bath Road, Hayes,  
MIDDLESEX UB3 5BX, Tel. +44 181 730 5000, Fax. +44 181 754 8421

**United States:** 811 East Arques Avenue, SUNNYVALE, CA 94088-3409,  
Tel. +1 800 234 7381, Fax. +1 800 943 0087

**Uruguay:** see South America

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**Yugoslavia:** PHILIPS, Trg N. Pasica 5/v, 11000 BEOGRAD,  
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International Marketing & Sales Communications, Building BE-p, P.O. Box 218,  
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**Internet:** <http://www.semiconductors.philips.com>

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Printed in The Netherlands

115002/00/03/pp8

Date of release: 1999 May 10

Document order number: 9397 750 05919

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