

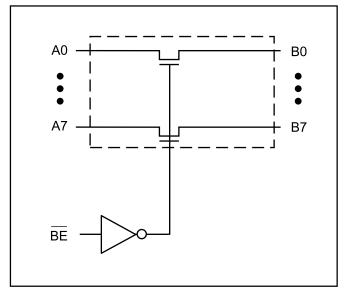
8-Bit, 2-Port, Bus Switch



Product Features

- Near zero propagation delay
- 5Ω switches connect inputs to outputs
- High Bandwidth Operation (>400 MHz)
- · Permits Hot Insertion
- 5V I/O Tolerant
- Rail-to-Rail 3.3 V or 2.5 V Switching
- 2.5V Supply Voltage Operation
- · Packages available:
 - -20-pin 150 mil wide plastic QSOP (Q)
 - -20-pin 173 mil wide plastic TSSOP (L)

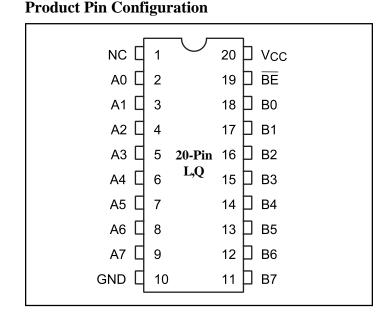
Logic Block Diagram



achieving industry leading performance.

signals that have high bandwidth (>400 MHz).

Product Description



2.5V/3.3V, High Bandwidth, Hot Insertion

Pericom Semiconductor's PI3C series of logic circuits are produced

using the Company's advanced submicron CMOS technology,

The PI3C3245 is a 2.5V or 3.3V 8-bit, 2-port bus switch designed

with a low ON resistance (5 Ω) allowing inputs to be connected

directly to outputs. The bus switch creates no additional propagational

delay or additional ground bounce noise. The switches are turned

ON by the Bus Enable (BE) input signal. It is very useful in switching

Truth Table(1)

Function	BE	A 0–7
Disconnect	Н	Hi-Z
Connect	L	B0-7

Note:

1. H = High Voltage Level Low Voltage Level L Hi-Z = High Impedance

Product Pin Description

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Pin Name	Description
BE	Bus Enable Input (Active LOW)
A0-7	Bus A
B0-7	Bus B
GND	Ground
V_{CC}	Power

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Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature65°C to +150°C
Ambient Temperature with Power Applied40°C to +85°C
Supply Voltage to Ground Potential (Inputs & V _{CC} Only)0.5Vto+4.6V
Supply Voltage to Ground Potential (Outputs & D/O Only)0.5V to+4.6V
DC Input Voltage0.5V to +5.5V
DC Output Current
Power Dissipation

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over Operating Range, $T_A = -40$ °C to +85°C, $V_{CC} = 3.3$ V ± 10 %)

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ. ⁽²⁾	Max.	Units
V _{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level	2.0			V
V _{IL}	Input LOW Voltage	Guaranteed Logic LOW Level	-0.5		0.8	, v
I _{IH}	Input HIGH Current	$V_{CC} = Max., V_{IN} = V_{CC}$			±1	
I _{IL}	Input LOW Current	$V_{CC} = Max., V_{IN} = GND$			±1	μΑ
I _{OZH}	High Impedance Output Current	$0 \le A, B \le V_{CC}$			±1	
V _{IK}	Clamp Diode Voltage	$V_{CC} = Min., I_{IN} = -18mA$	-0.73	-1.2		V
R _{ON}	Switch On Resistance ⁽⁴⁾	$V_{CC} = Min., V_{IN} = 0.0V, I_{ON} = 48mA$ $V_{CC} = Min., V_{IN} = 2.4V, I_{ON} = 15mA$		5 8	7 15	Ω

Capacitance $(T_A = 25^{\circ}C, f = 1 \text{ MHz})$

Parameters ⁽⁵⁾	Description	Test Conditions	Тур.	Units
C _{IN}	Input Capacitance	$V_{IN} = 0V$	3.5	
C _{OFF}	A/B Capacitance, Switch Off	$V_{IN} = 0V$	5.0	pF
C _{ON}	A/B Capacitance, Switch On	$V_{IN} = 0V$	10.0	

Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- 2. Typical values are at $V_{CC} = 3.3V$, $T_A = 25^{\circ}C$ ambient and maximum loading.
- 3. Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
- 4. Measured by the voltage drop between A and B pin at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A,B) pins.

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5. This parameter is determined by device characterization but is not production tested.

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Power Supply Characteristics

Parameters	Description	Test Conditions ⁽¹⁾		Min.	Typ. ⁽²⁾	Max.	Units
I _{CC}	Quiescent Power Supply Current	V _{CC} = Max.	$V_{IN} = GND \text{ or } V_{CC}$		260	500	μA
ΔI _{CC}	Supply Current per Input HIGH	V _{CC} = Max.	$V_{IN} = 3.0V^{(3)}$			750	•

Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
- 2. Typical values are at $V_{CC} = 3.3V$, $+25^{\circ}C$ ambient.
- 3. Per driven input (control input only); A and B pins do not contribute to ΔI_{CC} .

Switching Characteristics over 3.3V Operating Range

			0		
			PI3C	3245	
			Co	m.	
Parameters	Description	Conditions ⁽¹⁾	Min.	Max.	Units
t _{PLH}	Propagation Delay ^(2,3)	$C_L = 50 pF,$ $R_L = 500 \Omega$		0.25	
t _{PHL}	Ax to Bx	$R_L = 500\Omega$			
t _{PZH}	Bus Enable Time		1.5	6.5	ns
tpZL	BE to Ax or Bx	$C_L = 50pF$,			
t _{PHZ}	Bus Disable Time	$R_L = 500\Omega$,	1.5	5.5	
tPLZ	BE to Ax or Bx	$R = 500\Omega$			

Switching Characteristics over 2.5V Operating Range

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			Com.		
Parameters	Description	Conditions ⁽¹⁾	Min.	Max.	Units
tpLH	Propagation Delay ^(2,3)	$C_L = 50 pF,$ $R_L = 500 \Omega$		0.25	
$t_{ m PHL}$	Ax to Bx	$R_L = 500\Omega$			
tpZH	Bus Enable Time		1.5	9.8	ns
tpzL	BE to Ax or Bx	$C_L = 50 pF$,			
tpHZ	Bus Disable Time	$R_L = 500\Omega$,	1.5	8.3	
tPLZ	BE to Ax or Bx	$R = 500\Omega$			

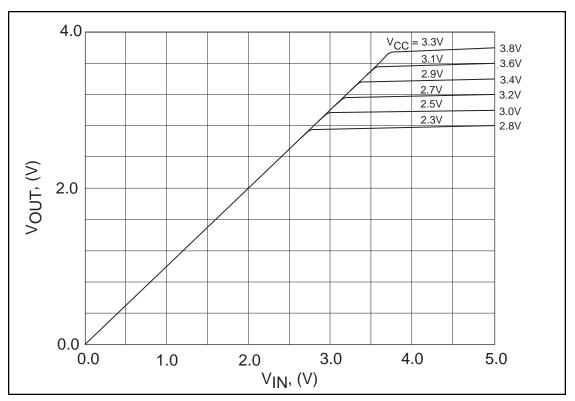
Notes:

- 1. See test circuit and waveforms.
- 2. This parameter is guaranteed but not tested on Propagation Delays.
- 3. The bus switch contributes no propagational delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

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Output Voltage vs. Input Voltage over Various Supply Voltages

Ordering Information

Part	Pin-Package	Temperature
PI3C3245L	20-TSSOP (L)	-40°C to +85°C
PI3C3245Q	20-QSOP (Q)	-40°C to +85°C