Trench Small Signal MOSFET

8 V, Dual P-Channel, SC-88 ESD Protection

Features

- Leading –8 V Trench for Low R_{DS(ON)} Performance
- ESD Protected Gate
- Small Footprint (2 x 2 mm)
- Same Package as SC-70-6
- Pb–Free Package May be Available. The G–Suffix Denotes a Pb–Free Lead Finish

Applications

- Load Power switching
- DC-DC Conversion
- Li-Ion Battery Charging Circuits
- Cell Phones, Media Players, Digital Cameras, PDAs

MAXIMUM RATINGS (T_J = 25°C unless otherwise stated)

Param	Symbol	Value	Unit		
Drain-to-Source Voltage			V _{DSS}	-8.0	V
Gate-to-Source Voltage			V_{GS}	±8.0	V
Continuous Drain Current	Steady State	T _A = 25 °C	I _D	-0.775	Α
(Based on R _{θJA})	State	T _A = 85 °C		-0.558	
Power Dissipation	Steady	T _A = 25 °C	P _D	0.27	W
(Based on R _{θJA})	State	T _A = 85 °C		0.14	
Continuous Drain Current	Steady State	T _A = 25 °C	I _D	-1.1	Α
(Based on R _{θJL})	State	T _A = 85 °C		-0.8	
Power Dissipation	Steady	T _A = 25 °C		0.55	W
(Based on R _{θJL})	State	T _A = 85 °C	P _D	0.29	
Pulsed Drain Current t ≤10 μs			I _{DM}	±1.2	Α
Operating Junction and Storage Temperature			T _J , T _{STG}	–55 to 150	့ပ
Continuous Source Current (Body Diode)			Is	-0.775	Α
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)			TL	260	°C

THERMAL RESISTANCE RATINGS (Note 1)

Parameter	Symbol	Тур	Max	Unit
Junction-to-Ambient - Steady State	$R_{\theta JA}$	400	460	°C/W
Junction-to-Lead (Drain) - Steady State	$R_{\theta JL}$	194	226	

1. Surface mounted on FR4 board using 1 oz Cu area = 0.9523 in sq.

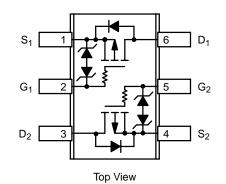


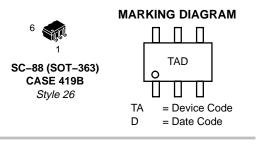
ON Semiconductor®

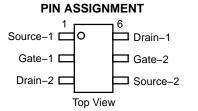
http://onsemi.com

V _{(BR)DSS}	R _{DS(on)} TYP	I _D Max
	0.22 Ω @ -4.5 V	
-8 V	0.32 Ω @ -2.5 V	-0.775 A
	0.51 Ω @ –1.8 V	

SOT-363 SC-88 (6 LEADS)







ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

ELECTRICAL CHARACTERISTICS (T_J =25°C unless otherwise stated)

Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS							
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$		-8.0	-10.5		V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V(BR)DSS/ T _J				-6.0		mV/°C
Zero Gate Voltage Drain Current	I _{DSS}	$V_{GS} = 0 \text{ V}, V_{D}$	_{OS} = -6.4 V			1.0	μΑ
Gate-to-Source Leakage Current	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8.0 \text{ V}$				10	μΑ
ON CHARACTERISTICS (Note 2)							
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}$, $ID = -250 \mu A$		-0.45	-0.83	-1.0	V
Gate Threshold Temperature Coefficient	V _{GS(TH)} /T _J				2.2		mV/ °C
Drain-to-Source On Resistance	R _{DS(on)}	$V_{GS} = -4.5 \text{ V}, I_D = -0.57 \text{ A}$			0.22	0.3	Ω
		$V_{GS} = -2.5 \text{ V, I}$	$I_D = -0.48 \text{ A}$		0.32	0.46	
		V _{GS} = -1.8 V, I	$I_D = -0.20 \text{ A}$		0.51	0.9	
Forward Transconductance	9FS	$V_{GS} = -4.0 \text{ V}, I_D = -0.57 \text{ A}$			2.0		S
CHARGES AND CAPACITANCES							
Input Capacitance	C _{ISS}	$V_{GS} = 0 \text{ V, f} = 1.0 \text{ MHz,}$ $V_{DS} = -8.0 \text{ V}$			160	225	pF
Output Capacitance	Coss				38	55	
Reverse Transfer Capacitance	C _{RSS}				28	40	
Total Gate Charge	Q _{G(TOT)}	$V_{GS} = -4.5 \text{ V, V}$ $I_{D} = -0$	$I_{DS} = -5.0 \text{ V},$		2.2	4.0	nC
Threshold Gate Charge	Q _{G(TH)}	1 _D = -0	I _D = -0.6 A		0.1		
Gate-to-Source Charge	Q _{GS}				0.5		
Gate-to-Drain Charge	Q_{GD}				0.5		
SWITCHING CHARACTERISTICS (No	ote 3)						
Turn-On Delay Time	td _(ON)	$V_{GS} = -4.5 \text{ V, V}$ $I_{D} = -0.5 \text{ A, F}$	$I_{DD} = -4.0 \text{ V},$		13		ns
Rise Time	tr	$I_D = -0.5 \text{ A}, R_G = 8.0 \Omega$			23		
Turn-Off Delay Time	td _(OFF)				50		
Fall Time	tf				36		
DRAIN-SOURCE DIODE CHARACTE	RISTICS						
Forward Diode Voltage	V _{SD}	$V_{GS} = 0 \text{ V},$ $I_{S} = -0.23 \text{ A}$	T _J = 25°C		0.76	1.1	V
		IS = -0.23 A	T _J = 125°C		0.63		
Reverse Recovery Time	t _{RR}	$V_{GS} = 0 \text{ V, dI}_{S}/\text{dt} = 100 \text{ A/}\mu\text{s,}$ $I_{S} = -0.77 \text{ A}$			78		ns

Pulse Test: pulse width ≤ 300 μs, duty cycle ≤ 2%.
 Switching characteristics are independent of operating junction temperatures.

TYPICAL PERFORMANCE CURVES (T_J = 25°C unless otherwise noted)

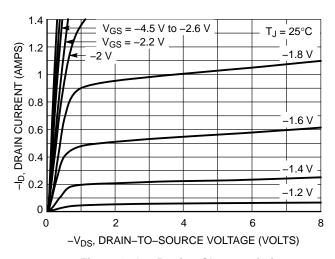


Figure 1. On-Region Characteristics

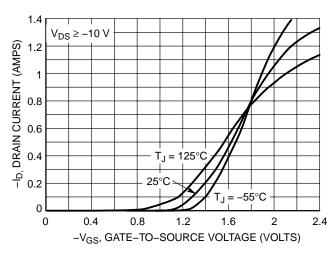


Figure 2. Transfer Characteristics

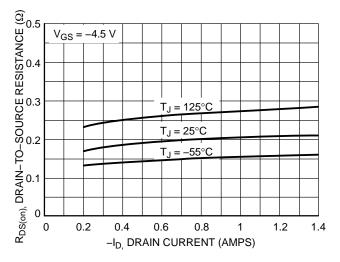


Figure 3. On–Resistance vs. Drain Current and Temperature

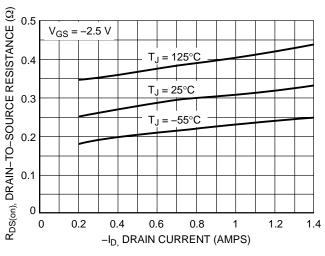


Figure 4. On–Resistance vs. Drain Current and Temperature

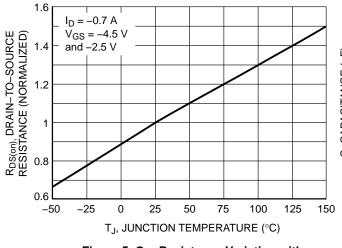


Figure 5. On–Resistance Variation with Temperature

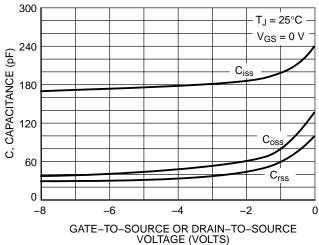


Figure 6. Capacitance Variation

TYPICAL PERFORMANCE CURVES (T_J = 25°C unless otherwise noted)

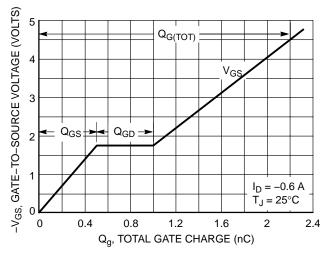


Figure 7. Gate-to-Source and Drain-to-Source Voltage vs. Total Charge

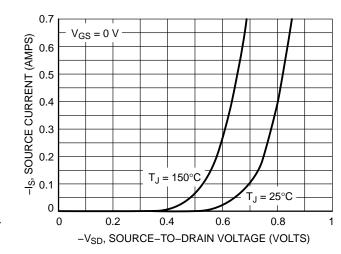


Figure 8. Diode Forward Voltage vs. Current

ORDERING INFORMATION

Device Order Number	Package Type	Tape and Reel Size [†]	
NTJD2152PT1	SOT-363	3000 / Tape & Reel	
NTJD2152PT1G	SOT-363 (Pb-Free)	3000 / Tape & Reel	
NTJD2152PT2	SOT-363	3000 / Tape & Reel	
NTJD2152PT2G	SOT-363 (Pb-Free)	3000 / Tape & Reel	
NTJD2152PT4	SOT-363	10,000 / Tape & Reel	
NTJD2152PT4G	SOT-363 (Pb-Free)	10,000 / Tape & Reel	

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

PACKAGE DIMENSIONS

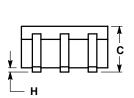
SC-88 (SOT-363) CASE 419B-02

CASE 419B-ISSUE T

NOTES:

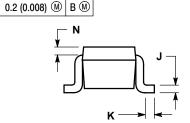
- DIMENSIONING AND TOLERANCING PER ANSI
 Y14 5M 1982
- 2. CONTROLLING DIMENSION: INCH.
- 3. 419B-01 OBSOLETE, NEW STANDARD 419B-02.

	INC	HES	MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.071	0.087	1.80	2.20	
В	0.045	0.053	1.15	1.35	
U	0.031	0.043	0.80	1.10	
D	0.004	0.012	0.10	0.30	
G	0.026 BSC		0.65 BSC		
Н		0.004		0.10	
J	0.004	0.010	0.10	0.25	
K	0.004	0.012	0.10	0.30	
N	0.008 REF		0.20 REF		
S	0.079	0.087	2.00	2.20	

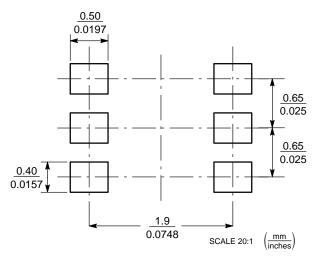


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S



SOLDERING FOOTPRINT*



SC-88/SC70-6

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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