

NCP582

Low Noise 150 mA CMOS LDO Regulator with Enable

The NCP582 series of low dropout regulators are designed for portable battery powered applications which require precise output voltage accuracy, low supply current, and high ripple rejection. These devices feature an enable function and are offered in active low and active high with auto discharge.

The following ceramic capacitors are the recommended values to be used with these devices; for $V_{out} < 2.5$ V, $C_{in} = C_{out} = 1.0 \mu F$, $V_{out} \geq 2.5$ V, $C_{in} = C_{out} = 0.47 \mu F$.

Features

- Ultra-Low Dropout Voltage of 220 mV at 150 mA
- Low Output Noise of 30 μV_{rms}
- Excellent Line and Load Regulation
- High Output Voltage Accuracy of $\pm 2\%$
- Low Supply Current of 75 μA
- Excellent Power Supply Rejection Ratio
- Wide Operating Voltage of 1.5 V to 3.3 V
- Low Quiescent Current of 0.1 μA
- Fast Dynamic Performance
- Fold Back Protection Circuit
- Low Temperature Drift Coefficient on the Output Voltage
- These are Pb-Free Devices

Typical Applications

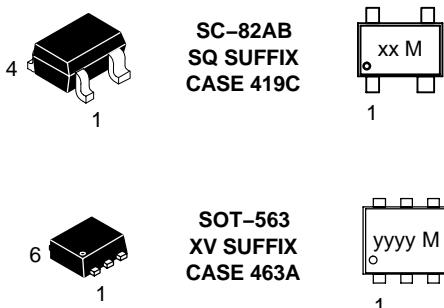
- Portable Equipment
- Hand-Held Instrumentation
- Camcorders and Cameras



ON Semiconductor®

<http://onsemi.com>

MARKING DIAGRAMS



xx, yyyy = Specific Device Code
M = Date Code

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 12 of this data sheet.

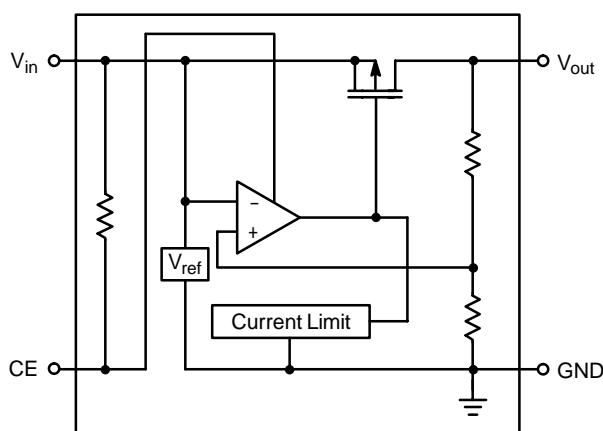


Figure 1. Simplified Block Diagram for Active Low

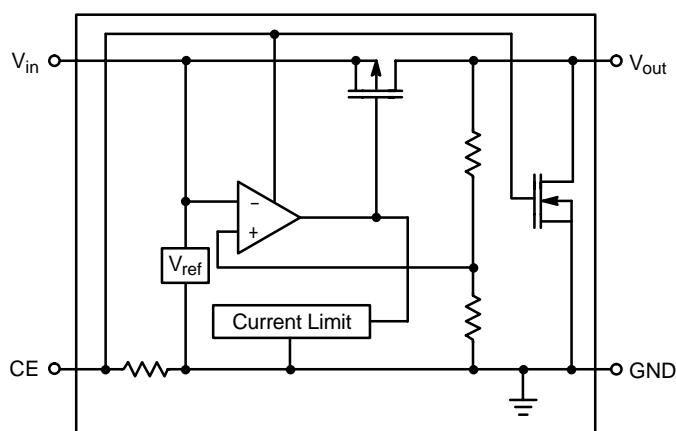


Figure 2. Simplified Block Diagram for Active High with Auto Discharge

NCP582

PIN FUNCTION DESCRIPTION

SOT-563 Pin	SC-82AB Pin	Symbol	Description
1	4	V_{in}	Power supply input voltage.
2	2	GND	Power supply ground.
3	3	V_{out}	Regulated output voltage.
4	-	NC	No connect.
5	-	GND	Power supply ground.
6	1	\overline{CE} or CE	Chip enable pin.

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Input Voltage	V_{in}	6.5	V
Input Voltage (\overline{CE} or CE Pin)	V_{CE}	-0.3 to V_{in} +0.3	V
Output Voltage	V_{out}	-0.3 to V_{in} +0.3	V
Output Current	I_{out}	200	mA
Power Dissipation SC-82AB SOT-563	P_D	150 500	mW
Operating Junction Temperature Range	T_J	-40 to +85	°C
Storage Temperature Range	T_{stg}	+150	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

NCP582

ELECTRICAL CHARACTERISTICS ($V_{in} = V_{out} + 1.0\text{ V}$, $T_A = 25^\circ\text{C}$, unless otherwise noted.)

Characteristic	Symbol	Min	Typ	Max	Unit
Input Voltage	V_{in}	2.0	—	6.0	V
Output Voltage ($I_{out} = 1.0\text{ mA}$ to 30 mA)	V_{out}	$V_{out} \times 0.980$	—	$V_{out} \times 1.020$	V
Line Regulation ($I_{out} = 30\text{ mA}$) ($V_{out} > 1.7\text{ V}$; $V_{out} + 0.5\text{ V} \leq V_{in} \leq 6.0\text{ V}$) ($V_{out} \leq 1.7\text{ V}$; $2.2\text{ V} \leq V_{in} \leq 6.0\text{ V}$)	Reg _{line}	—	0.02	0.10	%/V
Load Regulation ($I_{out} = 1.0\text{ mA}$ to 150 mA)	Reg _{load}	—	22	40	mV
Dropout Voltage ($I_{out} = 150\text{ mA}$) $V_{out} = 1.5\text{ V}$ $V_{out} = 1.8\text{ V}$ $V_{out} = 2.5\text{ V}$ $2.8\text{ V} \leq V_{out} \leq 3.3\text{ V}$	V_{DO}	— — — —	0.38 0.32 0.28 0.22	0.70 0.55 0.50 0.35	V
Power Supply Current ($I_{out} = 0\text{ mA}$)	I_{supply}	—	75	95	μA
Output Current	I_{out}	150	—	—	mA
Quiescent Current ($V_{CE} = \text{Gnd}$ for Active High with Auto Discharge) ($V_{CE} = V_{in}$ for Active Low)	I_Q	—	0.1	1.0	μA
Output Short Circuit Current ($V_{out} = 0$)	I_{lim}	—	40	—	mA
Ripple Rejection ($I_{out} = 30\text{ mA}$) ($V_{out} > 1.7\text{ V}$; $V_{in} - V_{out} = 1.0\text{ V}$) ($V_{out} \leq 1.7\text{ V}$; $V_{in} - V_{out} = 1.2\text{ V}$) $f = 1.0\text{ kHz}$ $f = 10\text{ kHz}$	RR	— —	70 60	— —	dB
Enable Input Threshold Voltage High Low	$V_{th_{enh}}$ $V_{th_{enl}}$	1.5 0	— —	V_{in} 0.3	V
Output Noise Voltage (Bandwidth = 10 Hz to 100 kHz)	V_n	—	30	—	μVRms
Output Voltage Temperature Coefficient ($I_{out} = 30\text{ mA}$, $-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$)	$\Delta V_{out}/\Delta T$	—	± 100	—	ppm°C

TYPICAL CHARACTERISTICS

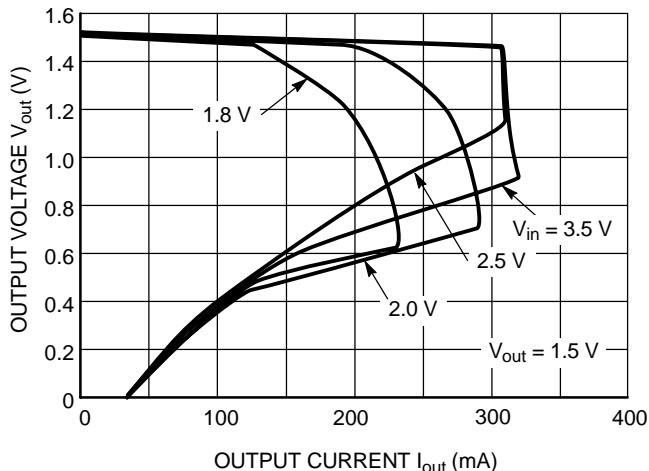


Figure 3. Output Voltage vs. Output Current

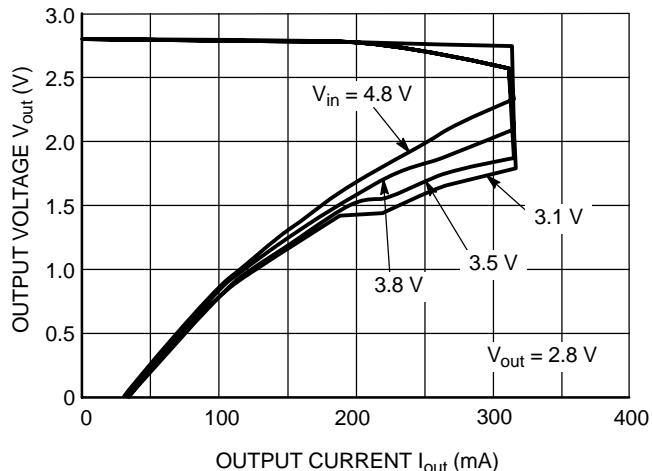


Figure 4. Output Voltage vs. Output Current

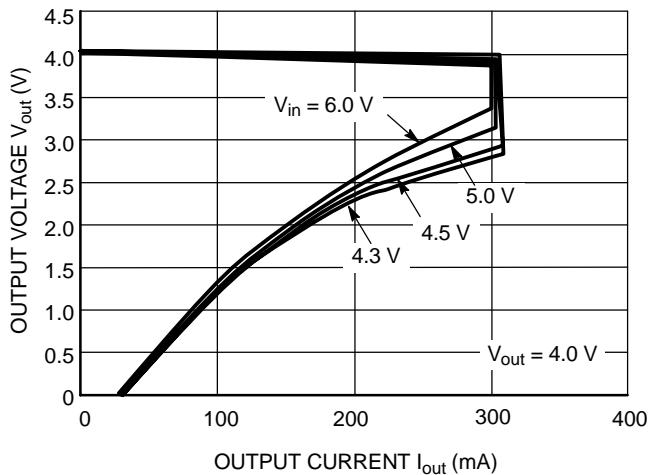


Figure 5. Output Voltage vs. Output Current

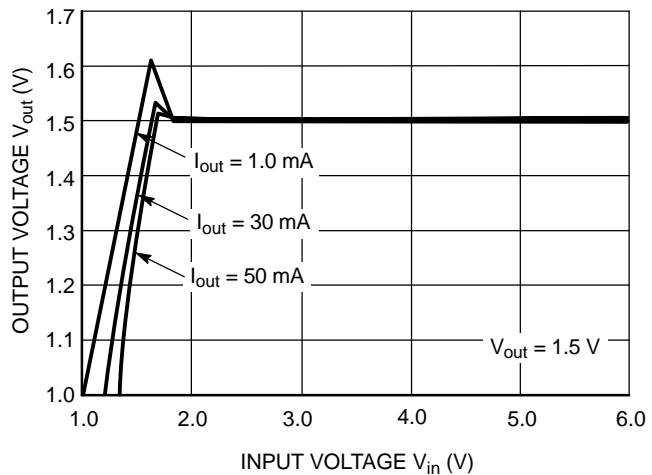


Figure 6. Output Voltage vs. Input Voltage

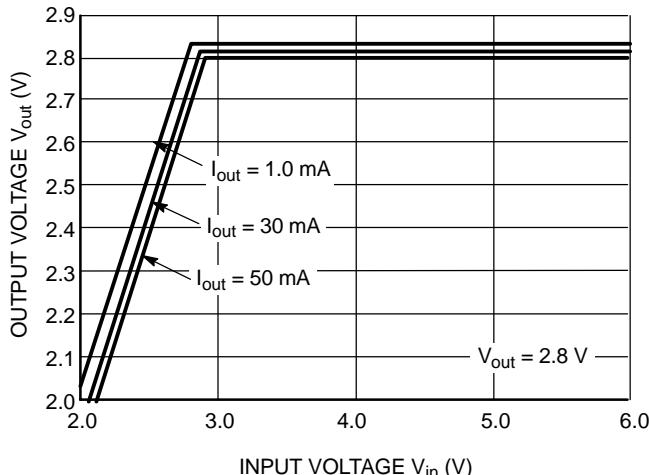


Figure 7. Output Voltage vs. Input Voltage

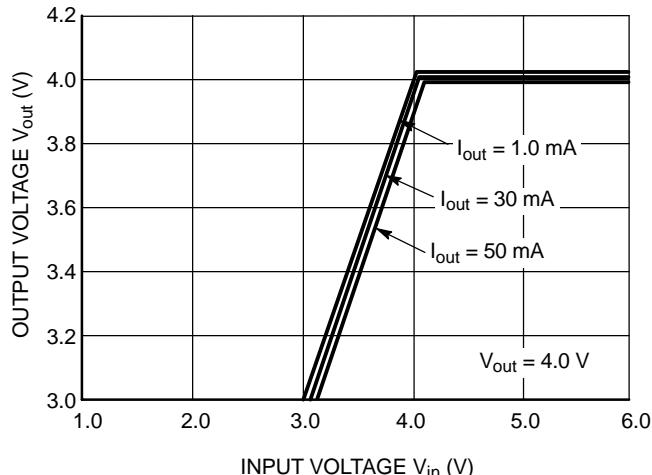
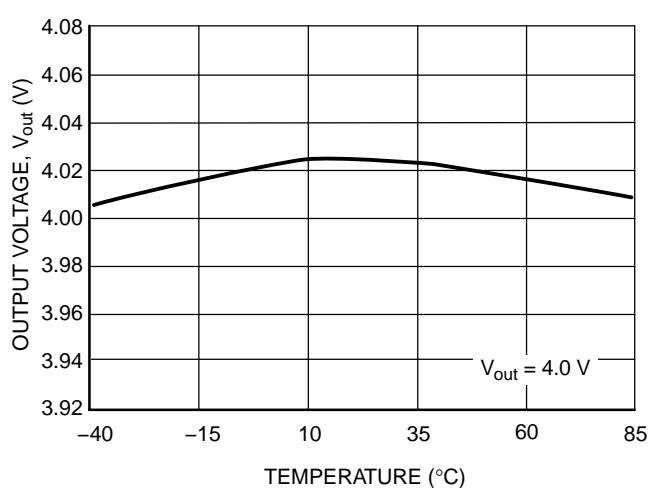
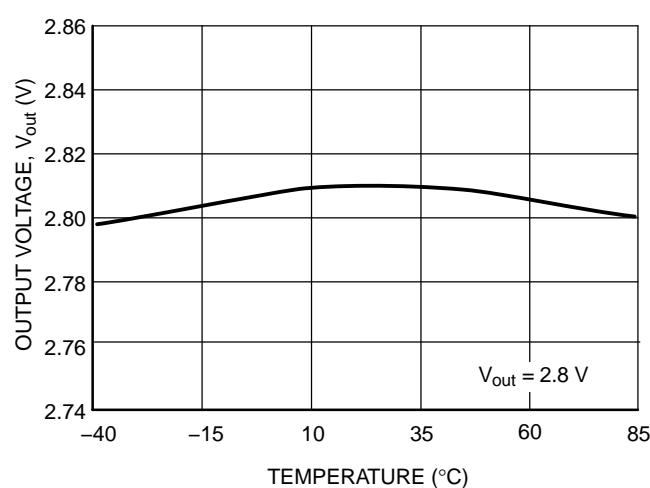
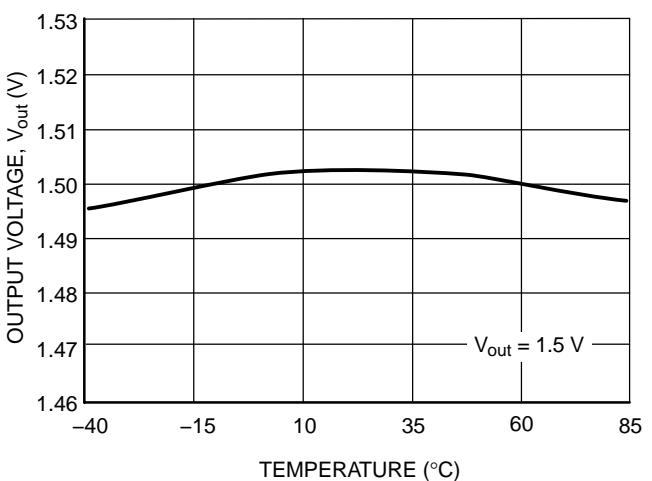
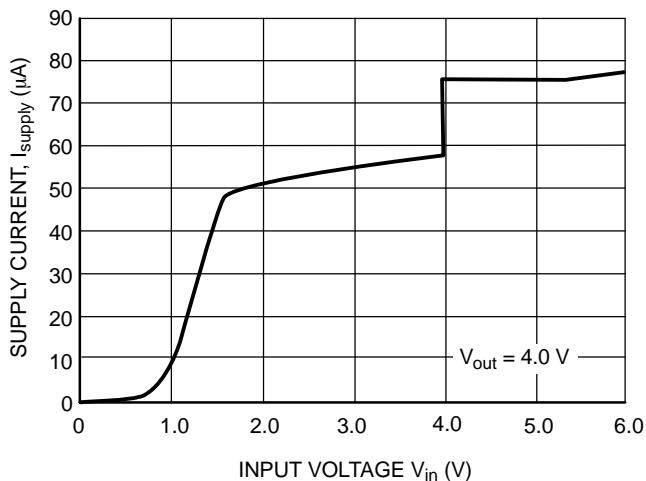
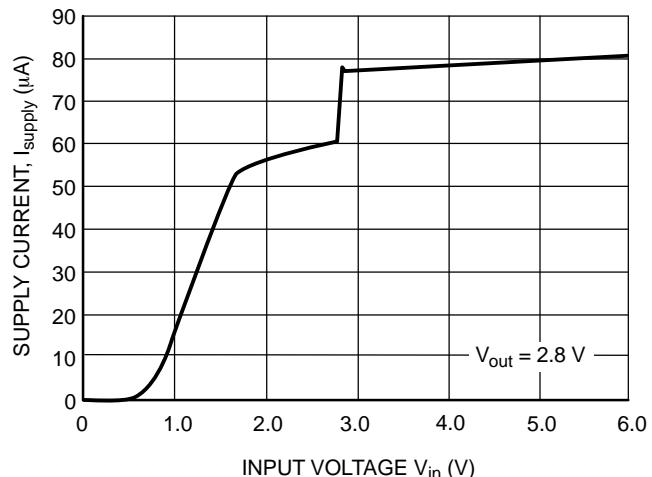
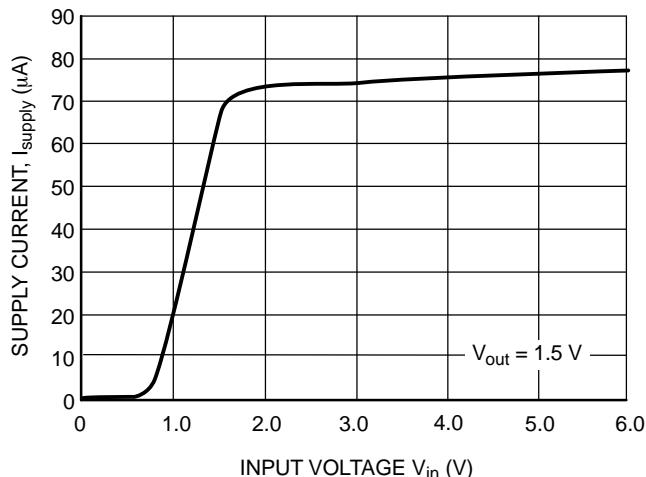


Figure 8. Output Voltage vs. Input Voltage

TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS

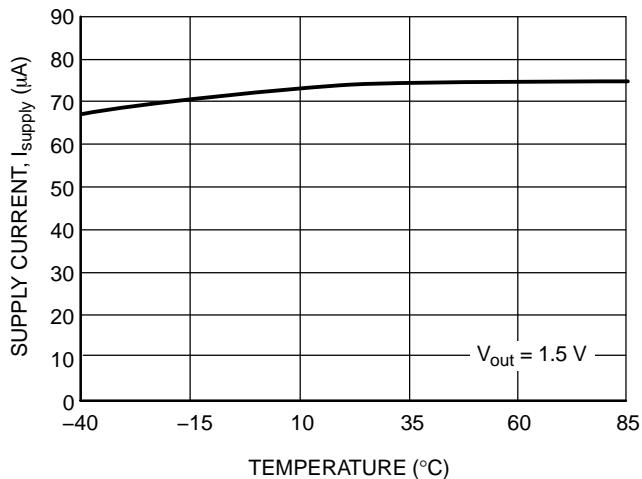


Figure 15. Power Supply Current vs. Temperature

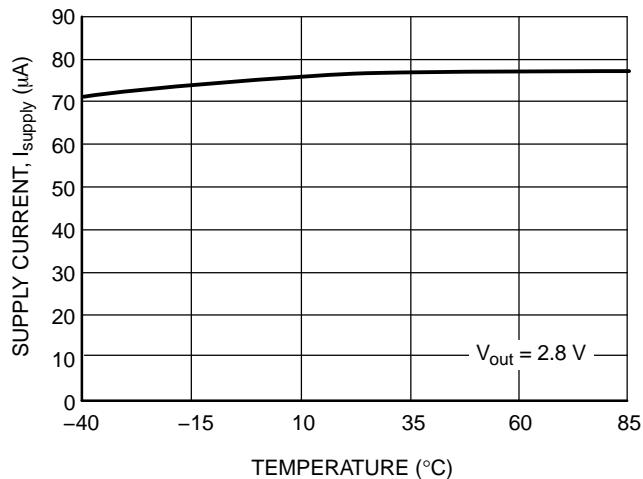


Figure 16. Power Supply Current vs. Temperature

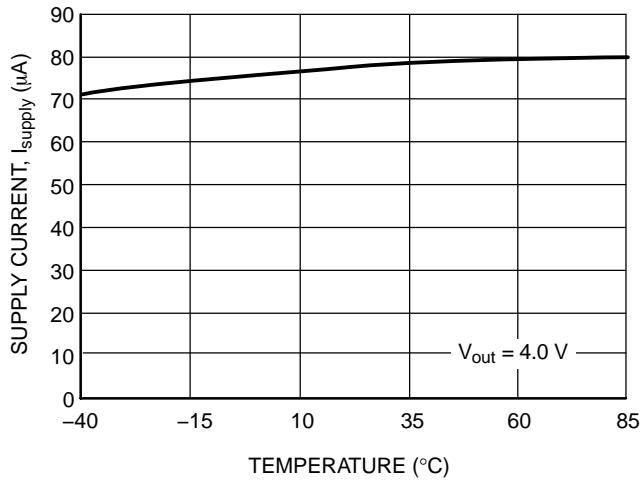


Figure 17. Power Supply Current vs. Temperature

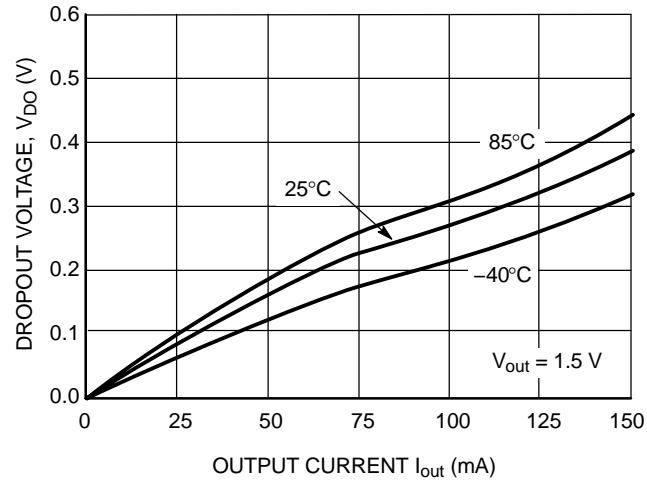


Figure 18. Dropout Voltage vs. Output Current

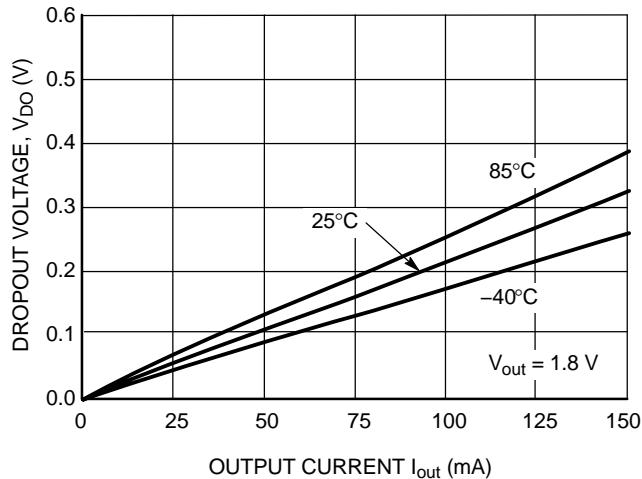


Figure 19. Dropout Voltage vs. Output Current

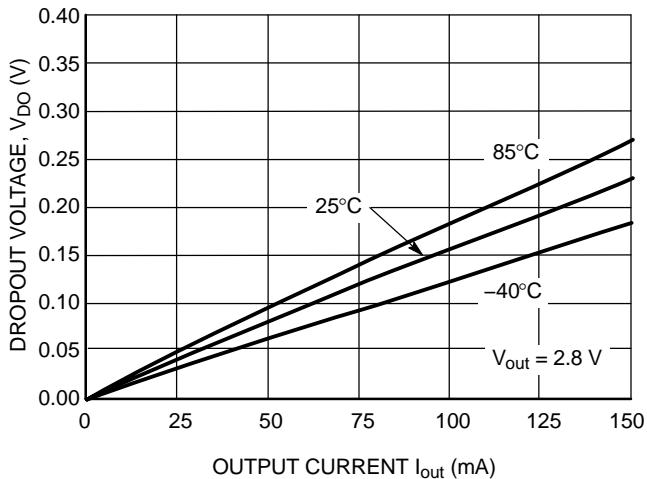


Figure 20. Dropout Voltage vs. Output Current

TYPICAL CHARACTERISTICS

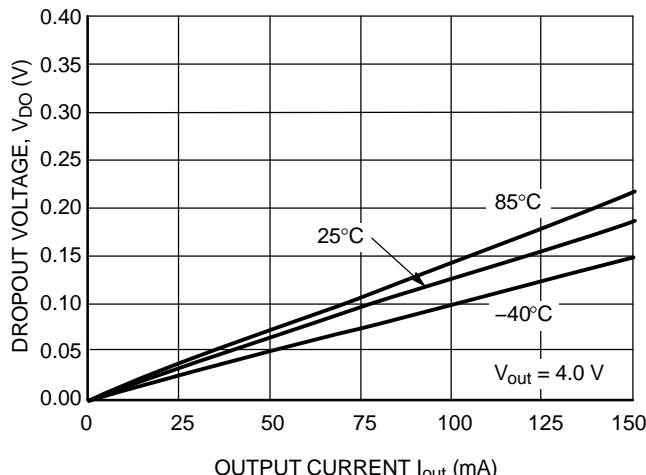


Figure 21. Dropout Voltage vs. Output Current

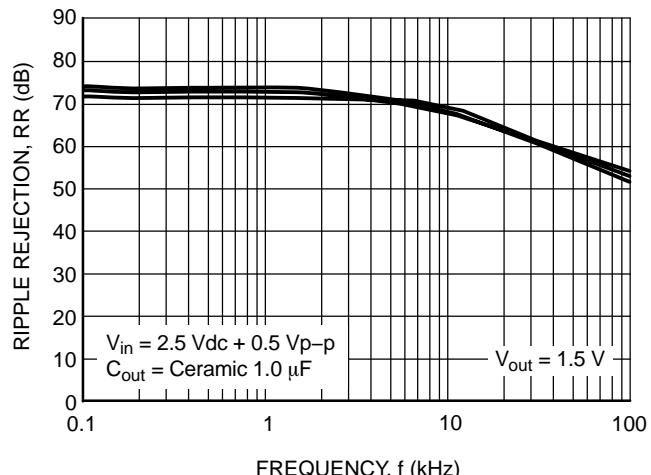


Figure 22. Ripple Rejection vs. Frequency

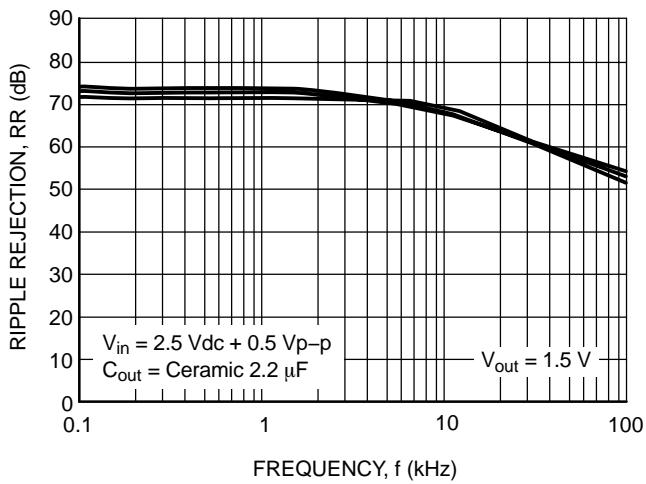


Figure 23. Ripple Rejection vs. Frequency

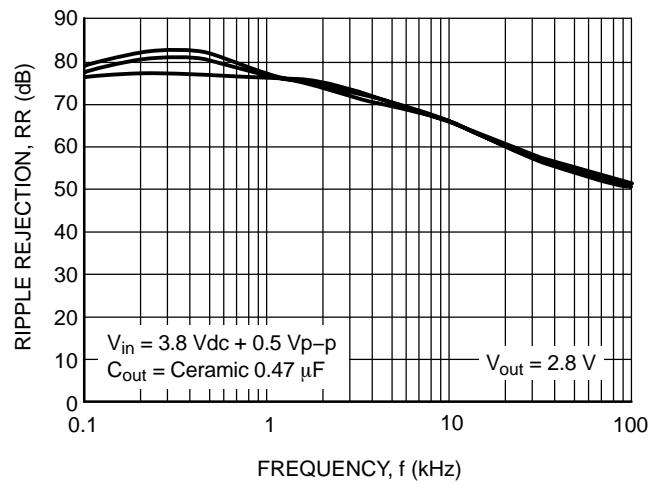


Figure 24. Ripple Rejection vs. Frequency

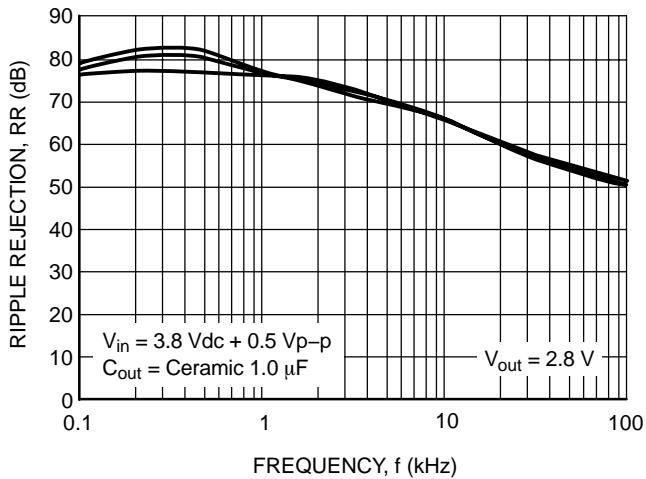


Figure 25. Ripple Rejection vs. Frequency

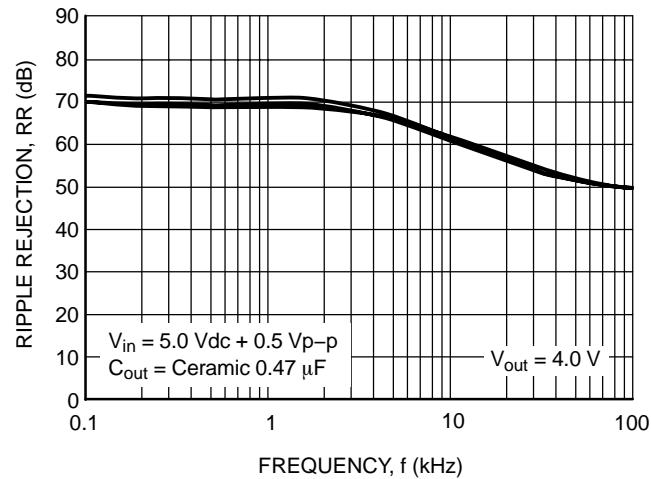


Figure 26. Ripple Rejection vs. Frequency

NCP582

TYPICAL CHARACTERISTICS

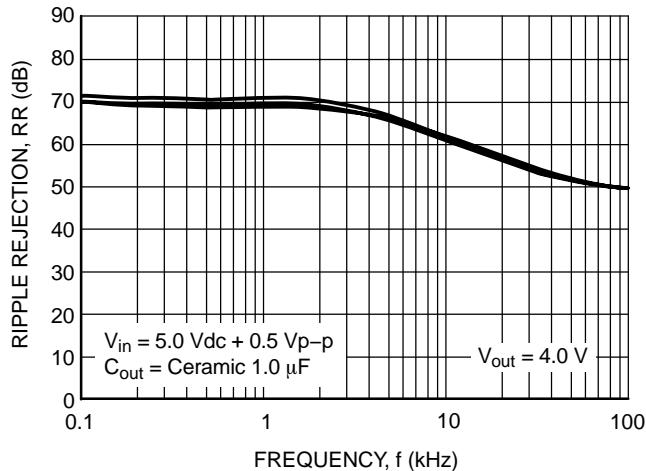


Figure 27. Ripple Rejection vs. Frequency

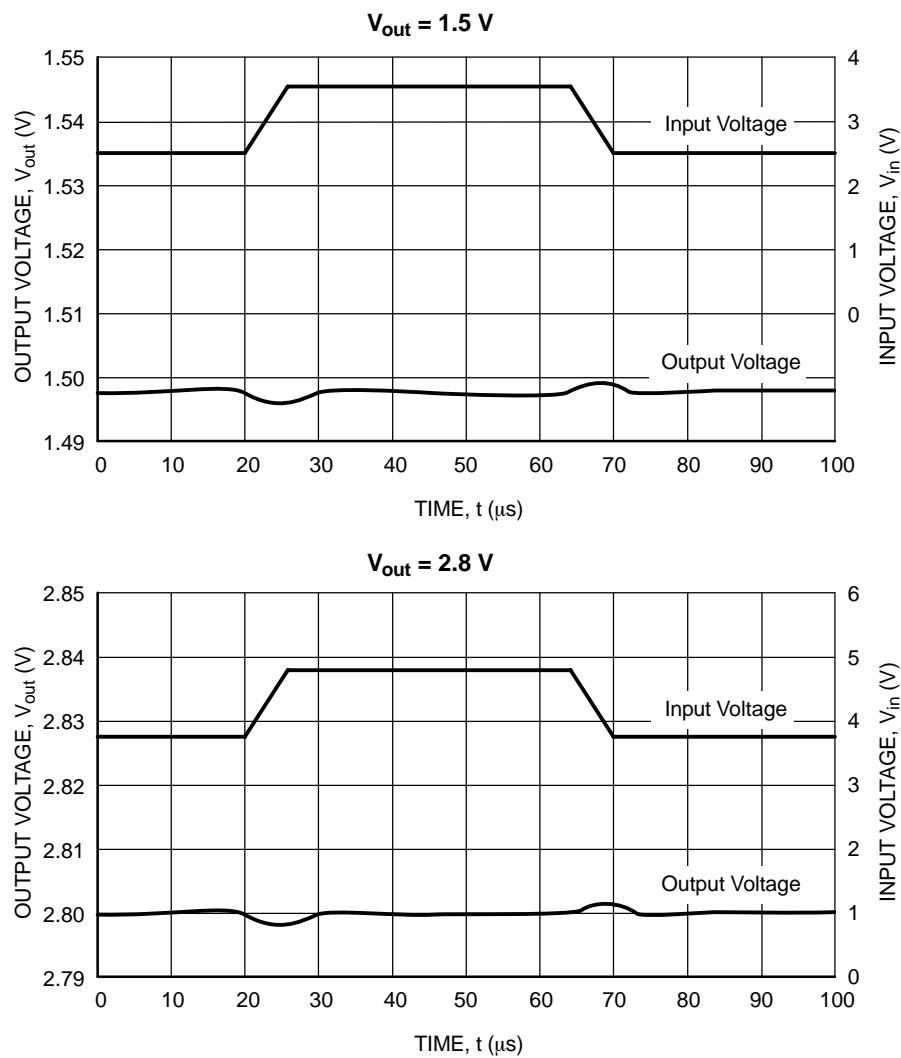


Figure 28. Input Transient Response
 $(I_{out} = 30 \text{ mA}, C_{in} = 0, t_r = t_f = 5.0 \mu\text{s}, C_{out} = 0.47 \mu\text{F})$

NCP582

TYPICAL CHARACTERISTICS

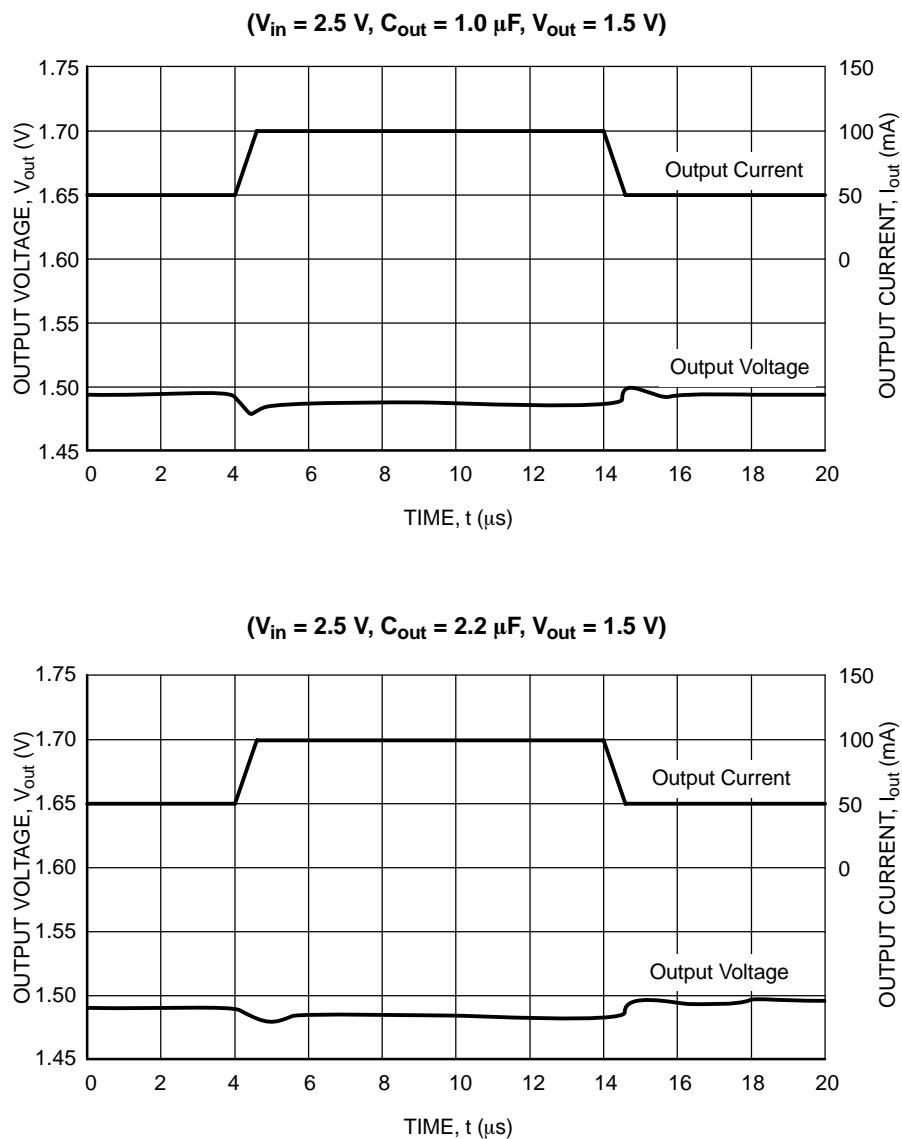
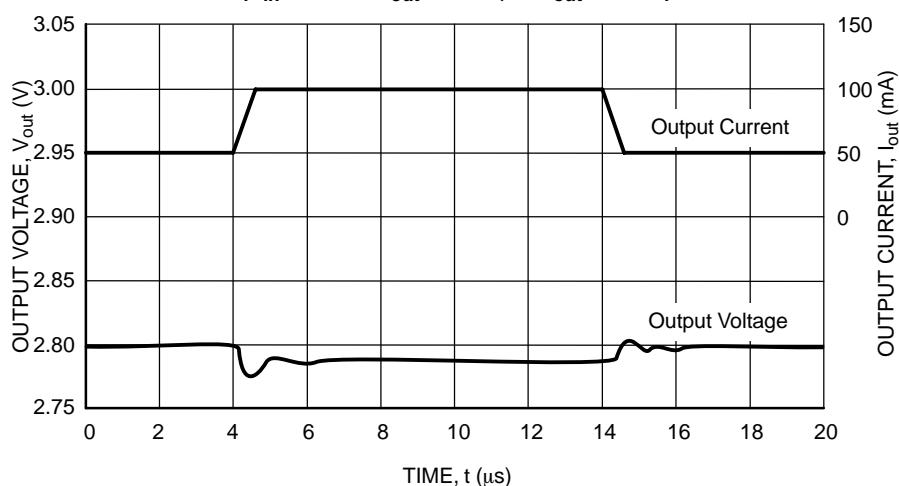


Figure 29. Load Transient Response
($tr = tf = 0.5 \mu\text{s}$, $C_{in} = 1.0 \mu\text{F}$)

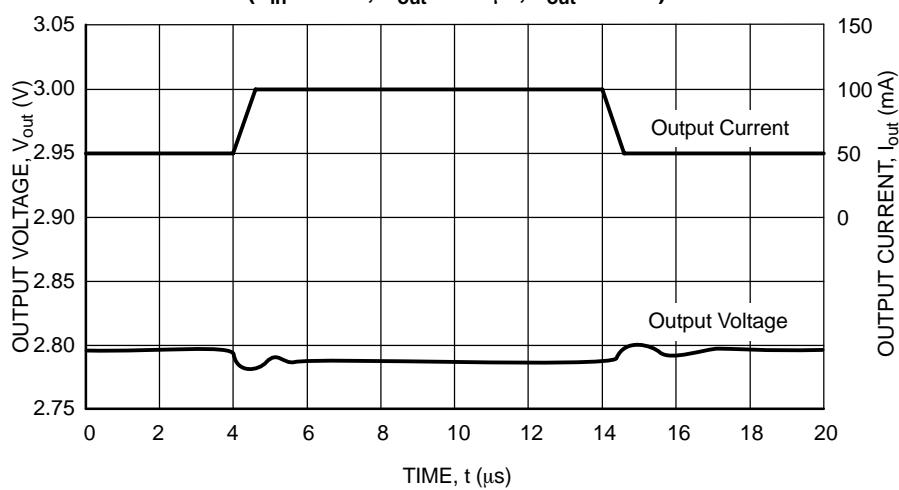
NCP582

TYPICAL CHARACTERISTICS

($V_{in} = 3.8 \text{ V}$, $C_{out} = 0.47 \mu\text{F}$, $V_{out} = 2.8 \text{ V}$)



($V_{in} = 3.8 \text{ V}$, $C_{out} = 1.0 \mu\text{F}$, $V_{out} = 2.8 \text{ V}$)



($V_{in} = 3.8 \text{ V}$, $C_{out} = 2.2 \mu\text{F}$, $V_{out} = 2.8 \text{ V}$)

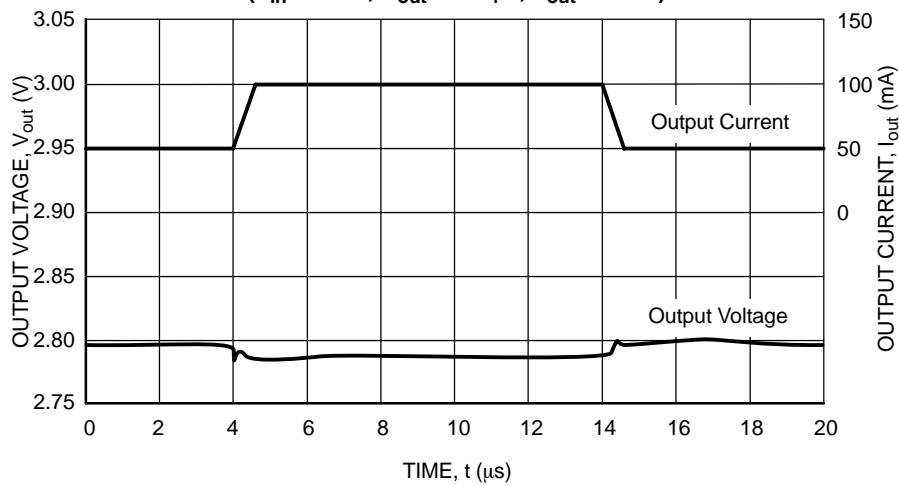


Figure 29. (continued) Load Transient Response
($t_r = t_f = 0.5 \mu\text{s}$, $C_{in} = 1.0 \mu\text{F}$)

NCP582

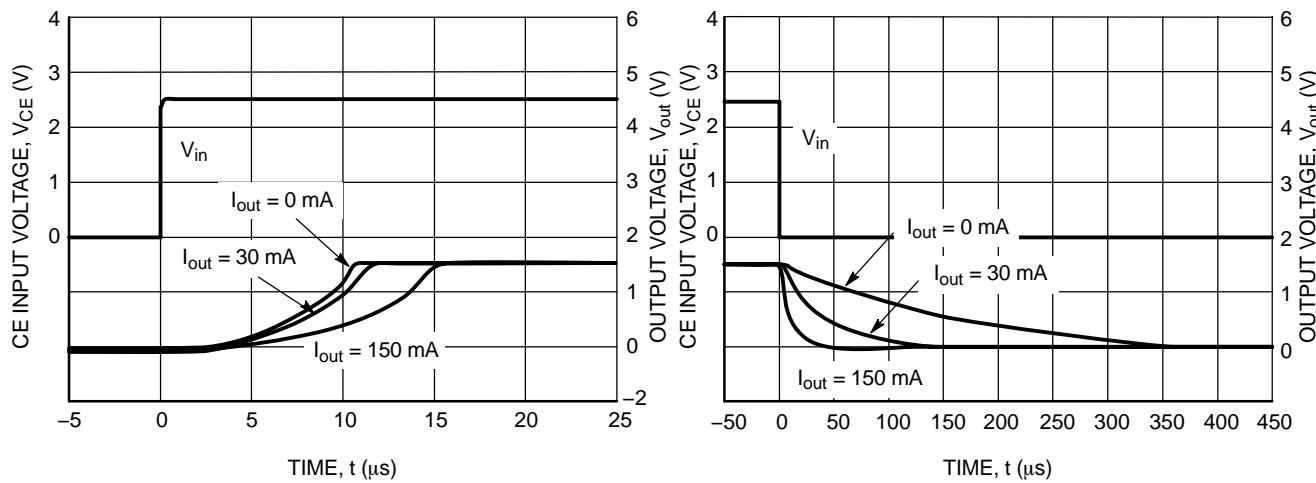


Figure 30. Turn-On/Off Speed with CE Pin (D Version)
 $(V_{out} = 1.5\text{ V}, V_{in} = 2.5\text{ V}, C_{in} = 1.0\text{ }\mu\text{F}, C_{out} = 1.0\text{ }\mu\text{F})$

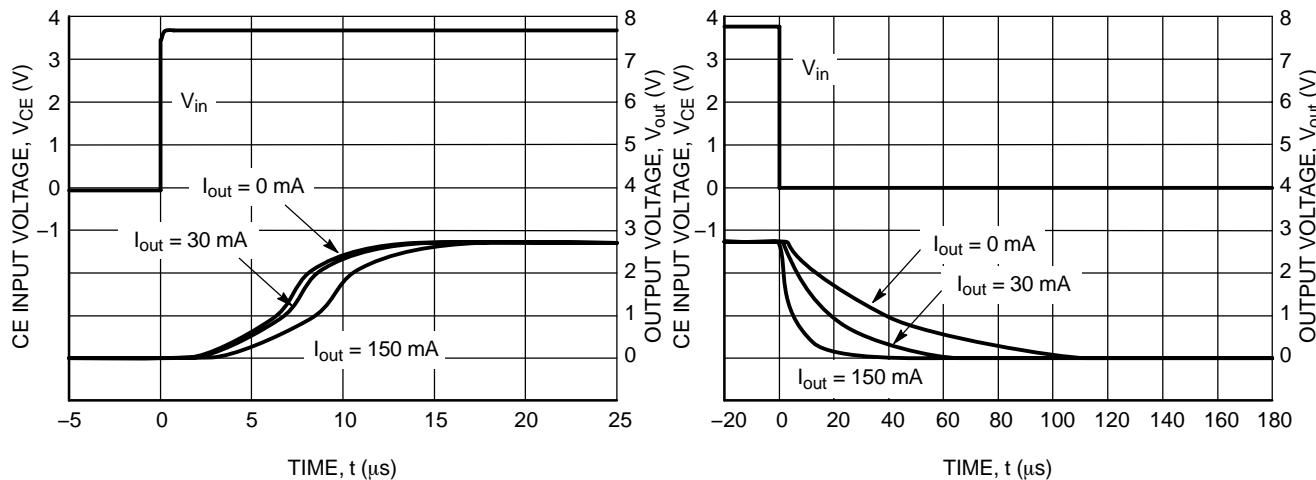


Figure 31. Turn-On/Off Speed with CE Pin (D Version)
 $(V_{out} = 2.8\text{ V}, V_{in} = 3.8\text{ V}, C_{in} = 0.47\text{ }\mu\text{F}, C_{out} = 0.47\text{ }\mu\text{F})$

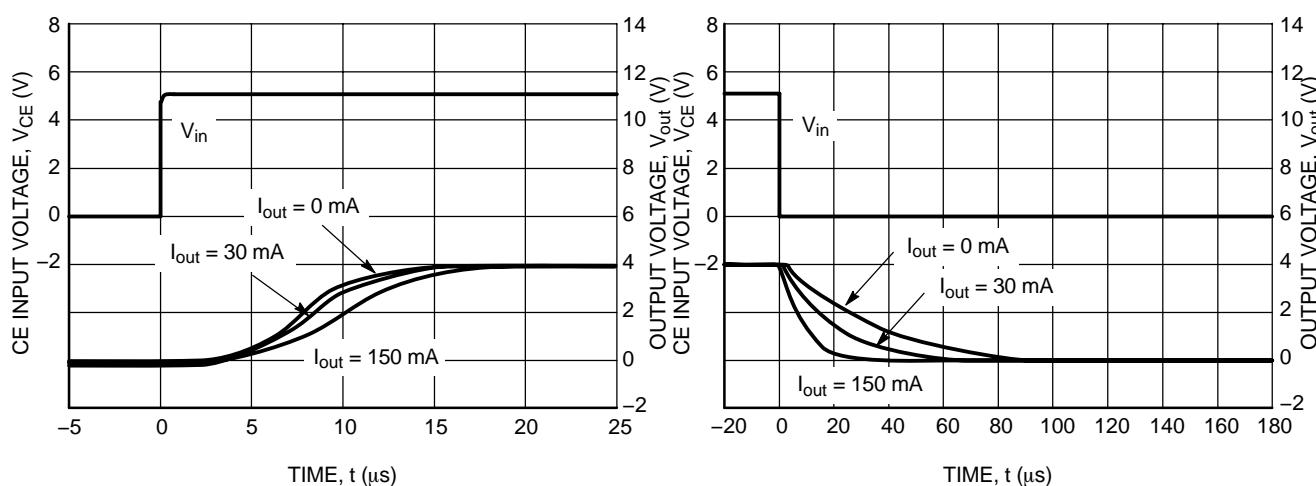


Figure 32. Turn-On/Off Speed with CE Pin (D Version)
 $(V_{out} = 4.0\text{ V}, V_{in} = 5.0\text{ V}, C_{in} = 0.47\text{ }\mu\text{F}, C_{out} = 0.47\text{ }\mu\text{F})$

APPLICATION INFORMATION

Input Decoupling

A 1.0 μF ceramic capacitor is the recommended value to be connected between V_{in} and GND. For PCB layout considerations, the traces of V_{in} and GND should be sufficiently wide in order to minimize noise and prevent unstable operation.

Output Decoupling

It is best to use a 1.0 μF capacitor value when $V_{\text{out}} < 2.5 \text{ V}$ and a 0.47 μF when $V_{\text{out}} \geq 2.5 \text{ V}$. For better

performance, select a capacitor with low Equivalent Series Resistance (ESR). For PCB layout considerations, place the output capacitor close to the output pin and keep the leads short as possible.

Noise Decoupling

The NCP582 series are low noise regulators and reach a noise level of only 30 μVRms between 10 Hz and 100 kHz.

ORDERING INFORMATION

Device	Output Type / Features	Nominal Output Voltage	Marking	Package	Shipping†
NCP582DSQ15T1G	Active High w/Auto Discharge	1.5	SF	SC-82AB (Pb-Free)	3000 Tape & Reel
NCP582DSQ18T1G	Active High w/Auto Discharge	1.8	SJ	SC-82AB (Pb-Free)	3000 Tape & Reel
NCP582DSQ25T1G	Active High w/Auto Discharge	2.5	TF	SC-82AB (Pb-Free)	3000 Tape & Reel
NCP582DSQ28T1G	Active High w/Auto Discharge	2.8	TJ	SC-82AB (Pb-Free)	3000 Tape & Reel
NCP582DSQ30T1G	Active High w/Auto Discharge	3.0	UA	SC-82AB (Pb-Free)	3000 Tape & Reel
NCP582DSQ33T1G	Active High w/Auto Discharge	3.3	UD	SC-82AB (Pb-Free)	3000 Tape & Reel
NCP582DXV15T1G*	Active High w/Auto Discharge	1.5	F15D	SOT-563 (Pb-Free)	4000 Tape & Reel
NCP582DXV18T1G*	Active High w/Auto Discharge	1.8	F18D	SOT-563 (Pb-Free)	4000 Tape & Reel
NCP582DXV25T1G*	Active High w/Auto Discharge	2.5	F25D	SOT-563 (Pb-Free)	4000 Tape & Reel
NCP582DXV28T1G*	Active High w/Auto Discharge	2.8	F28D	SOT-563 (Pb-Free)	4000 Tape & Reel
NCP582DXV29T1G*	Active High w/Auto Discharge	2.9	F29D	SOT-563 (Pb-Free)	4000 Tape & Reel
NCP582DXV30T1G*	Active High w/Auto Discharge	3.0	F30D	SOT-563 (Pb-Free)	4000 Tape & Reel
NCP582DXV33T1G*	Active High w/Auto Discharge	3.3	F33D	SOT-563 (Pb-Free)	4000 Tape & Reel
NCP582LSQ15T1G	Active Low	1.5	JF	SC-82AB (Pb-Free)	3000 Tape & Reel
NCP582LSQ18T1G	Active Low	1.8	JJ	SC-82AB (Pb-Free)	3000 Tape & Reel
NCP582LSQ25T1G	Active Low	2.5	KF	SC-82AB (Pb-Free)	3000 Tape & Reel
NCP582LSQ28T1G	Active Low	2.8	KJ	SC-82AB (Pb-Free)	3000 Tape & Reel
NCP582LSQ30T1G	Active Low	3.0	LA	SC-82AB (Pb-Free)	3000 Tape & Reel
NCP582LSQ33T1G	Active Low	3.3	LD	SC-82AB (Pb-Free)	3000 Tape & Reel
NCP582LXV15T1G*	Active Low	1.5	F15A	SOT-563 (Pb-Free)	4000 Tape & Reel
NCP582LXV18T1G*	Active Low	1.8	F18A	SOT-563 (Pb-Free)	4000 Tape & Reel
NCP582LXV25T1G*	Active Low	2.5	F25A	SOT-563 (Pb-Free)	4000 Tape & Reel
NCP582LXV28T1G*	Active Low	2.8	F28A	SOT-563 (Pb-Free)	4000 Tape & Reel
NCP582LXV29T1G*	Active Low	2.9	F29A	SOT-563 (Pb-Free)	4000 Tape & Reel
NCP582LXV30T1G*	Active Low	3.0	F30A	SOT-563 (Pb-Free)	4000 Tape & Reel
NCP582LXV33T1G*	Active Low	3.3	F33A	SOT-563 (Pb-Free)	4000 Tape & Reel

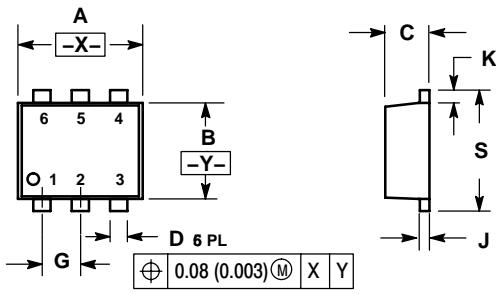
*Samples and production start 1Q05.

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

NCP582

PACKAGE DIMENSIONS

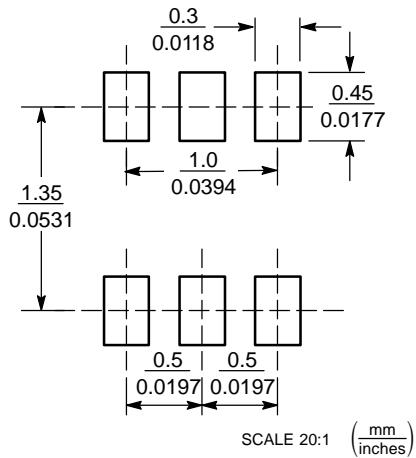
SOT-563
XV SUFFIX
CASE 463A-01
ISSUE D



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: MILLIMETERS
 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	1.50	1.70	0.059	0.067
B	1.10	1.30	0.043	0.051
C	0.50	0.60	0.020	0.024
D	0.17	0.27	0.007	0.011
G	0.50 BSC		0.020 BSC	
J	0.08	0.18	0.003	0.007
K	0.10	0.30	0.004	0.012
S	1.50	1.70	0.059	0.067

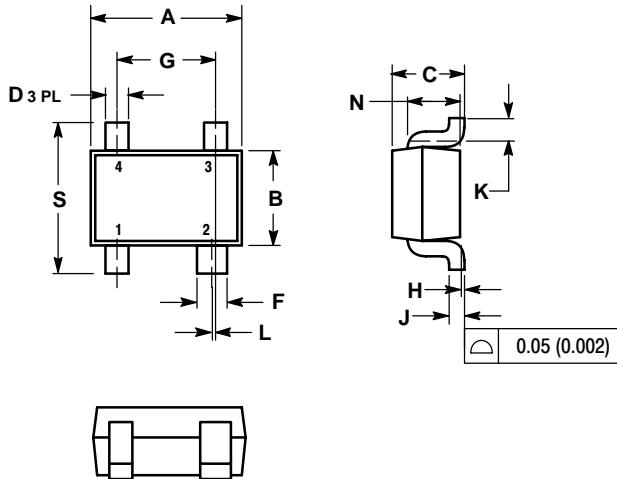
SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

PACKAGE DIMENSIONS

SC-82AB
SQ SUFFIX
CASE 419C-02
ISSUE C



NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.
3. 419C-01 OBSOLETE. NEW STANDARD IS 419C-02.
4. DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	1.8	2.2	0.071	0.087
B	1.15	1.35	0.045	0.053
C	0.8	1.1	0.031	0.043
D	0.2	0.4	0.008	0.016
F	0.3	0.5	0.012	0.020
G	1.1	1.5	0.043	0.059
H	0.0	0.1	0.000	0.004
J	0.10	0.26	0.004	0.010
K	0.1	---	0.004	---
L	0.05 BSC	0.002 BSC		
N	0.2 REF	0.008 REF		
S	1.8	2.4	0.07	0.09

ON Semiconductor and  are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor

P.O. Box 61312, Phoenix, Arizona 85082-1312 USA

Phone: 480-829-7710 or 800-344-3860 Toll Free USA/Canada

Fax: 480-829-7709 or 800-344-3867 Toll Free USA/Canada

Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free
USA/Canada

Japan: ON Semiconductor, Japan Customer Focus Center
2-9-1 Kamimeguro, Meguro-ku, Tokyo, Japan 153-0051
Phone: 81-3-5773-3850

ON Semiconductor Website: <http://onsemi.com>

Order Literature: <http://www.onsemi.com/litorder>

For additional information, please contact your local Sales Representative.