

670MHz Low Noise Amplifiers



The EL5132 and EL5133 are ultra-low voltage noise, high speed voltage feedback amplifiers that are ideal for

applications requiring low voltage noise, including communications and imaging. These devices offer extremely low power consumption for exceptional noise performance. Stable at gains as low as 10, these devices offer 120mA of drive performance. Not only do these devices find perfect application in high gain applications, they maintain their performance down to lower gain settings.

These amplifiers are available in small package options (SOT-23) as well as the industry-standard SO packages. All parts are specified for operation over the -40°C to +85°C temperature range.

Ordering Information

PART NUMBER	PACKAGE	TAPE & REEL	PKG. DWG. #
EL5132IS	8-Pin SO	-	MDP0027
EL5132IS-T7	8-Pin SO	7"	MDP0027
EL5132IS-T13	8-Pin SO	13"	MDP0027
EL5133IW-T7	5-Pin SOT-23	7" (3K pcs)	MDP0038
EL5133IW-T7A	5-Pin SOT-23	7" (250 pcs)	MDP0038

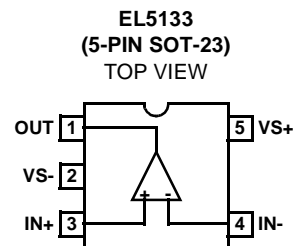
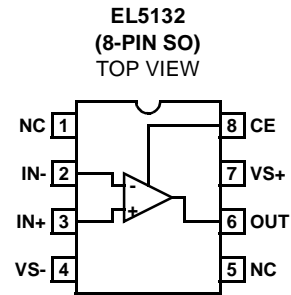
Features

- 670MHz -3dB bandwidth
- Ultra low noise 0.9nV/√Hz
- 1000V/μs slew rate
- Low supply current = 12mA
- Single supplies from 5V to 12V
- Dual supplies from ±2.5V to ±5V
- Fast disable on the EL5132
- Low cost

Applications

- Imaging
- Instrumentation
- Communications devices

Pinouts



EL5132, EL5133

Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$)

Supply Voltage from V_{S+} to V_{S-} 13.2V
 I_{IN-} , I_{IN+} , CE $\pm 5\text{mA}$
 Continuous Output Current 150mA
 Power Dissipation See Curves

Storage Temperature -65°C to $+125^\circ\text{C}$
 Ambient Operating Temperature -40°C to $+85^\circ\text{C}$
 Operating Junction Temperature $+125^\circ\text{C}$

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

IMPORTANT NOTE: All parameters having Min/Max specifications are guaranteed. Typical values are for information purposes only. Unless otherwise noted, all tests are at the specified temperature and are pulsed tests, therefore: $T_J = T_C = T_A$

Electrical Specifications $V_{S+} = +5\text{V}$, $V_{S-} = -5\text{V}$, $R_L = 150\Omega$, $R_F = 900\Omega$, $R_G = 100\Omega$, $T_A = 25^\circ\text{C}$, unless otherwise specified.

PARAMETER	DESCRIPTION	CONDITIONS	MIN	TYP	MAX	UNIT
V_{OS}	Offset Voltage		-1	0.5	1	mV
$T_C V_{OS}$	Offset Voltage Temperature Coefficient	Measured from T_{MIN} to T_{MAX}		0.8		$\mu\text{V}/^\circ\text{C}$
IB	Input Bias Current	$V_{IN} = 0\text{V}$	8	12	20	μA
I_{OS}	Input Offset Current	$V_{IN} = 0\text{V}$	-1250	400	+1250	nA
$T_C I_{OS}$	Input Bias Current Temperature Coefficient	Measured from T_{MIN} to T_{MAX}		3		$\text{nA}/^\circ\text{C}$
PSRR	Power Supply Rejection Ratio	$V_{S+} = \pm 4.75\text{V}$ to $\pm 5.25\text{V}$	75	87		dB
CMRR	Common Mode Rejection Ratio	$V_{IN} = \pm 3.0\text{V}$	80	100		dB
CMIR	Common Mode Input Range	Guaranteed by CMRR test	± 3	± 3.3		V
R_{IN}	Input Resistance	Common mode	2	5		$\text{M}\Omega$
C_{IN}	Input Capacitance			2		pF
I_S	Supply Current		9.2	11	13	mA
AVOL	Open Loop Gain	$V_{OUT} = \pm 2.5\text{V}$, $R_L = 1\text{k}\Omega$ to GND	5	8.5		KV/V
V_O	Output Voltage Swing	$R_F = 900\Omega$, $R_G = 100\Omega$, $R_L = 150\Omega$	± 3.1	3.5		V
I_{SC}	Short Circuit Current	$R_L = 10\Omega$	70	140		mA
BW	-3dB Bandwidth	$A_V = +10$, $R_L = 1\text{k}\Omega$		670		MHz
BW	$\pm 0.1\text{dB}$ Bandwidth	$A_V = +10$, $R_L = 1\text{k}\Omega$		90		MHz
GBWP	Gain Bandwidth Product			3000		MHz
PM	Phase Margin	$R_L = 1\text{k}\Omega$, $C_L = 6\text{pF}$		55		$^\circ$
SR	Slew Rate	$R_L = 100\Omega$, $V_{OUT} = \pm 2.5\text{V}$	700	1000		$\text{V}/\mu\text{s}$
t_R , t_F	Rise Time, Fall Time	$\pm 0.1V_{STEP}$		TBD		ns
OS	Overshoot	$\pm 0.1V_{STEP}$		TBD		%
t_{PD}	Propagation Delay	$\pm 0.1V_{STEP}$		TBD		ns
t_S	0.01% Settling Time			6.6		ns
dG	Differential Gain	$A_V = +2$, $R_F = 1\text{k}\Omega$		0.01		%
dP	Differential Phase	$A_V = +2$, $R_F = 1\text{k}\Omega$		0.01		$^\circ$
e_N	Input Noise Voltage	$f = 10\text{kHz}$		0.9		$\text{nV}/\sqrt{\text{Hz}}$
i_N	Input Noise Current	$f = 10\text{kHz}$		4.9		$\text{pA}/\sqrt{\text{Hz}}$

All Intersil U.S. products are manufactured, assembled and tested utilizing ISO9000 quality systems.
Intersil Corporation's quality certifications can be viewed at www.intersil.com/design/quality

Intersil products are sold by description only. Intersil Corporation reserves the right to make changes in circuit design, software and/or specifications at any time without notice. Accordingly, the reader is cautioned to verify that data sheets are current before placing orders. Information furnished by Intersil is believed to be accurate and reliable. However, no responsibility is assumed by Intersil or its subsidiaries for its use; nor for any infringements of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Intersil or its subsidiaries.

For information regarding Intersil Corporation and its products, see www.intersil.com