# Silicon N-Channel Power MOS FET Array

# **HITACHI**

### **Application**

High speed power switching

#### **Features**

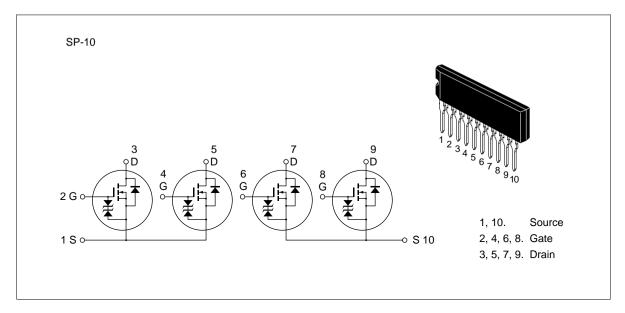
• Low on-resistance

$$R_{DS(on)} \le 0.18$$
 ,  $V_{GS} = 10$  V,  $I_D = 5$  A  $R_{DS(on)} \le 0.25$  ,  $V_{GS} = 4$  V,  $I_D = 5$  A

- Capable of 4 V gate drive
- Low drive current
- High speed switching
- High density mounting
- Suitable for motor driver, solenoid driver and lamp driver



## Outline



## **Absolute Maximum Ratings** ( $Ta = 25^{\circ}C$ ) (1 Unit)

| Item                                      | Symbol                       | Rating      | Unit |
|---|------------------------------|-------------|------|
| Drain to source voltage                   | $V_{\scriptscriptstyle DSS}$ | 60          | V    |
| Gate to source voltage                    | $V_{GSS}$                    | ±20         | V    |
| Drain current                             | $I_D$                        | 5           | А    |
| Drain peak current                        | I <sub>D(pulse)</sub> *1     | 20          | A    |
| Body to drain diode reverse drain current | I <sub>DR</sub>              | 5           | A    |
| Channel dissipation                       | Pch (Tc = $25^{\circ}$ C)*2  | 28          | W    |
| Channel dissipation                       | Pch*2                        | 4           | W    |
| Channel temperature                       | Tch                          | 150         | °C   |
| Storage temperature                       | Tstg                         | -55 to +150 | °C   |

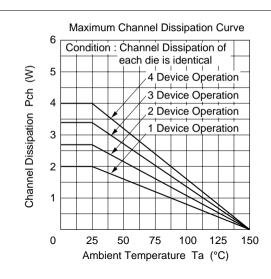
Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

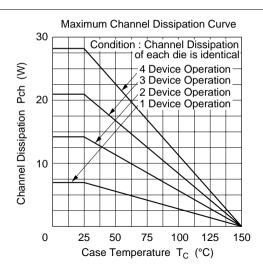
2. 4 devices operation

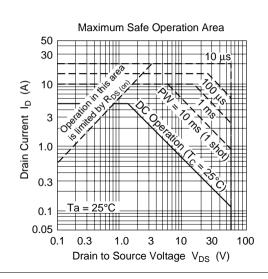
## **Electrical Characteristics** (Ta = 25°C) (1 Unit)

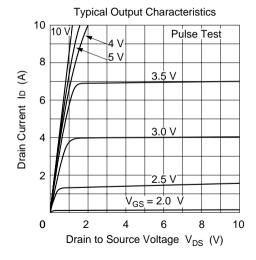
| Item                                       | Symbol              | Min | Тур  | Max  | Unit | Test conditions  |
|--|---------------------|-----|------|------|------|--|
| Drain to source breakdown voltage          | $V_{(BR)DSS}$       | 60  | _    | _    | V    | $I_D = 10 \text{ mA}, V_{GS} = 0$                                      |
| Gate to source breakdown voltage           | $V_{(BR)GSS}$       | ±20 | _    | _    | V    | $I_G = \pm 100 \ \mu A, \ V_{DS} = 0$                                  |
| Gate to source leak current                | I <sub>GSS</sub>    | _   | _    | ±10  | μΑ   | $V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$                                |
| Zero gate voltage drain current            | I <sub>DSS</sub>    | _   | _    | 250  | μΑ   | $V_{DS} = 50 \text{ V}, V_{GS} = 0$                                    |
| Gate to source cutoff voltage              | $V_{GS(off)}$       | 1.0 | _    | 2.0  | V    | $I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}$                            |
| Static drain to source on state resistance | R <sub>DS(on)</sub> | _   | 0.12 | 0.18 | Ω    | $I_D = 5 A$ $V_{GS} = 10 V^{*1}$                                       |
|  |                     | _   | 0.17 | 0.25 | Ω    | $I_D = 5 A$ $V_{GS} = 4 V^{*1}$  |
| Forward transfer admittance                | y <sub>fs</sub>     | 3.5 | 6.0  | _    | S    | $I_D = 5 A$<br>$V_{DS} = 10 V^{*1}$                                    |
| Input capacitance                          | Ciss                | _   | 400  | _    | pF   | $V_{DS} = 10 \text{ V}$  |
| Output capacitance                         | Coss                | _   | 220  | _    | pF   | $V_{GS} = 0$   |
| Reverse transfer capacitance               | Crss                | _   | 60   | _    | pF   | f = 1 MHz  |
| Turn-on delay time                         | $t_{d(on)}$         | _   | 5    | _    | ns   | $I_D = 5 A$  |
| Rise time                                  | t <sub>r</sub>      | _   | 55   | _    | ns   | $V_{GS} = 10 \text{ V}$  |
| Turn-off delay time                        | t <sub>d(off)</sub> | _   | 140  | _    | ns   | $R_L = 6 \Omega$   |
| Fall time                                  | t <sub>f</sub>      | _   | 90   | _    | ns   | _  |
| Body to drain diode forward voltage        | $V_{DF}$            |     | 1.0  |      | V    | $I_F = 5 \text{ A}, V_{GS} = 0$  |
| Body to drain diode reverse recovery time  | t <sub>rr</sub>     | _   | 100  | _    | ns   | $I_F = 5 \text{ A}, V_{GS} = 0$<br>$dIF/dt = 50 \text{ A}/\mu\text{s}$ |

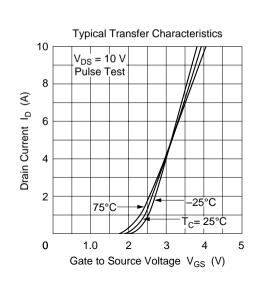
Note: 1. Pulse test

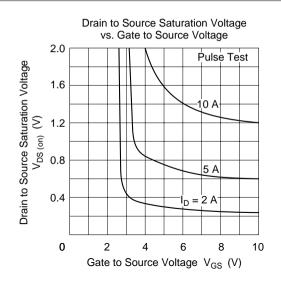


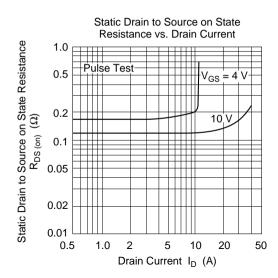


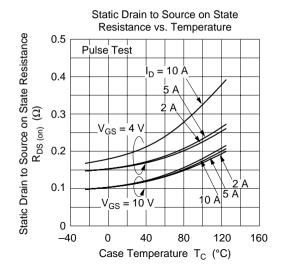


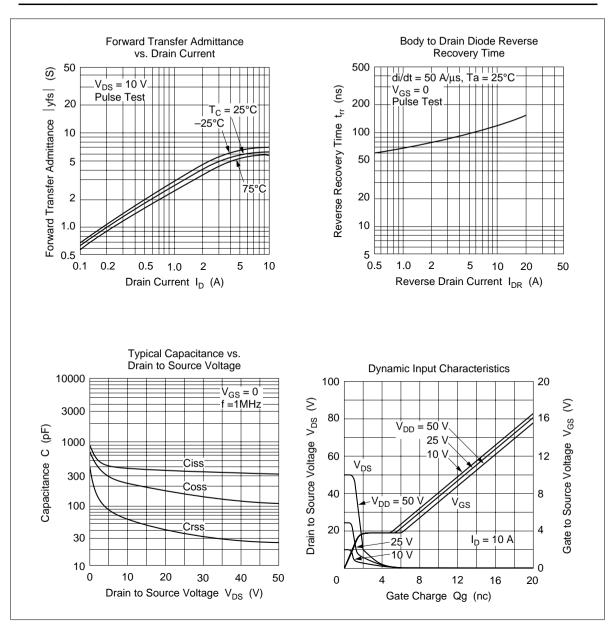


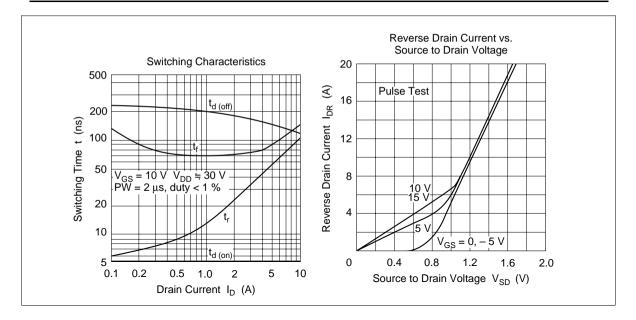




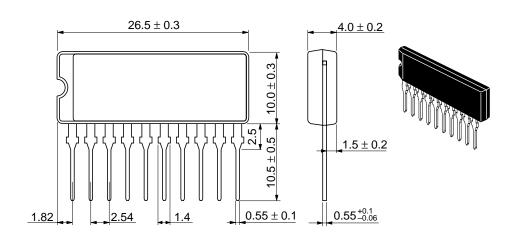








Unit: mm



| ٢  |   |   |   |   |   |   |   |   |   |    | ١ |
|----|---|---|---|---|---|---|---|---|---|----|---|
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| _  |   |   |   |   |   |   |   |   |   |    |   |

| Hitachi Code             | SP-10 |
|--------------------------|-------|
| JEDEC                    |       |
| EIAJ                     |       |
| Weight (reference value) | 2.9 g |

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