



N-Channel 40-V (D-S), 175 °C MOSFET

PRODUCT SUMMARY

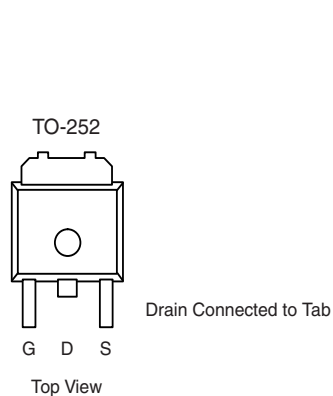
V _{DS} (V)	r _{DS(on)} (Ω)	I _D (A) ^a	Q _g (Typ)
40	0.020 at V _{GS} = 10 V	20	11.4 nC
	0.025 at V _{GS} = 4.5 V	20	

FEATURES

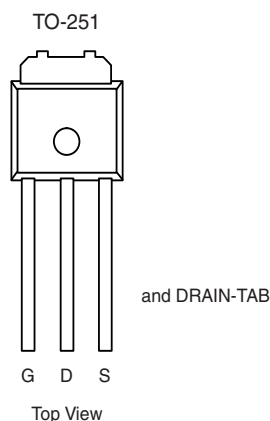
- TrenchFET[®] Power MOSFET
- 100 % R_g & UIS Tested

APPLICATIONS

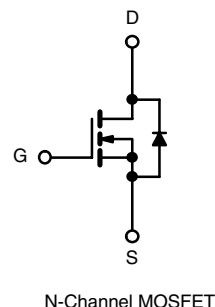
- LCD TV Inverter
- Secondary Synchronous Rectifier

RoHS
COMPLIANT

Order Number:
SUD50N04-25P-E3 (Lead (Pb)-free)



Order Number:
SUU50N04-25P-E3 (Lead (Pb)-free)

ABSOLUTE MAXIMUM RATINGS T_A = 25 °C, unless otherwise noted

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	40	V
Gate-Source Voltage	V _{GS}	± 16	
Continuous Drain Current (T _J = 150 °C)	I _D	20 ^a	A
		18.9	
		8.5 ^b	
		4.9 ^b	
Pulsed Drain Current	I _{DM}	50	A
Continuous Source-Drain Diode Current	I _S	20 ^a	
		2.3 ^b	
Single Pulse Avalanche Current	I _{AS}	20	mJ
Avalanche Energy	E _{AS}	20	
Maximum Power Dissipation	P _D	28.8	W
		14.4	
		2.8 ^b	
		1.4 ^b	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 175	°C

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^b	R _{thJA}	43	52	°C/W
Maximum Junction-to-Case	R _{thJC}	4.3	5.2	

Notes:

a. Package limited.

b. Surface mounted on 1" x 1" FR4 board.

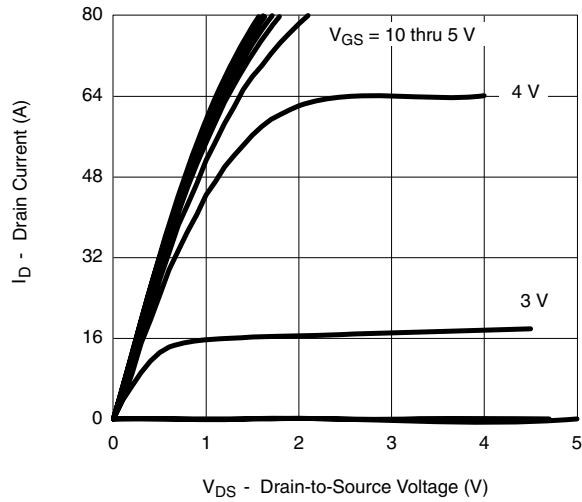
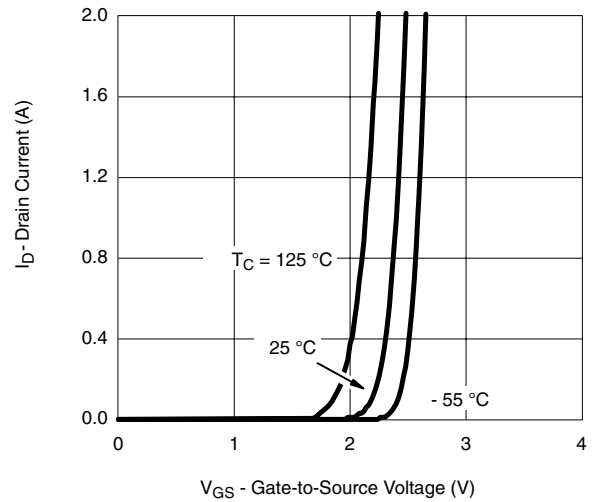
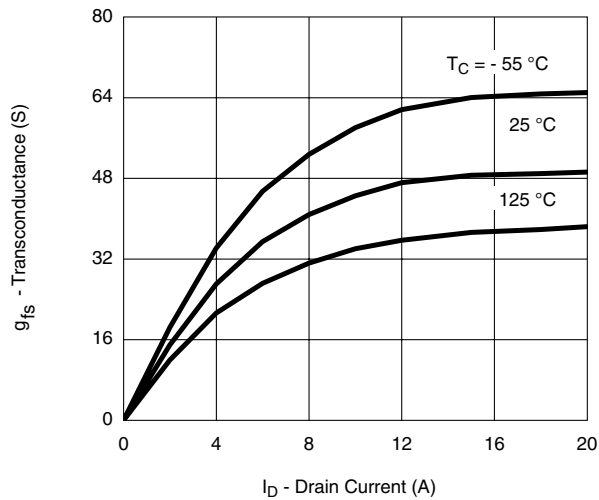
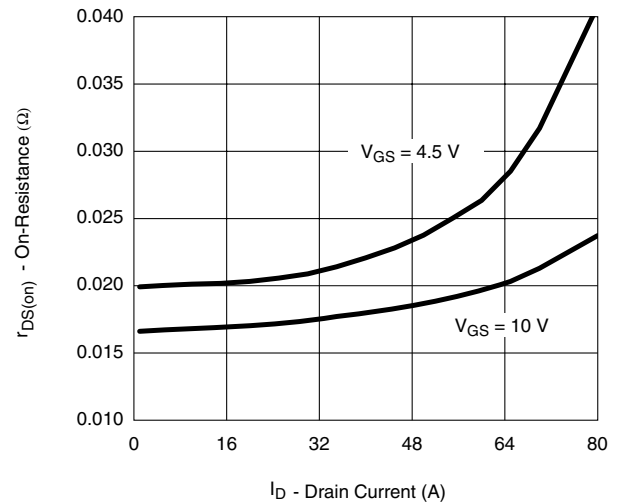
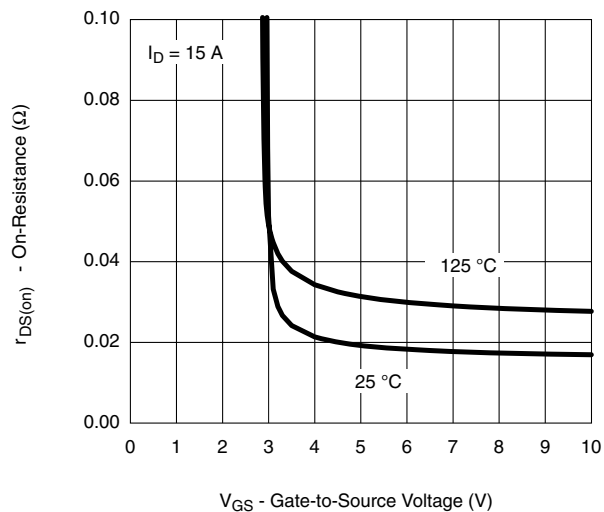
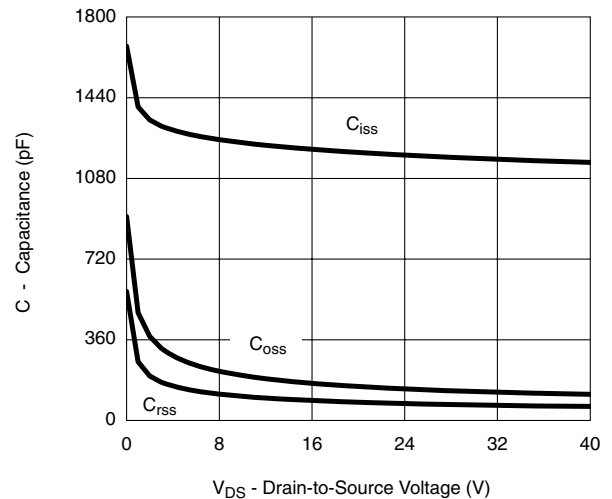
SPECIFICATIONS T _J = 25 °C, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	40			V
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	I _D = 250 μA		37		mV/°C
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J			5.3		
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	1.0		2.6	V
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 16 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 40 V, V _{GS} = 0 V			1	μA
		V _{DS} = 40 V, V _{GS} = 0 V, T _J = 100 °C			20	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 5 V, V _{GS} = 10 V	30			A
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = 10 V, I _D = 15 A		0.016	0.020	Ω
		V _{GS} = 4.5 V, I _D = 10 A		0.020	0.025	
Forward Transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 15 A		42		S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{DS} = 20 V, V _{GS} = 0 V, f = 1 MHz		1195		pF
Output Capacitance	C _{oss}			150		
Reverse Transfer Capacitance	C _{rss}			80		
Total Gate Charge	Q _g	V _{DS} = 20 V, V _{GS} = 10 V, I _D = 30 A		25	38	nC
Gate-Source Charge	Q _{gs}	V _{DS} = 20 V, V _{GS} = 4.5 V, I _D = 30 A		11.4	18	
Gate-Drain Charge	Q _{gd}			3.2		
Gate Resistance	R _g	f = 1 MHz		1.6	2.5	
Turn-On Delay Time	t _{d(on)}	V _{DD} = 20 V, R _L = 0.66 Ω I _D ≡ 30 A, V _{GEN} = 4.5 V, R _g = 1 Ω		13	20	ns
Rise Time	t _r			14	21	
Turn-Off Delay Time	t _{d(off)}			16	25	
Fall Time	t _f			7	14	
Turn-On Delay Time	t _{d(on)}	V _{DD} = 20 V, R _L = 0.66 Ω I _D ≡ 30 A, V _{GEN} = 10 V, R _g = 1 Ω		6	12	
Rise Time	t _r			9	18	
Turn-Off Delay Time	t _{d(off)}			24	36	
Fall Time	t _f			7	14	
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			20	A
Pulse Diode Forward Current ^a	I _{SM}				50	
Body Diode Voltage	V _{SD}	I _S = 10 A		0.87	1.2	V
Body Diode Reverse Recovery Time	t _{rr}	I _F = 20 A, di/dt = 100 A/μs, T _J = 25 °C		25	38	ns
Body Diode Reverse Recovery Charge	Q _{rr}			22	33	nC
Reverse Recovery Fall Time	t _a			17		ns
Reverse Recovery Rise Time	t _b			8		

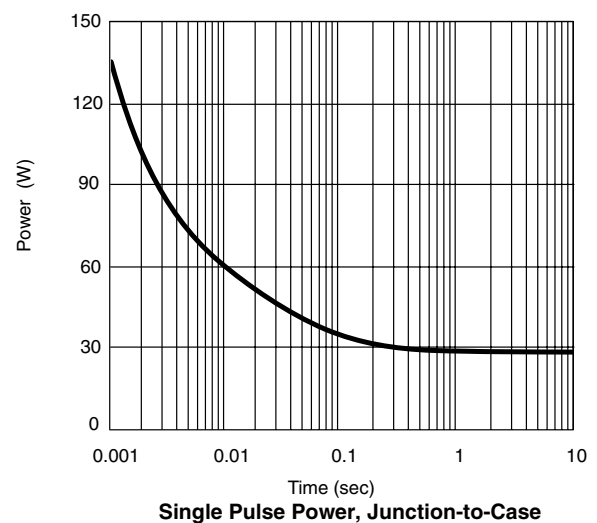
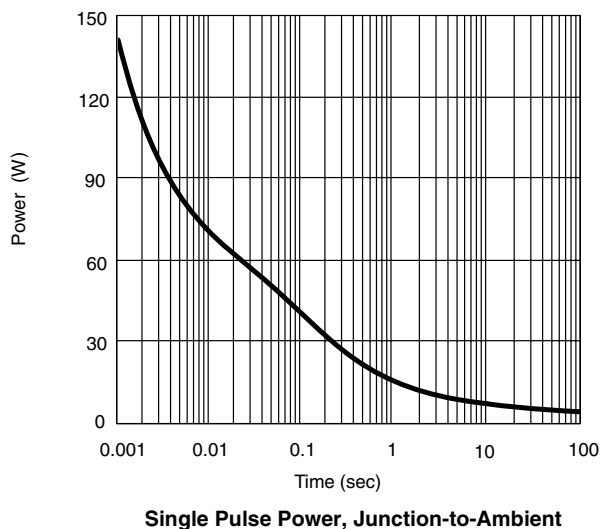
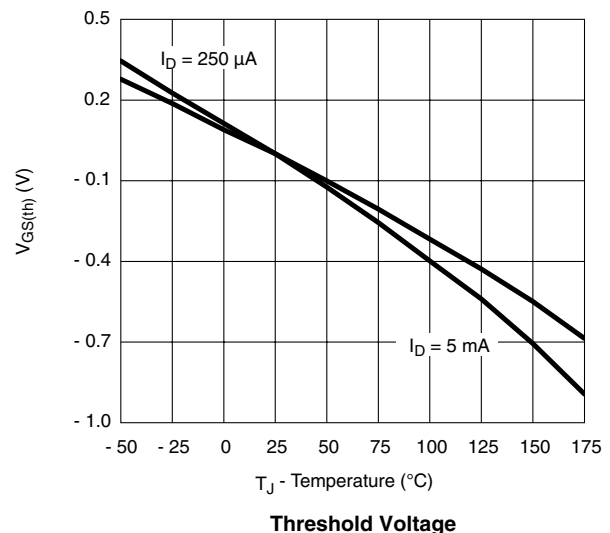
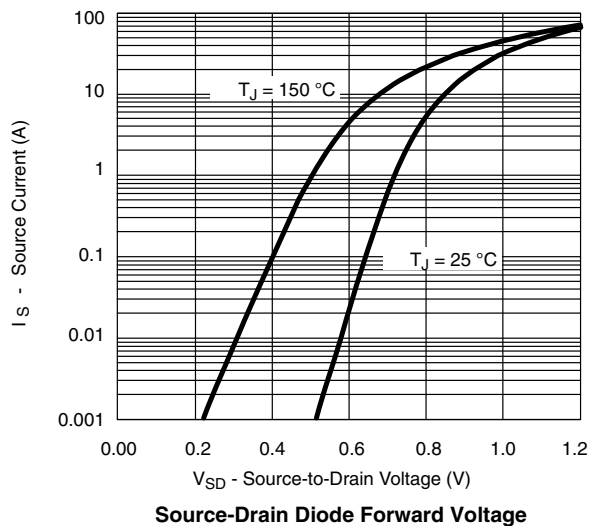
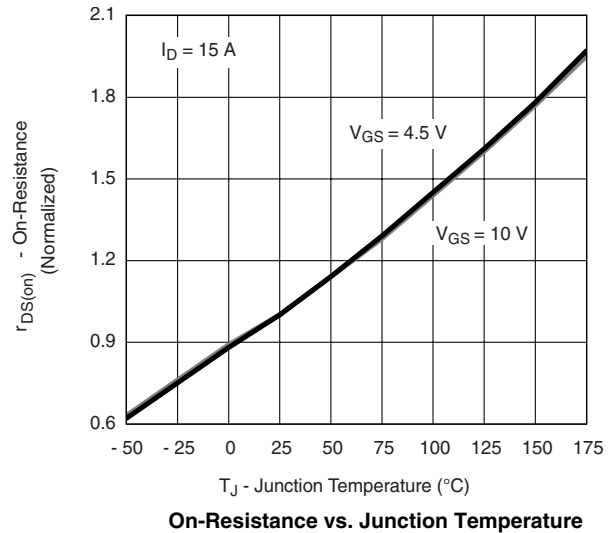
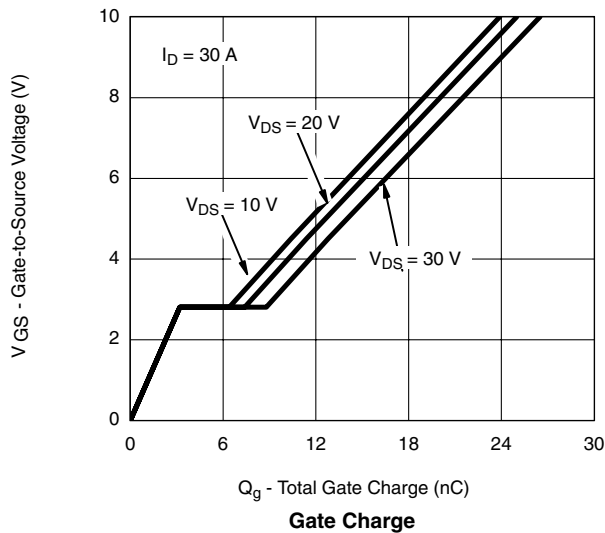
Notes:

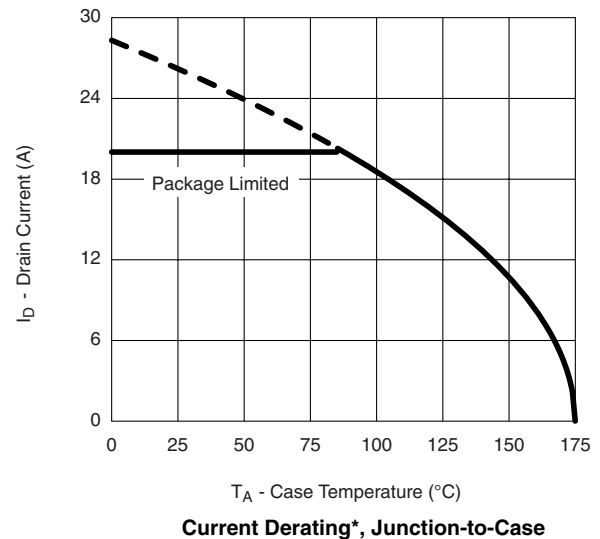
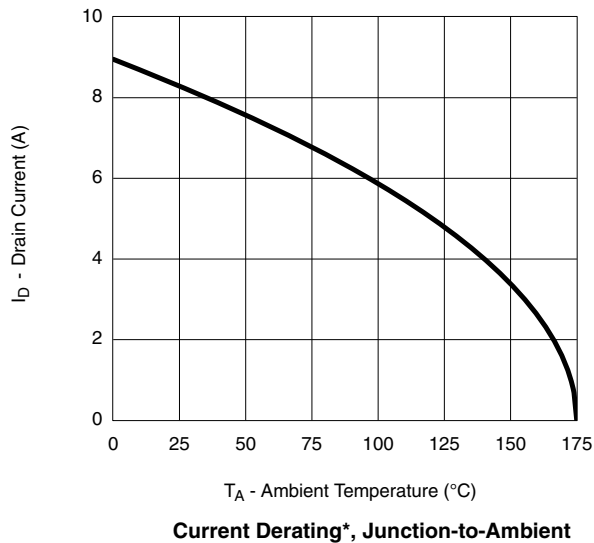
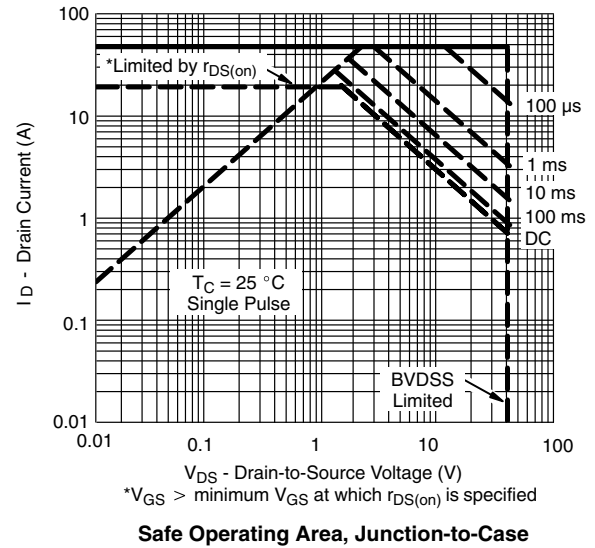
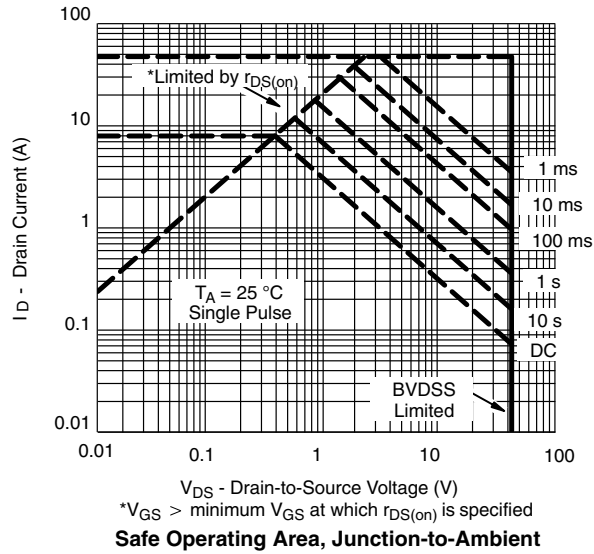
a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.

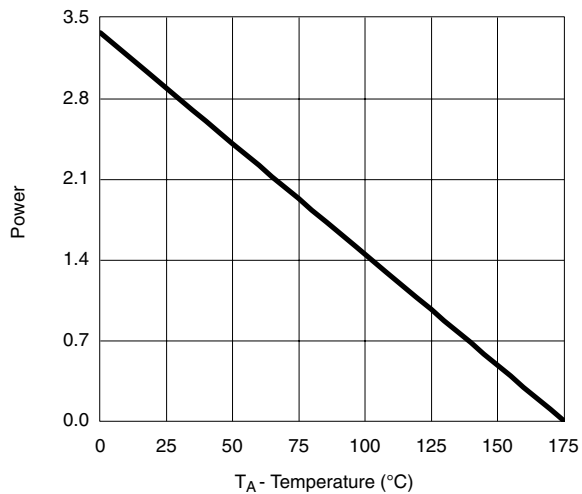
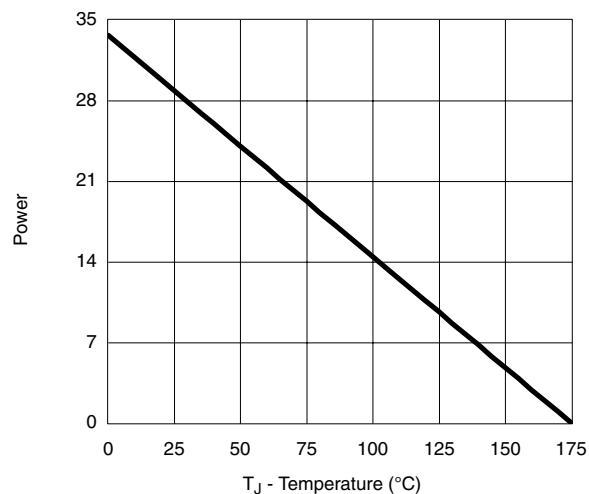
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

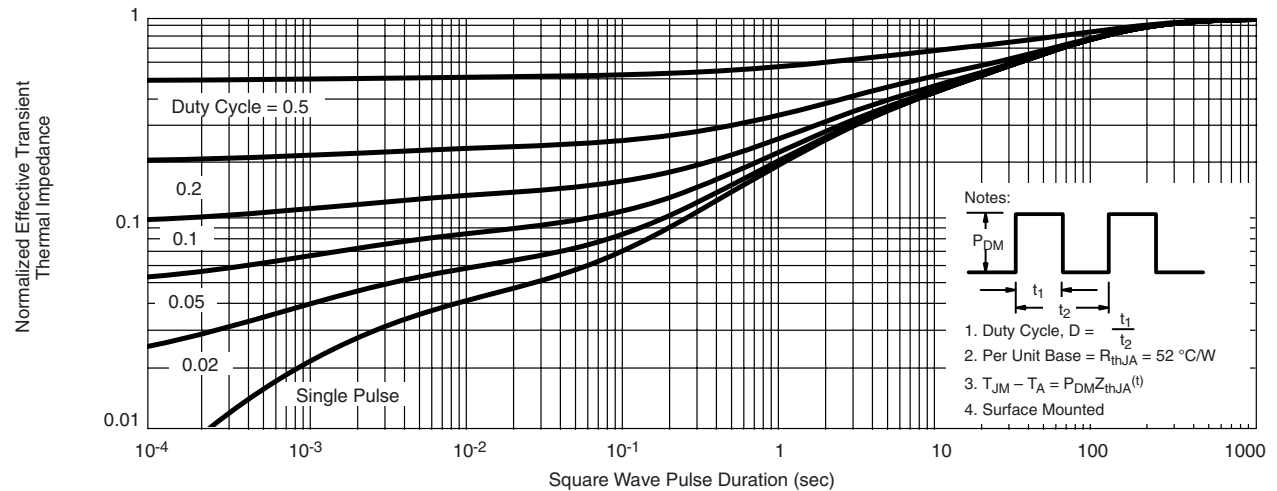
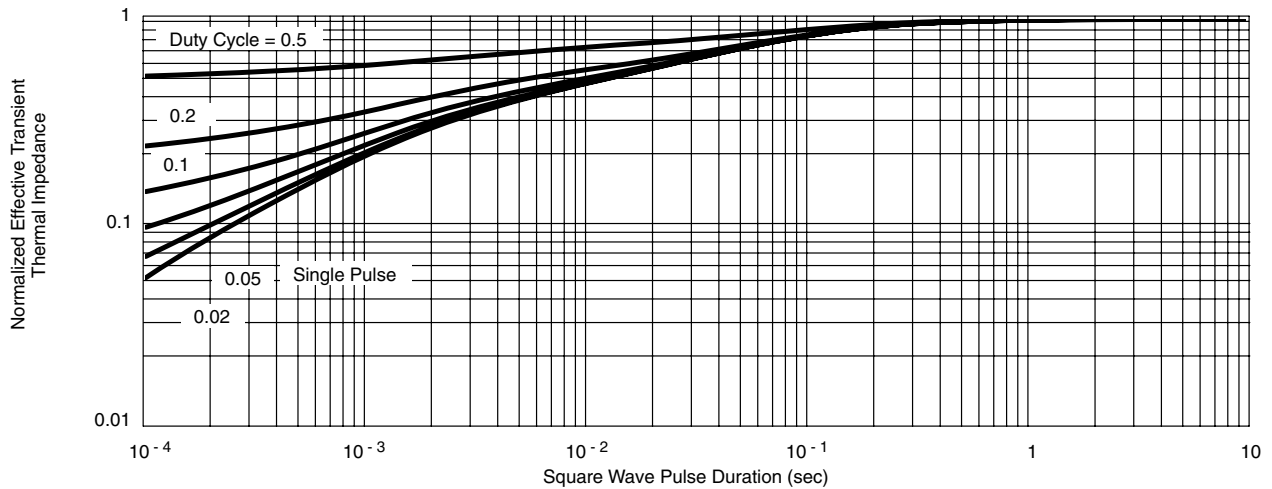
**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted**Output Characteristics****Transfer Characteristics****Transconductance****On-Resistance vs. Drain Current****On-Resistance vs. Gate-to-Source Voltage****Capacitance**

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted

**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted**Power Derating*, Junction-to-Ambient****Power Derating*, Junction-to-Case**

*The power dissipation P_D is based on $T_{J(max)} = 175\text{ °C}$, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted**Normalized Thermal Transient Impedance, Junction-to-Ambient****Normalized Thermal Transient Impedance, Junction-to-Case**

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