Ver.: 1.1

ISSUED DATE:	2008-01-14
VERSION :	Ver 1.1
	ary Product Specification duct Specification

MODEL NO.: TS028HAACB07-00

Customer :

Approved by	Notes

SHANGHAI TIANMA Confirmed:

prepared by	Checked by	Approved by
徐昀	刘庆全	刘庆全

This technical specification is subjected to change without notice

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TIANMA MICRO-ELECTRONICS

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TS028HAACB07-00

Record of Revision

Rev	Issued Date	Description
1.0	Dec,11,2007	Preliminary release
1.1	Jan,14,2008	Modified RA conditions

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1 GENERAL SPECIFICATIONS

	Feature	Spec		
	Size	2.83"		
	Resolution	240(RGB) X 320		
	Interface	CPU		
	Color Depth	16bit		
	Technology type	a-si		
Display Spec.	Pixel pitch (mm)	0.18 x 0.18		
	Display colors	65 k		
	Pixel Configuration	R.G.B. Vertical Stripe		
	Display Mode	TM with Normally White		
	Surface Treatment	HC, 3H		
	Gray Scale Inversion Direction	12 o'clock		
	LCM (W x H x D) (mm)	50.0x69.2x4.2		
Maabaalaal	Active Area(mm)	43.2 x 57.6		
Mechanical Characteristics	With /Without TSP	WITH TSP		
	Weight (gram)	TBD.		
	LED Numbers	4 LEDs parallel		
	Driver IC	NT39116		

Note 1: Requirements on Environmental Protection: RoHS

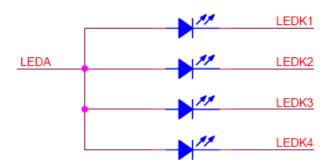


2 INPUT/OUTPUT TERMINALS

2.1 TFT LCD Panel

Pin No.	Symbol	Level	Description					
1	LEDK4	-	LED light cathode					
2	LEDK3	-	LED light cathode					
3	LEDK2	-	LED light cathode					
4	LEDK1	•	LED light cathode					
5	LEDA	•	LED light anode					
6	IM0	H/L	IM0 IM3 INTERFACE MODE DB PIN 0 0 80 system 16 bit interface DB17~10,8~1 1 0 80 system 8 bit interface DB17~10					
7	IM3		0 1 80system 18 bit interface DB17~0 1 1 80system 9 bit interface DB17~9					
8	RESET	H/L	Reset signal.					
9	VSYNC	H/L	Frame synchronous signal for RGB interface operation. Low active. Connect to IOGND/IOVCC when not in use.					
10~27	DB17~DB0	H/L	Data bus. Connect to IOGND/IOVCC when not in use.					
28	RD	H/L	Read signal.					
29	WR	H/L	Write signal.					
30	RS	H/L	Command / data select pin.					
31	CS	H/L	Chip selection.					
32	FMARK	H/L	Frame head pulse signal, which is used when writing data to the internal RAM. When not in use ,left open.					
33	IOVCC	1.8V/2.8V(typ)	I/O power supply for LCD driver.					
34	VCC	2.8V (typ)	Power supply for LCD.					
35	VCI	2.8V (typ)	Analog Power supply for LCD driver.					
36	GND	0V	Ground					
37	X(R)	-	Touch panel coordinate in the right side of envisage drawing					
38	Y(D)	-	Touch panel coordinate in the down side of envisage drawing					
39	X(L)	-	Touch panel coordinate in the left side of envisage drawing					
40	Y(U)	-	Touch panel coordinate in the up side of envisage drawing					

Note 2-1: The figure below shows the connection of backlight LED.



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3. ABSOLUTE MAXIMUM RATINGS

Ta = 25°C

Item	Symbol	MIN	MAX	Unit	Remark
Supply Voltage	VCC	-0.3	4.6	V	
Analog Supply Voltage	VCI	-0.3	4.6	V	
Input Signal Voltage	/CS,RS,/WR,/RD, /RESET, DB[0 :15]	-0.3	VCC+0.3	٧	
Back Light Forward Current	ILED		25	mA	One LED
Operating Temperature	TOPR	-20	60	$^{\circ}$	
Storage Temperature	TSTG	-30	70	$^{\circ}$	

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4 ELECTRICAL CHARACTERISTICS

4.1. Driving TFT LCD Panel

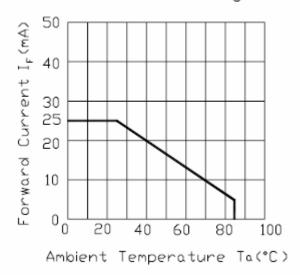
GND=0V, Ta=25 $^{\circ}$ C

Item		Symbol	MIN	TYP	MAX	Unit	Remark
Logic Supp	ly Voltage	VCC	2.5	2.8	3.3	V	
Analog Supp	ply Voltage	VCI	2.5	2.8	3.3	V	
Input Signal	Low Level	V_{IL}	-0.3		0.2*VCC	V	
Voltage	High Level	V _{IH}	0.8*VCC		VCC	V	
(Panel+LSI)		Black Mode (60Hz)		TBD		mW	
Power Consu	umption	8 color Mode		TBD		mW	
·		Sleeping Mode		TBD		mW	

4.2 Driving Backlight Ta=25°C

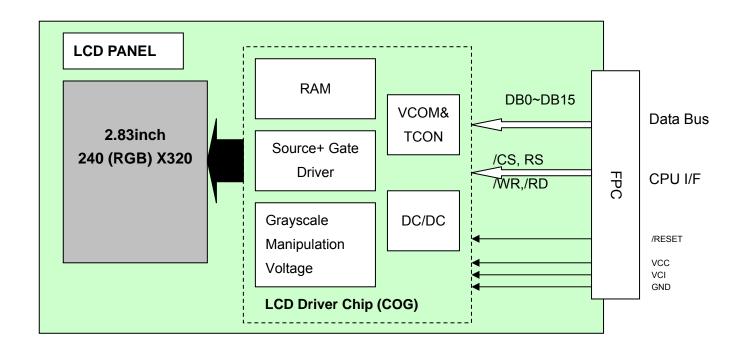
Item	Symbol	MIN	TYP	MAX	Unit	Remark
Forward Current	I _F		20	25	mA	
Forward Current Voltage	V_{F}		3.2		V	
Backlight Power Consumption	W_{BL}		256		mW	





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4.3 Block Diagram



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5. INTERFACE TIMING

5.1 Timing Parameter

Item	Symbol	Unit	Min	Тур	Max
Write bus cycle time	t _{CYCW}	ns	100	-	-
Write low-level pulse width	PW_{LW}	ns	50	-	500
Write high-level pulse width	PW_{HW}	ns	50	-	-
Write rise / fall time	t _{Wr} / t _{Wf}	ns	ı	-	25
Write Setup time (RS to /CS, /WR)	t _{AS}	ns	10	-	-
Address hold time	t _{AH}	ns	5	-	_
Write data set up time	t _{DSW}	ns	10	-	-
Write data hold time	t _H	ns	15	-	-

Table 5.1 timing parameter

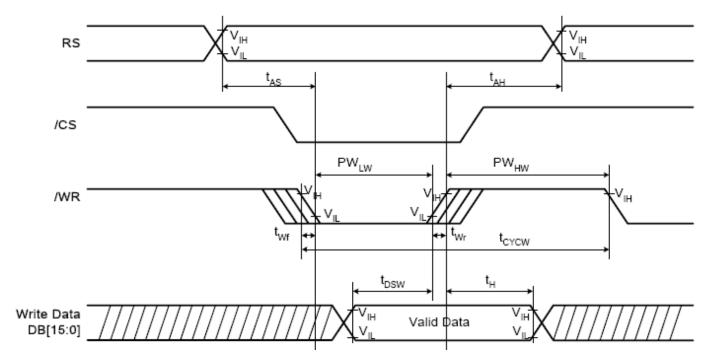


Figure 5.1 i80 System Bus Timing

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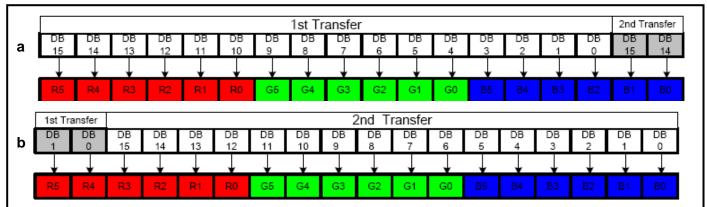
5.2 Register write timing in I80 series system



Figure 5.2 i80 16-bit System Bus Interface Timing

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5.3 GRAM write timing in I80 series system



a. TRI=1,DFM=0,16+2-bit;

b. TRI=1,DFM=1,2+16-bit;

Figure 5.3.1 i80-System Interface with 16-bit Data Bus

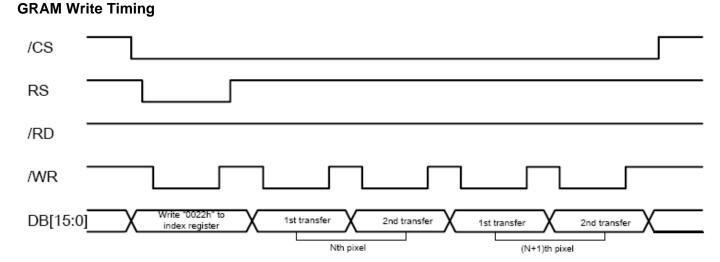
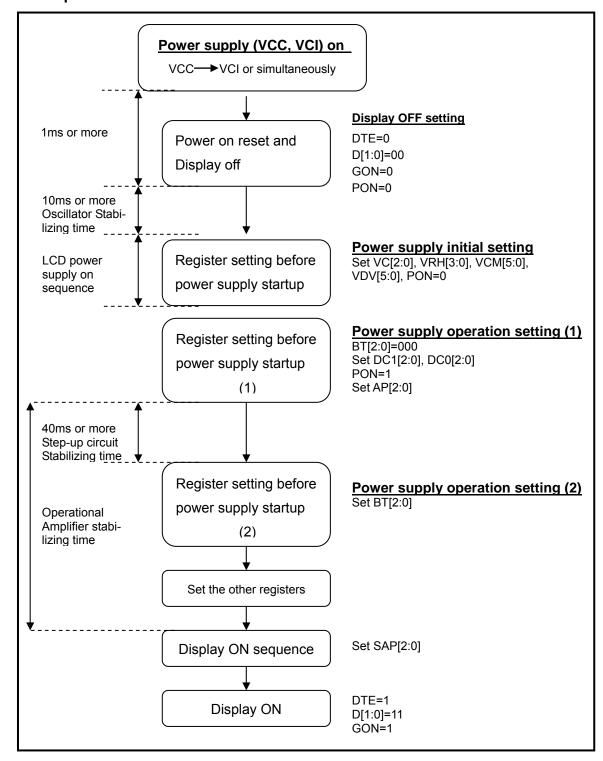


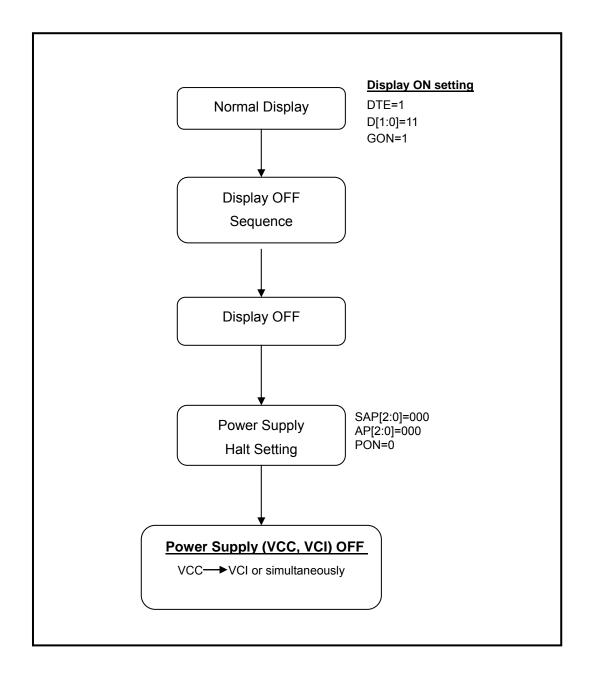
Figure 5.3.2 GRAM Write Timing of i80 16-bit System Interface

6. POWER ON/OFF SEQUENCE

6.1 Power on Sequence



6.2 Power off Sequence





6 OPTICAL CHARACTERISTICS

6.1 Optical Specification

Ta=25°C

Item		Symbol	Condition	Min	Typ.	Max.	Unit	Remark	
77' A 1		θТ		50	60				
		Θ B	CD > 10	30	40		Dagge	Nata 2	
View An	gies	Θ L	CR≥10	50	60		Degree	Note 2	
		⊖R		50	60				
Contrast I	Ratio	CR	<i>θ</i> =0°		350			Note3	
Response	Timo	Tr	2 5℃		25		me	Note4	
Kesponse	Tille	Tf	23 (25		ms	Note4	
	White	х		0.260	0.310	0.360			
	vviiite	y		0.280	0.330	0.380			
	RED	x		0.564	0.614	0.664			
Chromaticity		у	Brightness	0.321	0.371	0.421		Note1, 5	
Cinomaticity	GREEN	х	is on	0.284	0.334	0.384		rioter, 5	
		у		0.547	0.597	0.647			
	BLUE	х		0.091	0.141	0.191			
	DLOE	у		0.060	0.110	0.160			
Uniform	Uniformity			75	80		%	Note6	
NTSC					55		%	Note 5	
Luminance (with TSP)	L		150	180		cd/m ²	Note7	

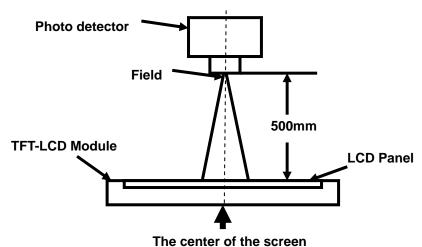
Test Conditions:

- 1. Vcc=2.8V I_L =20mA(Backlight current), the ambient temperature is 25 $^{\circ}$ C.
- 2. The test systems refer to Note 1 and Note 2.

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Note 1: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 5 minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.



Item	Photo detector	Field
Contrast Ratio		1°
Luminance	SR-3	
Chromaticity	3K-3	
Lum Uniformity		
Response Time	BM-7A	

Note 2: Definition of viewing angle range and measurement system.

viewing angle is measured at the center point of the LCD by LCD5200.

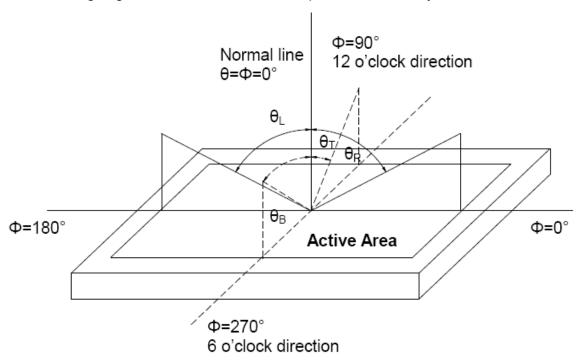


Fig. 1 Definition of viewing angle

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Note 3: Definition of contrast ratio

Contrast ratio (CR) = Luminance measured when LCD is on the "White" state

Luminance measured when LCD is on the "Black" state

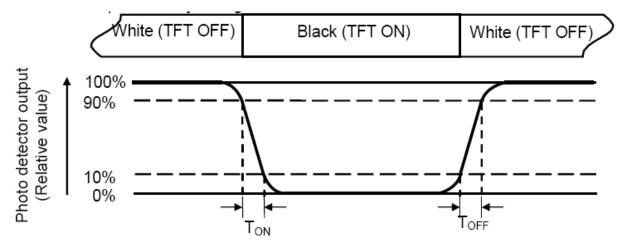
"White state ":The state is which the LCD is driven by Vwhite.

"Black state": The state is which the LCD is driven by Vblack.

Vwhite: To be determined **V**black: To be determined.

Note 4: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (TON) is the time between photo detector output intensity changed from 90% to 10%. And fall time (TOFF) is the time between photo detector output intensity changed from 10% to 90%.



Note 5: Definition of color chromaticity (CIE1931) Color coordinates measured at center point of LCD.

Note 6: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

Luminance Uniformity(U) = Lmin/Lmax

L-----Active area length W----- Active area width

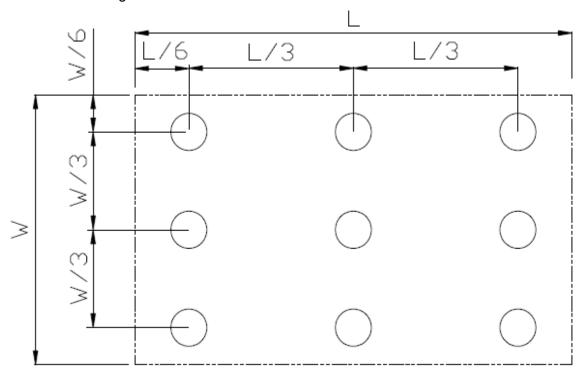


Fig. 2 Definition of uniformity

Lmax: The measured maximum luminance of all measurement position.

Lmin: The measured minimum luminance of all measurement position.

Note 7: Definition of Luminance:

Measure the luminance of white state at center point



7 Environmental / Reliability Tests

No	Test Item	Condition	Remarks
1	High Temperature Op-	Ts=+60°C, 120hrs	Note1
	eration		IEC60068-2-2,GB2423.2—89
2	High Temperature &	+60℃, 90% RH max,120 hours	IEC60068-2-3,
	High Humidity storage		GB/T2423.3—2006
3	Low Temperature Op-	Ta=-20℃, 120hrs	Note 2, IEC60068-2-1
	eration		GB2423.1—89
4	High Temperature Stor-	Ta=+70°C, 120hrs	IEC60068-2-2,
	age (non-operation)		GB2423.2—89
5	Low Temperature Stor-	Ta=-20°ℂ, 120hrs	IEC60068-2-1
	age (non-operation)		GB2423.1—89
6	Thermal Shock (non-operation)	-20℃ 30 min~+60℃ 30 min,	Start with cold temperature, end
		Change time:5min, 20 Cycle	with high temperature
			IEC60068-2-14,GB2423.22—87
		C=150pF, R=330 Ω , 5points/panel	IEC61000-4-2
7	Electro Static Discharge	Air:±8KV , 5times;Contact:±4KV , 5 times;	GB/T17626.2—1998
	(operation)	(Environment: $15^{\circ}\text{C} \sim 35^{\circ}\text{C}$, $30\% \sim 60\%$,	
		86Kpa∼106Kpa)	
8		Frequency range:10~55Hz, Stroke:1.5mm	IEC60068-2-6
	Vibration (non-operation)	Sweep:10Hz~55Hz~10Hz 2 hours for each	GB/T2423.10—1995
		direction of X.Y.Z.(6 hours for total)	GB/12423.10—1993
9	Shock (non-operation)	60G 6ms, ±X,±Y,±Z 3times for each di-	IEC60068-2-27
		rection	GB/T2423.5—1995
10	Package Drop Test	Height:80 cm, 1 corner, 3 edges, 6	IEC60068-2-32
		surfaces	GB/2423.8—1995

Note1: Ts is the temperature of panel's surface.

Note2: Ta is the ambient temperature of samples.

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8. MECHANICAL DRAWING

TBD

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9. Packing Drawing

TBD



10. Precautions for Use of LCD Modules

10.1 Handling Precautions

- **10.1.1.** The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- **10.1.2**. If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- **10.1.3.** Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- **10.1.4.** The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- **10.1.5.** If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:
- Isopropyl alcohol
- Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents
- **10.1.6.** Do not attempt to disassemble the LCD Module.
- **10.1.7**. If the logic circuit power is off, do not apply the input signals.
- **10.1.8.** To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
- a. Be sure to ground the body when handling the LCD Modules.
- b. Tools required for assembly, such as soldering irons, must be properly ground.
- c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
- d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

10.2 Storage precautions

- **10.2.1.** When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.
- **10.2.2.** The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 0° C ~ 40° C Relatively humidity: ≤80%

10.2.3. The LCD modules should be stored in the room without acid, alkali and harmful gas.

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10.3 Transportation Precautions

The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

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