



**TIANMA**

# SPECIFICATION FOR LCD MODULE

CUSTOMER: TM Standard LCM

CUSTOMER NO.: -----

PRODUCT NO.: TM9632RGFWGWC

PRODUCT TYPE: Graphic Matrix STN

VERSION: V2.0

Preliminary specification

Final specification

CUSTOMER APPROVED

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## REVISION RECORD

Version	Page	Revision Items	Name	Date
1.0		First release		
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## 1 Description

The TM9632RGFWGWC, a Graphic LCM unit consists of 96 (segment)×32 (common) dot-matrix LCD panel, LCD driver and a FPC. With an IC of COG type, the whole LCM is very smart.

- ◆ Wide temperature range
- ◆ COG type
- ◆ Requirements on environmental protection: RoHS.

## 2 Features

Item	Contents
LCD type	FSTN
	Negative
LCD Duty	1/33
LCD Bias	1/7
Polarizer	Transmissive
LCD background color	Black
Segment color	White
Backlighting	LED
Backlighting type	Side
Backlighting color	White
View direction	6:00 o'clcok
Operating temperature	-10°~60°C
Storage temperature	-20°C~70°C
LCD Controller	ST7579
Frame	---
Technology	COG
Data Transfer	4-line SPI

### 3 Absolute maximum ratings

(VSS=0V, Ta=25°C)

Parameter	Symbol	Min	Max	Unit	Remark
Logic circuit supply voltage	VDD	-0.3	+3.6	V	---
Input voltage of logic control or data pins	V <sub>I</sub>	-0.3	VDD+0.3	V	---
LCD driving voltage	V <sub>LCD</sub>	-0.3	10.5	V	Note [3-2]
Operating temperature range	T <sub>OP</sub>	-10	+60	°C	Note [3-3]
Storage temperature range	T <sub>ST</sub>	-20	+70	°C	

Note [3-1]: No parameter is allowed to exceed these maximum ratings.

Note [3-2]: LCD operating voltage V<sub>LCD</sub> is a referenced value for our LCD test.

Note [3-3]: 95% RH MAX (40 °C ≥ Ta);

Maximum wet-bulb temperature is 39°C or less. (Ta >40 °C) No dew condensation.

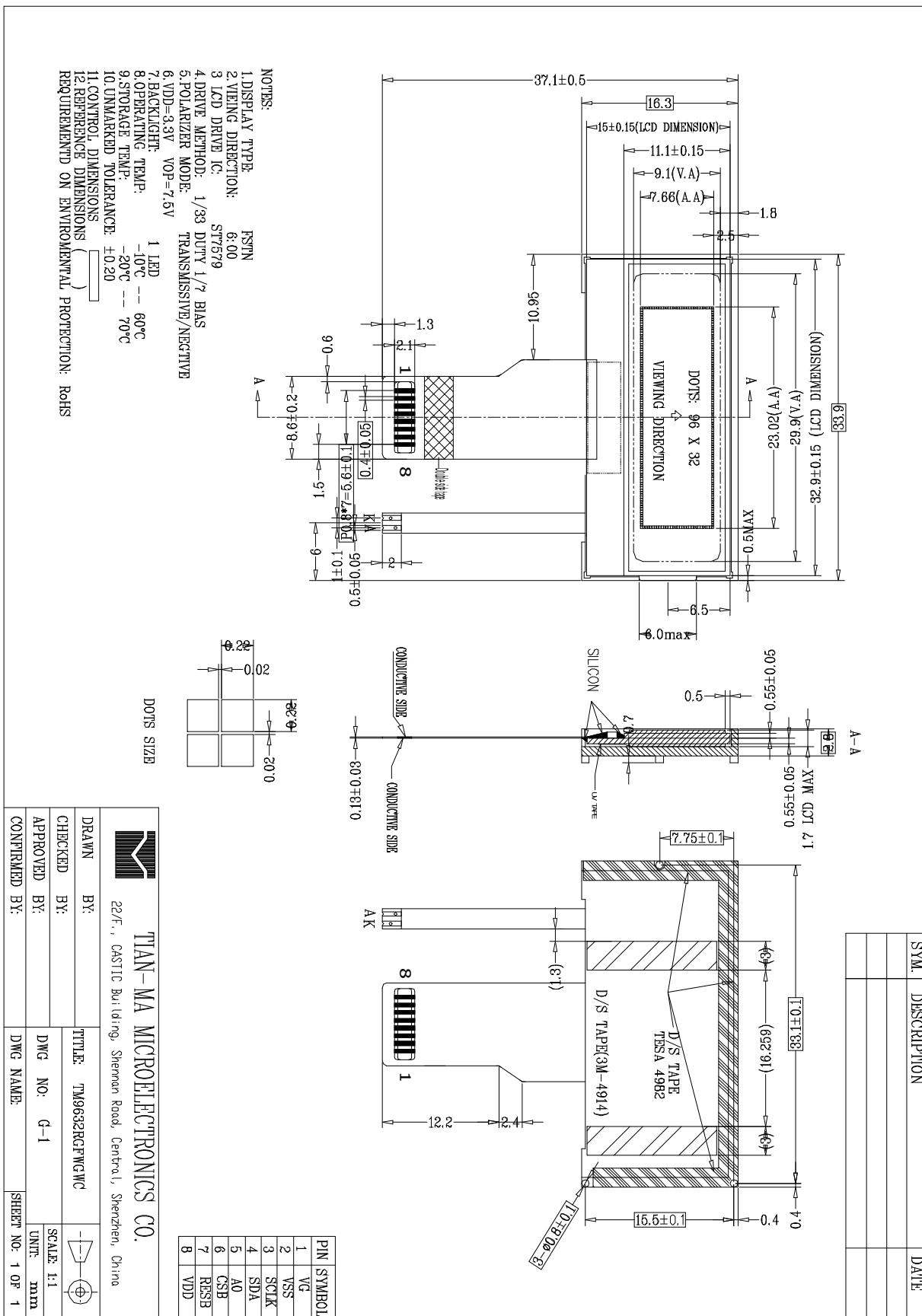
Note [3-4]: Only operation is guaranteed at operating temperature. Contrast, response time and another display quality are evaluated at +25°C.

## 4 Mechanical Characteristics

### 4.1 Mechanical features

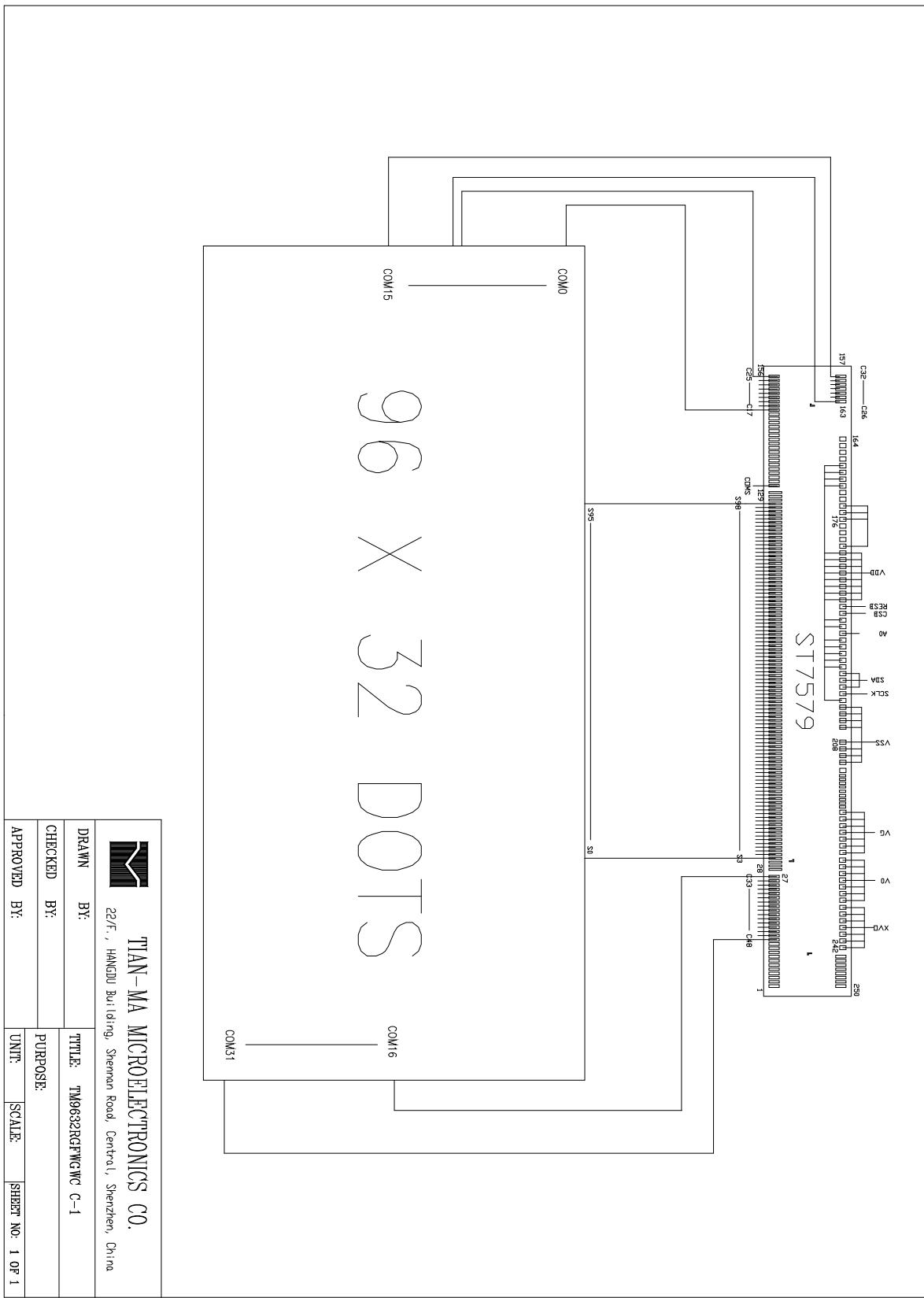
Parameter	Standard Value	Unit
Display type	Graphics Matrix LCM	--
Number of dots	96 × 32	--
View area (W×H)	29.9 × 9.1	mm
Active Area (W×H)	23.02 × 7.66	mm
Dot Size (W×H)	0.22 × 0.22	mm
Dot Pitch (W×H)	0.24 × 0.24	mm
Module size (W×H×D)	33.9 × 16.3 × 2.8 (without FPC)	mm
Module total weight (approx.)	(TBD)	g

## 4.2 Mechanical drawing



## 5 Circuit

### 5.1 Block Diagram



## 6 Interface description

Pin No.	Symbol	I/O	Description
1	VG	P	LCD driving voltage for segment. Connect a capacitor to Ground
2	VSS	P	Ground
3	SCLK	I	The clock input of SPI interface
4	SDA	I/O	The data line of SPI interface
5	A0	I	Data/instruction control selection pin. A0=High, it indicates that D[0:7] are data bits. A0=Low, D[0:7] are instruction bits
6	CSB	I	Chip selection input signal. Low active
7	RESB	I	Reset execution input signal. Low active
8	VDD	P	Power supply for LCD Driver

## 7 Instruction Code & Timing characteristics

### 7.1 Command and initial code

The module TM9632RGFWGWC includes ST7579 as its driver. After reset, we have to use some commands to set up the display. And we may use this when we want to change the display type. We explain some commands below. For more details, please refer to the datasheet of ST7579.

INSTRUCTION	A0	WR (R/W)	COMMAND BYTE								DESCRIPTION
			D7	D6	D5	D4	D3	D2	D1	D0	
<b>H independent instruction</b>											
NOP	0	0	0	0	0	0	0	0	0	0	No operation
Reserved	0	0	0	0	0	0	0	0	0	1	Do not use
Function set	0	0	0	0	1	MX	MY	PD	H1	H0	Power-down; entry mode; Extended instruction control
Read status byte	0	1	PD	0	0	D	E	MX	MY	DO	Read status byte
Read data	1	1	D <sub>7</sub>	D <sub>6</sub>	D <sub>5</sub>	D <sub>4</sub>	D <sub>3</sub>	D <sub>2</sub>	D <sub>1</sub>	D <sub>0</sub>	Read data to RAM
Write data	1	0	D <sub>7</sub>	D <sub>6</sub>	D <sub>5</sub>	D <sub>4</sub>	D <sub>3</sub>	D <sub>2</sub>	D <sub>1</sub>	D <sub>0</sub>	Write data to RAM

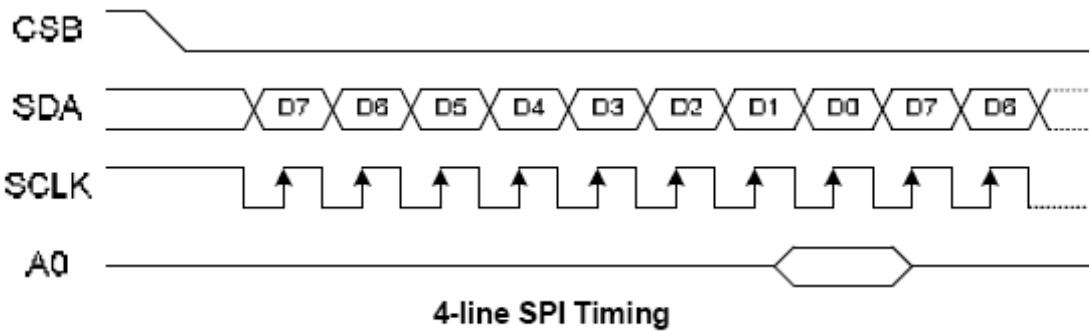
INSTRUCTION	A0	WR (R/W)	COMMAND BYTE								DESCRIPTION
			D7	D6	D5	D4	D3	D2	D1	D0	
<b>H[1:0]=[0:0]</b>											
Reserved	0	0	0	0	0	0	0	0	1	X	Do not use
Set V <sub>LCD</sub> range	0	0	0	0	0	0	0	1	0	PRS	V <sub>LCD</sub> range L/H select
END	0	0	0	0	0	0	0	1	1	0	Release read/modify/write
Read/modify/write	0	0	0	0	0	0	0	1	1	1	RAM address at R:+0, W:+1
Display control	0	0	0	0	0	0	1	D	0	E	Sets display configuration
Reserved	0	0	0	0	0	1	0	0	X	X	Do not use
Set Y address of RAM	0	0	0	1	0	0	Y <sub>3</sub>	Y <sub>2</sub>	Y <sub>1</sub>	Y <sub>0</sub>	Sets Y address of RAM 0≤Y≤9
Set X address of RAM	0	0	1	X <sub>6</sub>	X <sub>5</sub>	X <sub>4</sub>	X <sub>3</sub>	X <sub>2</sub>	X <sub>1</sub>	X <sub>0</sub>	Sets X address of RAM 0≤X≤101
<b>H[1:0]=[0:1]</b>											
Reserved	0	0	0	0	0	0	0	0	1	X	Do not use
Display configuration	0	0	0	0	0	0	1	DO	X	X	Top/bottom row mode set data order
Bias system	0	0	0	0	0	1	0	BS <sub>2</sub>	BS <sub>1</sub>	BS <sub>0</sub>	Sets bias system (BSx)
Set Start line S6	0	0	0	0	0	0	0	1	0	S6	Specify the initial display line S6
Set Start line	0	0	0	1	S5	S4	S3	S2	S1	S0	Specify the initial display line to realize vertical scrolling
Set V <sub>OP</sub>	0	0	1	V <sub>OP6</sub>	V <sub>OP5</sub>	V <sub>OP4</sub>	V <sub>OP3</sub>	V <sub>OP2</sub>	V <sub>OP1</sub>	V <sub>OP0</sub>	Write V <sub>OP</sub> to register



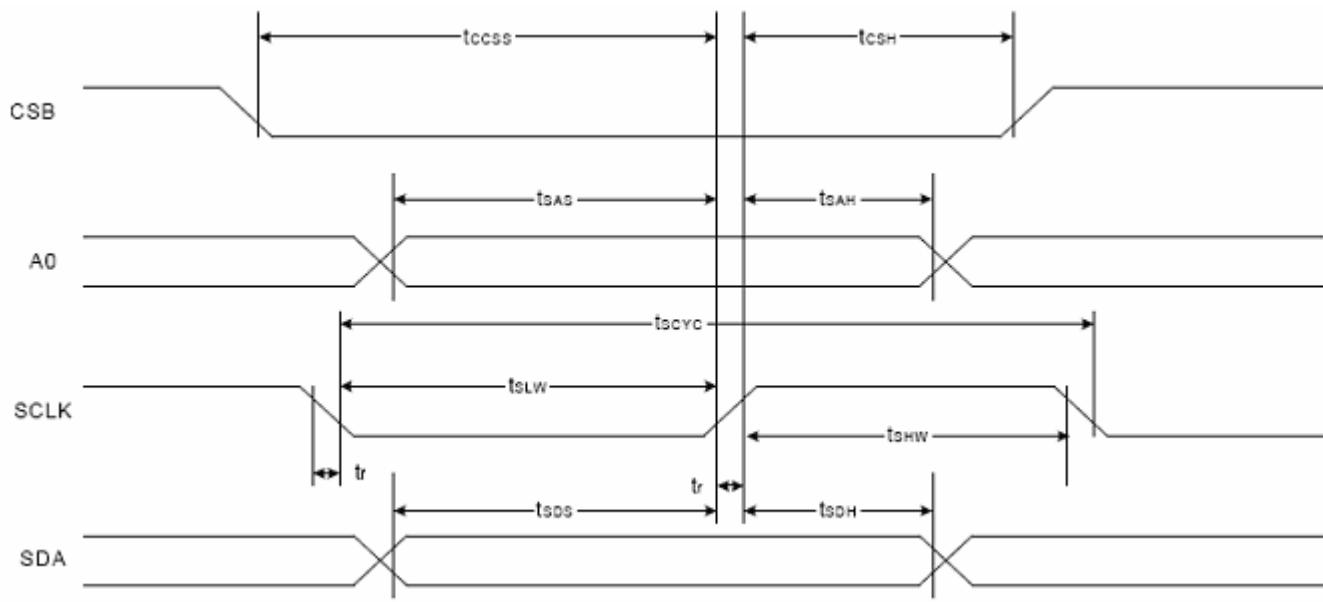
INSTRUCTION	A0	WR (R/W)	COMMAND BYTE								DESCRIPTION
			D7	D6	D5	D4	D3	D2	D1	D0	
<b>H[1:0]=[1:0]</b>											
Reserved	0	0	0	0	0	0	0	0	1	X	Do not use
Partial screen mode	0	0	0	0	0	0	0	1	0	PS	Partial screen enable
Partial screen size	0	0	0	0	0	0	1	0	0	WS	Set partial screen size
Display part	0	0	0	0	0	1	0	DP2	DP1	DP0	Set display part for partial screen mode
<b>H[1:0]=[1:1]</b>											
Test command mode	0	0	0	0	0	0	0	0	0	TEN	Test command enable/disable
RESET	0	0	0	0	0	0	0	0	1	1	Software reset
Display control	0	0	0	0	0	0	1	FR2	FR1	FR0	Frame rate control
N line inversion	0	0	0	1	0	NL4	NL3	NL2	NL1	NL0	Sets N line inversion
Booster Efficiency &Booster Stage(2)	0	0	1	0	0	1	BE1	BE0	PC1	PC0	Booster Efficiency Set
Reserved	0	0	1	X	X	X	X	X	X	X	Do not use

## 7.2 Interface Timing characteristics

Please refer to the datasheet of ST7579 for more details.



4-line SPI Timing

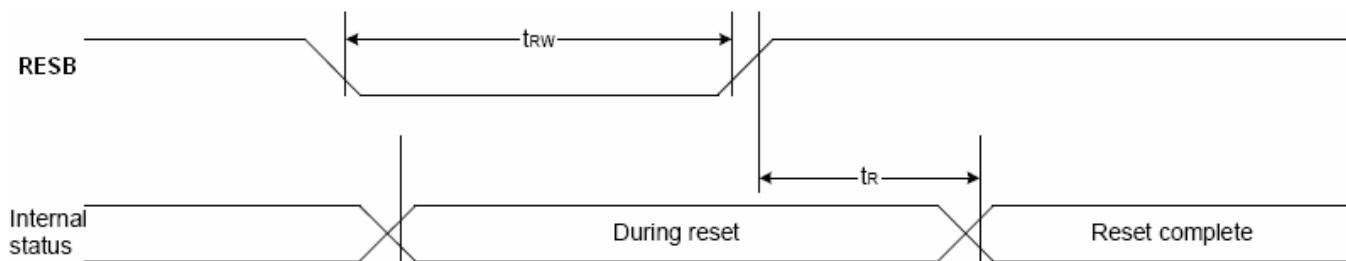


SERIAL INTERFACE(4-Line Interface)

(VDD=3.0V, Ta=-10°C ~ +60°C)

Item	Signal	Symbol	Condition	Rating		Units
				Min.	Max.	
Serial Clock Period	SCL	tSCYC		200	—	ns
SCL "H" pulse width		tSHW		100	—	
SCL "L" pulse width		tSLW		100	—	
Address setup time	A0	tSAS		30	—	ns
Address hold time		tSAH		120	—	
Data setup time	SI	tSDS		30	—	ns
Data hold time		tSDH		20	—	
CS-SCL time	CSB	tCSS		30	—	ns
CS-SCL time		tCSH		150	—	

### 7.3 Reset Timing



(VDD=3.0V, Ta=-10°C ~ +60°C)

Item	Signal	Symbol	Condition	Rating			Units
				Min.	Typ.	Max.	
Reset time		tR		—	—	2.0	us
Reset "L" pulse width	RESB	tRW		2.0	—	—	us

## 8 Electrical characteristics

Parameter	Symbol	Condition	MIN	TYP	MAX	UNIT
Logic circuit supply voltage	VDD	--	4.8	5.0	5.2	
Power supply LCD	V <sub>OP</sub>			7.5		
Input voltage for logic circuit	“H” level	V <sub>IH</sub>	0.8 VDD	VDD	VDD +0.3	V
	“L” level	V <sub>IL</sub>		VSS-0.3	VSS	
Output voltage for logic circuit	“H” level	V <sub>OH</sub>	--	VDD	--	
	“L” level	V <sub>OL</sub>	--	VSS	--	
Logic power supply current (Without backlighting)	I <sub>DD</sub>		--	(TBD)	--	mA
Used driver IC	SAP1024(Controller) + SDN8080(COM/SEG Driver)					

## 9 LED backlight characteristics

CIRCUIT DIAGRAM



Item	Symbol	Condition	MIN.	TYP.	MAX.	Unit	Remark
Forward voltage	V <sub>f</sub>	I <sub>f</sub> =17mA	3.0	3.2	3.4	V	---
Forward current	I <sub>f</sub>	V <sub>f</sub> =3.2V	--	17	---	mA	---
Lifetime	t	--	--	(TBD)	--	hour	Note [9-3]

Note [9-1]: The wavelength is measured with the bare LED backlight unit.

Note [9-2]: If the backlight is used above its' driving voltage or current for a long time, its lifetime will reduce or it will cause poor reliability.

Note [9-3]: The backlight lifetime lasts until the luminance reduces to 50% of its initial value.

## 10 Optical Characteristics

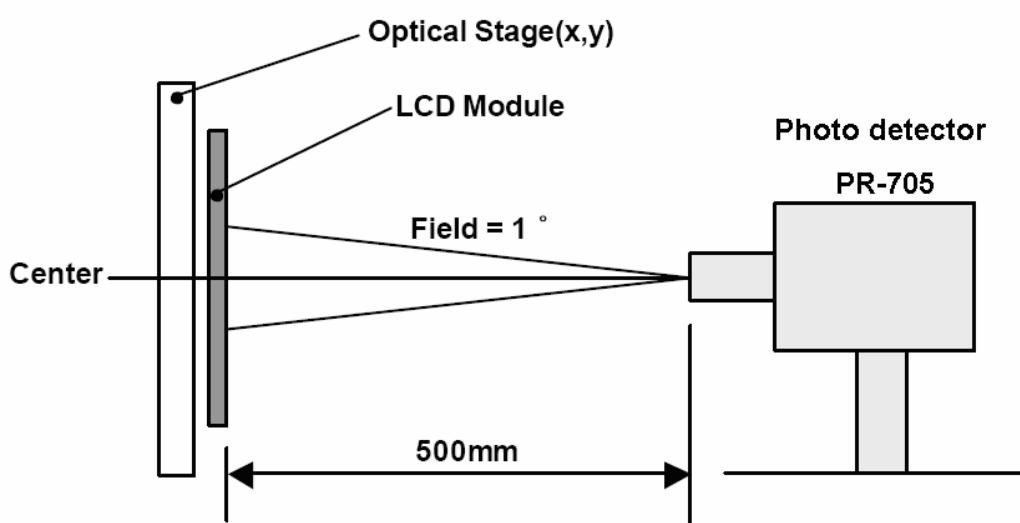
1/33Duty, 1/7Bias, Vo=7.5V(Note [10-2]), f=70Hz

Parameter	Symbol	Ratings			Unit	Measuring Temp.	Remark
		Min	Type	Max.			
Contrast ratio	$Cr$ ( $\theta=0^\circ, \Phi=0^\circ$ )	---	(TBD)	---	---	25°C	Note[10-1,3]
Response time	Turn on	$t_{on}$	---	(TBD)	---	ms	25°C
			---	(TBD)	---		0°C
	Turn off	$t_{off}$	---	(TBD)	---	ms	25°C
			---	(TBD)	---		0°C
Viewing angle ( $Cr \geq 2$ )	Up-down	$\theta_1$ ( $\Phi=0^\circ$ )	---	45	---	deg	25°C
			---	35	---		
	Left-right	$\theta_2$ ( $\theta=0^\circ$ )	---	30	---	deg	25°C
			---	30	---		
Luminance	$Y_L$ ( $\theta=0^\circ, \Phi=0^\circ$ )	---	(TBD)	---	cd/m <sup>2</sup>	25°C	Note[10-1] (I <sub>f</sub> =17mA)
Luminance Uniformity	$L_U$ ( $\theta=0^\circ, \Phi=0^\circ$ )	70	---	---	%	25°C	Note[10-1,6]

Note [10-1]: Measuring equipments: DMS-501, PR-705.

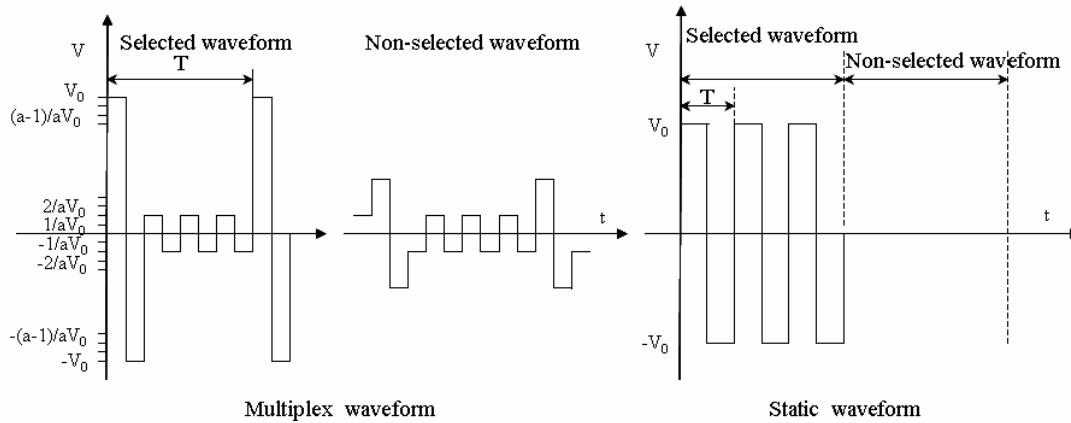
Measuring condition:

- After stabilizing and leaving the panel alone at a given temperature for 30 min, the measurement should be executed,
- Measuring surroundings: a stable, windless and dark room,
- Measuring temperature: Ta=25°C,
- 30 min after lighting the back-light.





Note [10-2]: The maximum and minimum ratings don't mean the LCD works well in the whole range of  $V_o$ .  $V_o$  must be adjusted to optimize the viewing angle and contrast. Refer to the following definition of drive voltage:



Operating voltage: $V_o$	Frame frequency: $f=1/T$
Duty: $1/N$	Bias: $1/a$

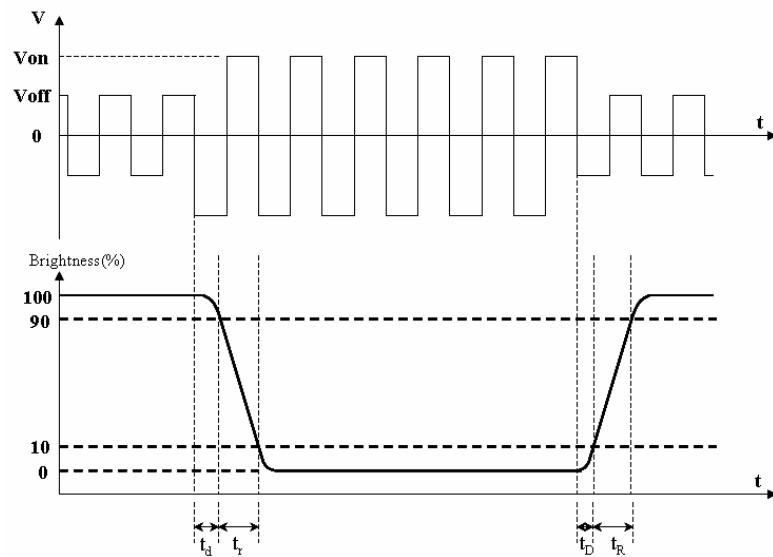
Note [10-3]: Contrast ratio(CR) is defined as follows:

$$CR = \frac{L_{ON} \text{ (Luminance of the ON segments)}}{L_{OFF} \text{ (Luminance of the OFF segments)}}$$

Note [10-4]: Definition of response time:

Turn on time (rise time):  $t_{on} = t_d + t_r$  (The time that the luminance level reaches 90% of the saturation level from 0% when ON signal is applied)

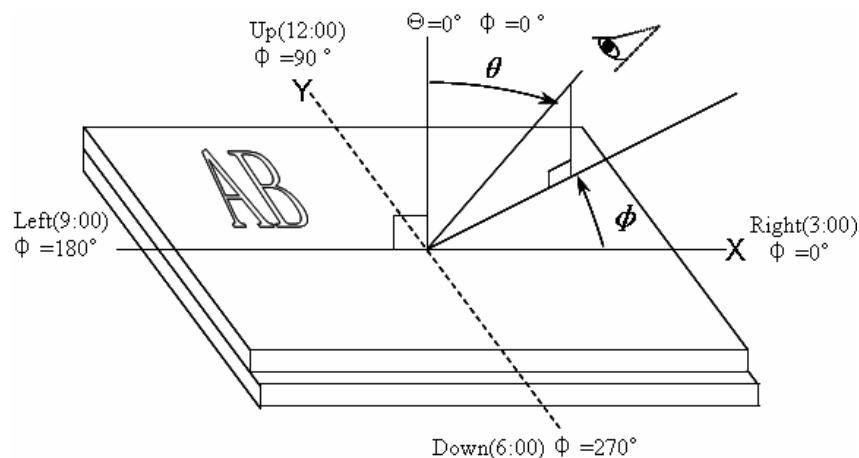
Turn off time (fall time):  $t_{off} = t_d + t_r$  (The time that the luminance level reaches 10% of the saturation level from 100% when OFF signal is applied)



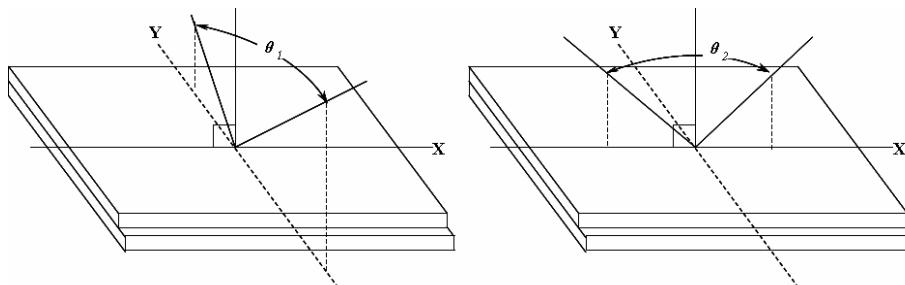


Note [10-5]: Definition of viewing direction

Refer to the picture below marked by  $\theta$  and  $\phi$



Definition of viewing angle



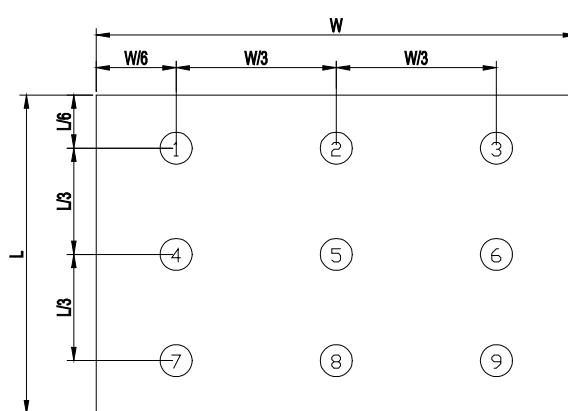
$\theta_1$  ——range of viewing angle from up to down;  $\theta_2$  ——range of viewing angle from left to right.

Note [10-6]: The definition of luminance uniformity:

The luminance uniformity is calculated by using following formula.

$$\text{Luminance uniformity (Lu)} = \frac{\text{Minimum luminance from } \square \text{ to } \square}{\text{Maximum luminance from } \square \text{ to } \square}$$

The luminance is measured at near the 9 points shown below.



## 11 Reliability

### 11.1 Content of Reliability Test

No.	Test Item	Test condition	Remark	Criterion
1	High Temperature Storage	Ta=70°C, 120H		Remark1 Remark2 Remark3 Remark4
2	Low Temperature Storage	Ta=-20°C, 120H	Note[11-1]	
3	High Temperature Operation	Ta=60°C, 120H		
4	Low Temperature Operation	Ta=-10°C, 120H	Note[11-1]	
5	High Temperature & High Humidity Operation	Ta=60°C, 90%RH, 120H	Note[11-1]	
6	Temperature Cycle Test (Non-Operating)	-20 °C↔+25 °C↔+70 °C, 100 Cycles 30min    5min    30min	Note[11-1]	
7	Vibration Test	Frequency: 10 ~150 Hz, Stroke: 1.5mm Sweep time: 11 min Test Period: 6 cycles for each direction of X,Y,Z,120 min every direction	Note[11-2]	Remark1 Remark5
8	Shock Test	Waveform : Half Sinusoidal Wave Shock Level:50 G, Pulse Width:11 ms, Direction: ±X, ±Y, ±Z, Cycle:3 times	Note[11-2]	Remark6

Note [11-1]: No dew condensation to be observed.

Note [11-2]: Vibration test will be conducted to the product itself without putting it in a container.

Note [11-3]: The test sample is inspected after 2 hours or more storing at room temperature and room humidity after each test item is finished.

Note [11-4]:The criteria refer to 11.2.

## 11.2 Inspection of criteria

Remark NO.	Content
1	Function test is OK. Missing Segment, shorts, unclear segment, non-display, display abnormally, liquid crystal leak are unallowable.
2	After testing, cosmetic defects should not happen, no low temperature bubbles, seal loose and fall, frame rainbow, ACF bubble growing are unallowable in the appearance test.
3	Total current consumption should not be over 10% of initial value.
4	After tests, the contrast ratio must be larger than 70% of its value before.
5	No glass crack, chipped glass, end seal loose frame crack and so on.
6	No structure looseness.

## 12 Package

**TBD**

## 13 Quality level

Examination or Test	At $T_a=25^{\circ}\text{C}$ (Unless otherwise stated)	Inspection				
		Min	Max	Unit	IL	AQL
External Visual Inspection	Under normal illumination and eyesight condition, the distance between eyes and LCD is 25cm.	Refer to appendix A		II	Major 1.0 Minor 2.5	
Display Defects	Under normal illumination and eyesight condition, display on inspection.	Refer to appendix B		II	Major 1.0 Minor 2.5	
Note: Major defects: Open segment or common, Short, Serious damages, Leakage Minor defects: Others Sampling standard conforms to GB2828						

## 14 Precautions for Use of LCD Modules

### 14.1 Handling Precautions

- 14.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 14.1.2 Liquid in LCD is hazardous substance, if the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, thoroughly and promptly wash it off using soap and water.
- 14.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 14.1.4 Don't touch, push or rub the exposed polarizer covering the display surface of the LCD module with anything harder than an HB pencil lead, the polarizer is soft and easily scratched, handle it carefully.
- 14.1.5 Don't put or attach anything on the display area to avoid leaving any marks on.
- 14.1.6 If the display surface is contaminated or becomes dusty, breathe on the surface and gently wipe it with a soft dry cloth. do not scrub hard to avoid damage the surface. If still not completely clear, moisten cloth with one of the following solvents:
  - Isopropyl alcohol
  - Ethyl alcohol
- Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:
  - Water
  - Ketone
  - Aromatic solvents
- 14.1.7 Do not attempt to disassemble the LCD Module.
- 14.1.8 If the logic circuit power is off, do not apply the input signals.
- 14.1.9 Avoid using the same display pattern long time (continuous ON segment).Software must be prepared so that the pattern will be changed

14.1.10 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

- a. Be sure to ground the body and electric appliances when handling the LCD Modules. It is preferable to use conductive mat on table and wear cotton clothes or conductive processed fiber. Synthetic fiber is not recommended.
- b. Tools required for assembly, such as soldering irons, must be properly ground.
- c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
- d. The LCD Module is coated with a film to protect the display surface. Be careful and slow when peeling off this protective film since static electricity may be generated. It is recommended to use ionic fan or machine when operating. It is recommended to remove the protection foil slowly (> 3 sec.).
- e. It is preferable to wear gloves etc, to avoid damaging the LCD. Please do not touch electrodes with bare hands or avoid any other contamination.

## 14.2 Storage precautions

14.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

14.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 5°C ~ 40°C

Relatively humidity: ≤80%

14.2.3 The LCD modules should be stored in a clean environment or room, free from acid, alkali and harmful gas.

14.2.4 Store the module in anti-static electricity container and without any physical load.

## 14.3 Transportation precautions

The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

## 14.4 Soldering

14.4.1 Use the high quality solders, only solder the I/O terminals.

14.4.2 No higher than 280°C and time less than 3-4 second during soldering.

14.4.3 Rewiring: no more than 3 times.

14.4.4 when you remove connector or cable soldered to I/O terminals, please confirm that solder is fully melted. If you remove by force, electrodes at I/O terminals may be damaged (or stripped off). It is recommended to use solder suction machine.

## 15. LCD Module Part Numbering System

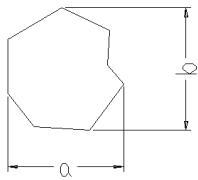
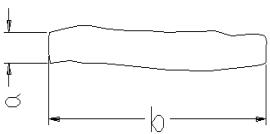
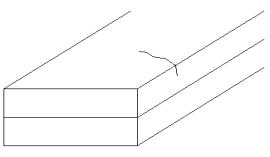
**TM** **9632** **R** **G** **F** **W** **G** **W** **C**

(1) (2) (3) (4) (5) (6) (7) (8) (9)

NO.	Explanation	
(1)	<b>TIANMA module indicating</b>	
(2)	<b>Module type: 96 columns×32 rows</b>	
(3)	<b>TIANMA module series: R series</b>	
(4)	<b>LCD type</b>	
	G	Negative, White Backlight, FSTN
(5)	<b>Backlight type</b>	
	F	Transmissive, LED
(6)	<b>Temperature range</b>	
	W	Wide temperature
(7)	<b>Technology</b>	
	G	COG
(8)	<b>The color of backlight</b>	
	W	White LED
(9)	<b>Module type</b>	
	C	DC/DC converter is included in the LCD driver

## Appendix A

### Inspection items and criteria for appearance defects

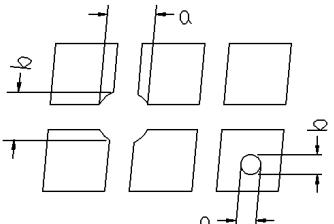
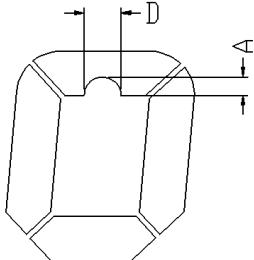
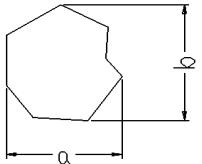
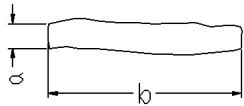
Items	Contents	Criteria		
Protective Glue		No clear defects		
Cover Tape		Covering all of the chip and no clear crimp		
Leakage		Not permitted		
Rainbow		According to the limit specimen		
Polarizer	Wrong polarizer attachment	Not permitted		
	Bubble between polarizer and glass	Not counted	Max. 3 defects allowed	
		$\phi < 0.3\text{mm}$	$0.3\text{mm} \leq \phi \leq 0.5\text{mm}$	
	Scratches of polarizer	According to the limit specimen		
Black spot (in viewing area)		Not counted	Max. 3 spots allowed	
		$X < 0.20\text{mm}$	$0.20\text{mm} \leq X \leq 0.5\text{mm}$	
		$X = (a+b)/2$		
Black line (in viewing area)		Not counted	Max. 3 lines allowed	
		$a < 0.02\text{mm}$	$0.02\text{mm} \leq a \leq 0.05\text{mm}$ $b \leq 2.0\text{mm}$	
Progressive cracks		Not permitted		

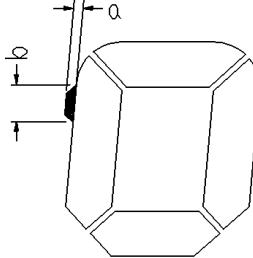
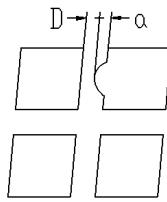
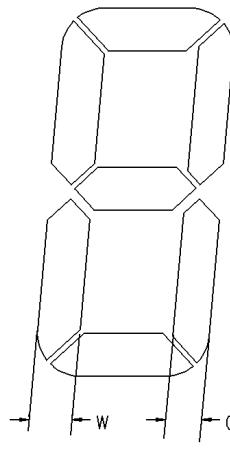


Item	Contents	Criteria			
Glass Cracks	Cracks on pads	a $\leq 3\text{mm}$	b $\leq W/5$	c $\leq T/2$	Max. 2 Cracks allowed
		$\leq 2\text{mm}$	$\leq W/5$	$T/2 < C < T$	
	Cracks on contact side	a $\leq 3\text{mm}$	b $\leq T/2$	C shall be not reach the seal area	Max. 5 cracks allowed
	Cracks on non-contact side	a $\leq 3\text{mm}$	b $\leq T/2$	$T/2 < b < T$	
		$\leq 2\text{mm}$	$C \leq 0.5\text{mm}$	$d \leq SW/3$	
Corner cracks		$e < 2.0\text{mm}^2$ $f < 2.0\text{mm}^2$			Max. 3 cracks allowed

## Appendix B

### Inspection items and criteria for display defects

Items	Contents	Criteria	
Open segment or open common	Not permitted		
Short	Not permitted		
Wrong viewing angle	Not permitted		
Contrast radio uneven	According to the limit specimen		
Crosstalk	According to the limit specimen		
Pin holes and cracks in segment (DOT)		Not counted	Max.3 dots allowed
		X<0.1mm	0.1mm≤X≤0.2mm
		$X=(a+b)/2$	
		Not counted	Max.2 dots allowed
Black spot (in viewing area)		A<0.1mm	0.1mm≤A≤0.2mm D<0.25mm
		Not counted	Max.3 spots allowed
		X<0.1mm	0.1mm≤X≤0.2mm
Black line (in viewing area)		Not counted	Max.3 lines allowed
		a<0.02mm	0.02mm≤a≤0.05mm b≤0.5mm
			Max.3 spots (lines) allowed

Items	Content	Criteria		
Transformation of segment		Not counted	Max. 2 defects allowed	
		$x < 0.1\text{mm}$	$0.1\text{mm} \leq x \leq 0.2\text{mm}$	
		$x = (a+b)/2$		Max.3 defects allowed
		Not counted	Max. 1 defects allowed	
		$a < 0.1\text{mm}$	$0.1\text{mm} \leq a \leq 0.2\text{mm}$ $D > 0$	
		Max.2 defects allowed $0.8W \leq a \leq 1.2W$  $a$ = measured value of width $W$ = nominal value of width		