

GENERAL SPECIFICATION

Item	Content
Number of Character	320x240
Module Size	167.1(W)x109.0(H)x11.0(D)mm Max
Viewing Area	122.0(W)x92.0(H)mm
Dot Size/Dot Pitch	0.34(W)x0.34(H)mm/0.36(W)x0.36(H)mm
Backlight	CCFL
Options	Black & White Positive/Negative/Extended Temperature/Bottom Viewing/with Touch Screen
Built-In Controller	SED1335

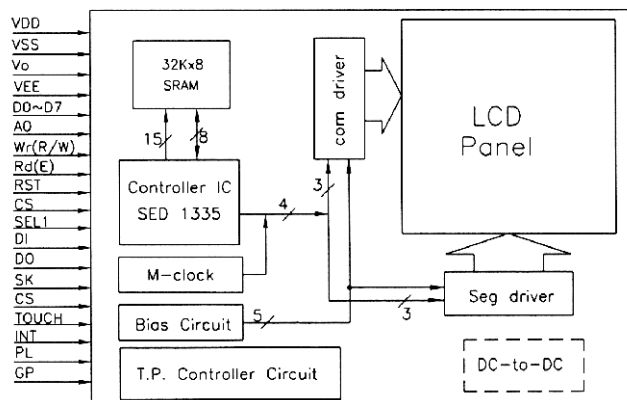
ELECTRICAL CHARACTERISTICS

Item	Symbol	Condition	Min.	Typ	Max.	Unit	note
Power Supply for Logic	$V_{DD-V_{SS}}$	-	2.7	4.5	5.5	Volt	-
Input Voltage	V_{IL}	L level	V_{SS}	$0.2V_{DD}$	-		-
	V_{IH}	H level	$0.8V_{DD}$	V_{DD}	-		
LCM Recommend LCD Module Driving Voltage	V_{DD-V_O}	Ta=0°C	21.7	22.2	22.7	Volt	-
		Ta=25°C	20.8	21.2	21.6		
		Ta=50°C	20.1	20.6	21.1		
Power Supply Current for LCM	$I_{DD}(B/L \text{ OFF})$	$V_{DD}=4.5V$ $Ta=25^{\circ}C$	25	27.5	30	mA	-
	I_{EE}	$V_{EE-V_{SS}}=21.2V$	2.5	3.3	3.8		
CCFL Starting Voltage	V_{FLS}	FLM=64Hz Ta=25°C	-	750	-	V_{RMS}	
CCFL driving Voltage	V_{CCFL}	$V_{CCFL}=450V_{RMS}$ $F_{CCFL}=30KHz$ Ta=25°C	-	360	-	V_{RMS}	-
CCFL driving Current	I_{CCFL}		-	5.0	-	mA	
CCFL driving Frequency	F_{CCFL}		15	30	85	KHz	
CCFL Saturation Time	T_{SAT}		-	1	-	minut	

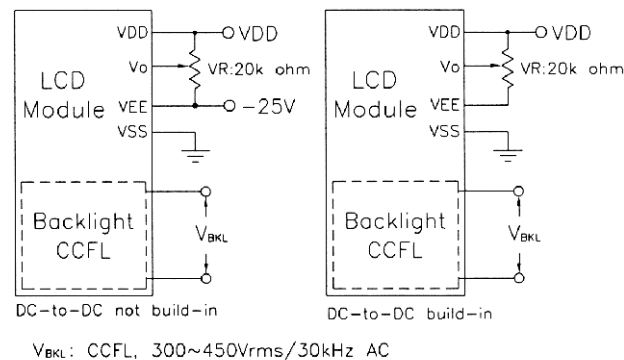
INTERFACE PIN ASSIGNMENT

Pin No.	Pin Out	Function Description
1	V _{SS}	Ground
2	V _{DD}	Logic supply voltage
3	V _O	Negative voltage power supply. Tuned from V _{DD} -V _{EE}
4	A _O	Data type select
5	WR(R/W)	8080 family: write signal 6800 family: R/W signal
6	RD(E)	8080 family: read signal 6800 family: Enable clock
7~14	DB0~DB7	3-state I/O Data bus.
15	CS	Chip select. This active-Low input enables the SED1335F . It's usually connected to the output of an address decoder device that maps the SED1335F into the memory space of the controlling microprocessor.
16	RES	This active-Low input performs hardware reset on the SED1335F . It's a Schmitt-trigger input for enhanced noise immunity; however, care should be taken to ensure that it is not triggered if the supply voltage is lowered.
17	V _{EE}	Supply voltage for LCD panel
18	SEL1	"0" for 8080 family MPU, "1" for 6800 family CPU.
19	DI	Data in of touch screen controller
20	DO	Data out of touch screen controller
21	SK	Serial clock for touch screen controller
22	CS	Chip select for touch screen controller. Active high.
23	TOUCH	Touch signal for touch screen controller. Goes low when screen is touched. Optional clock out.
24	INT	Interrupt. Goes high to signal interrupt. Optional clock output
25	PL	Connect 5th wire on 5 wire screens, or general purpose ADC input
26	GP	General purpose ADC input

BLOCK DIAGRAM



POWER SUPPLY



MECHANICAL

