

GENERAL SPECIFICATION

Item	Content
Number of Character	320x240
Module Size	167.1(W)x109.0(H)x11.0(D)mm Max
Viewing Area	122.0(W)x92.0(H)mm
Dot Size/Dot Pitch	0.34(W)x0.34(H)mm/0.36(W)x0.36(H)mm
Backlight	CCFL
Options	Black & White Positive/Negative/Extended Temperature/Bottom Viewing
Built-In Controller	SED1335

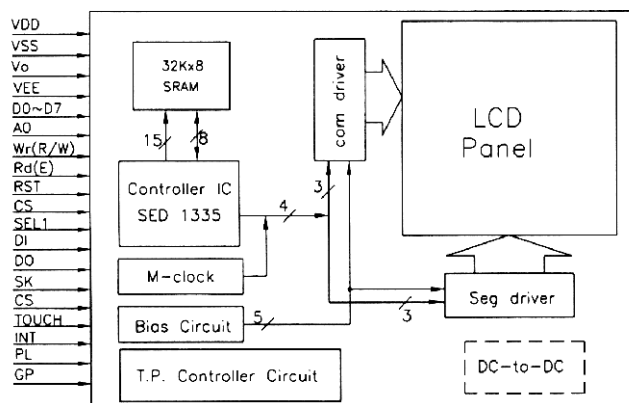
ELECTRICAL CHARACTERISTICS

Item	Symbol	Condition	Min.	Typ	Max.	Unit	note
Power Supply for Logic	$V_{DD}-V_{SS}$	-	2.7	4.5	5.5	Volt	-
Input Voltage	V_{IL}	L level	V_{SS}	$0.2V_{DD}$	-	Volt	-
	V_{IH}	H level	$0.8V_{DD}$	V_{DD}	-		
LCM Recommend LCD Module Driving Voltage	$V_{DD}-V_O$	$T_a=0^{\circ}\text{C}$	21.7	22.2	22.7	Volt	-
		$T_a=25^{\circ}\text{C}$	20.8	21.2	21.6		
		$T_a=50^{\circ}\text{C}$	20.1	20.6	21.1		
Power Supply Current for LCM	$I_{DD}(\text{B/L OFF})$	$V_{DD}=4.5\text{V}$ $T_a=25^{\circ}\text{C}$	-	30	35	mA	-
	I_{EE}	$V_{EE}-V_{SS}=21.2\text{V}$	-	2.9	4		
CCFL Starting Voltage	V_{FLS}	FLM=64Hz $T_a=25^{\circ}\text{C}$	-	750	-	V_{RMS}	-
CCFL driving Voltage	V_{CCFL}	-	-	360	-	V_{RMS}	-
CCFL driving Current	I_{CCFL}	$V_{CCFL}=450V_{RMS}$ $F_{CCFL}=30\text{KHz}$ $T_a=25^{\circ}\text{C}$	-	5.0	-	mA	-
CCFL driving Frequency	F_{CCFL}	-	15	30	85	KHz	-
CCFL Saturation Time	T_{SAT}	-	-	1	-	minut	-

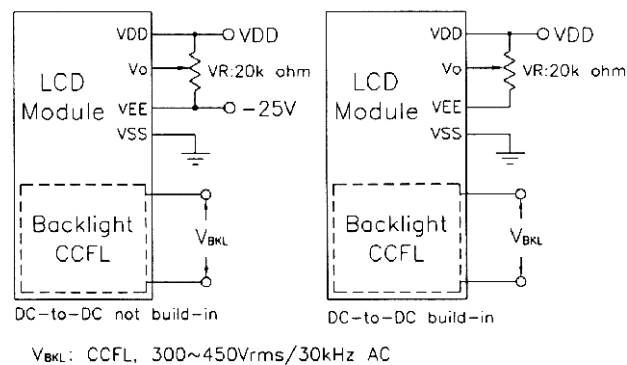
INTERFACE PIN ASSIGNMENT

Pin No.	Pin Out	Function Description
1	V_{SS}	Ground
2	V_{DD}	Logic supply voltage
3	V_O	Negative voltage power supply. Tuned from $V_{DD}-V_{EE}$
4	A_O	Data type select
5	WR(R/W)	8080 family: write signal 6800 family: R/W signal
6	RD(E)	8080 family: read signal 6800 family: Enable clock
7~14	DB0~DB7	3-state I/O Data bus.
15	CS	Chip select. This active-Low input enables the SED1335F . It's usually connected to the output of an address decoder device that maps the SED1335F into the memory space of the controlling microprocessor.
16	RES	This active-Low input performs hardware reset on the SED1335F . It's a Schmitt-trigger input for enhanced noise immunity; however, care should be taken to ensure that it is not triggered if the supply voltage is lowered.
17	V_{EE}	Supply voltage for LCD panel
18	SEL1	"0" for 8080 family MPU, "1" for 6800 family CPU.
19~22	NC	No connection

BLOCK DIAGRAM



POWER SUPPLY



MECHANICAL

