

GENERAL SPECIFICATION

Item	Content
Number of Character	122x32
Module Size	59.0(W)x29.3(H)x5.5(D)mm Max
Viewing Area	52.0(W)x15.0(H)mm
Dot Size/Dot Pitch	0.345(W)x0.345(H)mm/0.375(W)x0.375(H)mm
Backlight	Without/LED
Options	Gray STN/Yellow STN, Extended Temperature, Bottom Viewing
Built-In Controller	SED1520DAA

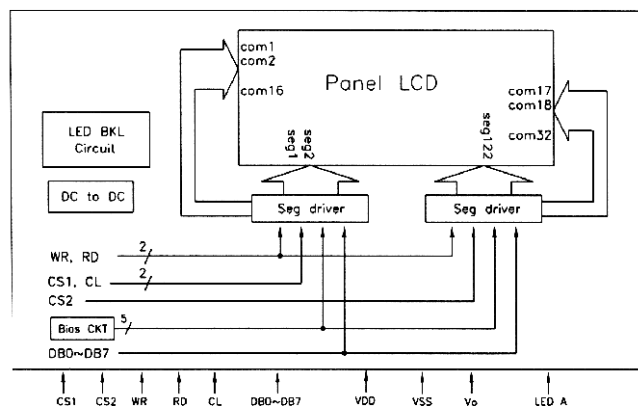
ELECTRICAL CHARACTERISTICS

Item	Symbol	Condition	Min.	Typ	Max.	Unit	note
Power Supply for Logic	$V_{DD}-V_{SS}$	-	2.4	5.0	7.0	Volt	-
Input Voltage	V_{IL}	L level	V_{SS}	$0.2V_{DD}$	-	Volt	-
	V_{IH}	H level	$0.8V_{DD}$	V_{DD}	-	Volt	
LCM Recommend LCD Module Driving Voltage	$V_{DD}-V_O$	$T_a=0^{\circ}\text{C}$	-	-	-	Volt	-
		$T_a=25^{\circ}\text{C}$	4.0	4.3	4.6		
		$T_a=50^{\circ}\text{C}$	-	-	-		
Power Supply Current for LCM	I_{DD} (LED B/L OFF)	$V_{DD}=5.0\text{V}$ $V_{DD}-V_O=4.3\text{V}$ FLM=64Hz	-	0.6	1.1	mA	-
	I_{EE} (LED B/L ON)	$V_{LED}=4.2\text{V}$	-	45	60		

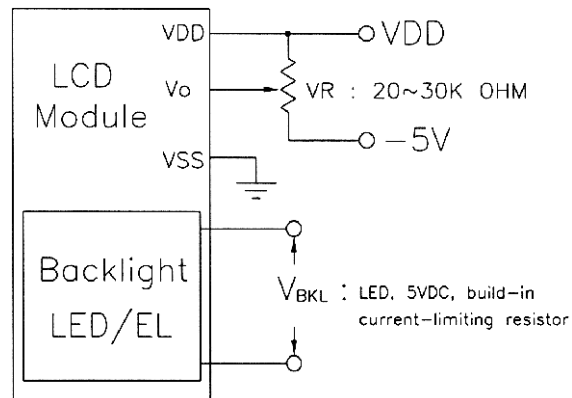
INTERFACE PIN ASSIGNMENT

Pin No.	Pin Out	Function Description
1	V_{SS}	GND
2	V_{DD}	Logic supply voltage
3	V_O	LCD driver supply voltage
4	A_O	Display Data/Display commands switching input. $A_O=0$: DB0~DB7 are command input and status output. $A_O=1$: DB0~DB7 are Display Data input/output.
5	CS1	Chip select signal for the left half of the screen. Active Low
6	CS2	Chip select signal for the right half of the screen. Active Low
7	CL	An external 2k Hz clock input.
8	/RD	Read, active Low for 80 series
9	/WR	Write, active Low for 80 series
10~17	DB0~DB7	3-state I/O data bus.
18	RES	Reset signal
19	LED A	Power supply for LED backlight
20	LED K	

BLOCK DIAGRAM



POWER SUPPLY



MECHANICAL

