DISPLAYTRONIC

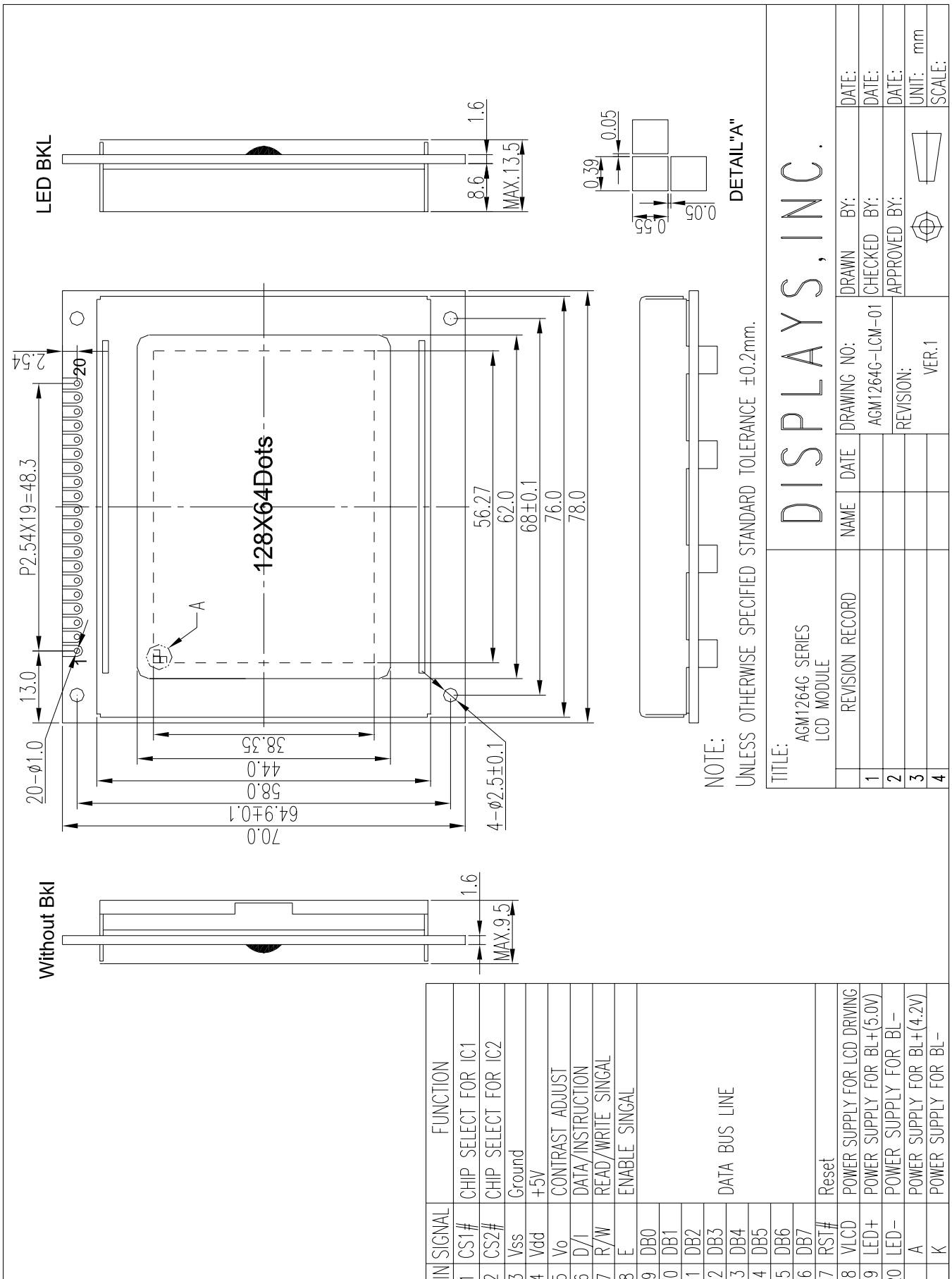
A DIVISION OF ZE XIAMEN CO., LTD.

SPECIFICATIONS FOR LIQUID CRYSTAL DISPLAY

PART NUMBER: **AGM 1264G SERIES**

DATE: **MAY 09, 2005**

1.0 MECHANICAL DIAGRAM



PIN	SIGNAL	FUNCTION
1	CS1#	CHIP SELECT FOR IC1
2	CS2#	CHIP SELECT FOR IC2
3	Vss	Ground
4	Vdd	+5V
5	Vo	CONTRAST ADJUST
6	D/I	DATA/INSTRUCTION
7	R/W	READ/WRITE SINGAL
8	E	ENABLE SINGAL
9	DB0	DATA BUS LINE
10	DB1	
11	DB2	
12	DB3	
13	DB4	
14	DB5	
15	DB6	
16	DB7	
17	RST#	Reset
18	VLCD	POWER SUPPLY FOR LCD DRIVING
19	LED+	POWER SUPPLY FOR BL+(5.0V)
20	LED-	POWER SUPPLY FOR BL-
	A	POWER SUPPLY FOR BL+(4.2V)
	K	POWER SUPPLY FOR BL-

2.0 MECHANICAL SPECS

1. Overall Module Size	78.0mm(W) x 70.0mm(H) x max 13.5mm(D) for LED backlight version 78.0mm(W) x 70.0mm(H) x max 9.5mm(D) for reflective version
2. Dot Size	0.39mm(W) x 0.55mm(H)
3. Dot Pitch	0.44mm(W) x 0.60mm(H)
4. Duty	1/64
5. Controller IC	KS0108B
6. LC Fluid Options	STN
7. Polarizer Options	Reflective
8. Backlight Options	LED
9. Temperature Range Options	-20ºC ~ 70ºC
10. Temperature Range Storage	-30ºC ~ 80ºC

3.0 ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min	Typ	Max	Unit
Operating temperature (Standard)	Top	0	-	50	ºC
Storage temperature (Standard)	Tst	-10	-	60	ºC
Operating temperature (Wide temperature)	Top	-20	-	70	ºC
Storage temperature (Wide temperature)	Tst	-30	-	80	ºC
Input voltage	Vin	Vss		Vdd	V
Supply voltage for logic	Vdd- Vss	-0.3	-	6.0	V
Supply voltage for LCD drive	Vdd- Vo		-	17.0	V

4.0 ELECTRICAL CHARACTERISTICS

Item	Symbol	Condition	Min	Typ	Max	Unit
Input voltage (high)	Vih	H level	3.5	-	Vdd	V
Input voltage (low)	Vil	L level	0	-	1.5	V
Recommended LC Driving Voltage (Wide Temp)	Vdd -Vo	-20ºC	-	11.5	14.0	V
		0ºC	-	10.9	-	
		50ºC	-	9.7	-	
		70ºC	-	9.1	-	
Power Supply Current	Idd	Vdd=5.0V	-	-	15.0	mA
LED Power Supply Voltage	VA-VK	Ifled =440mA-	-	4.4	-	V
LED Power Supply Current	Ifled	-	-	440	880	mA

5.0 OPTICAL CHARACTERISTICS

Item / Mode		Cr (Contrast Ratio)		θ (Viewing Angle)		ϕ (Viewing Angle)	
		25 ºC		25 ºC		25 ºC	
		MIN.	TYP.	MIN	TYP.	MIN	TYP.
R	A	2.80	3.05	80°	85°	-	35°
	B	7.10	7.70	80°	85°	-	35°
	C	-	-	-	-	-	-

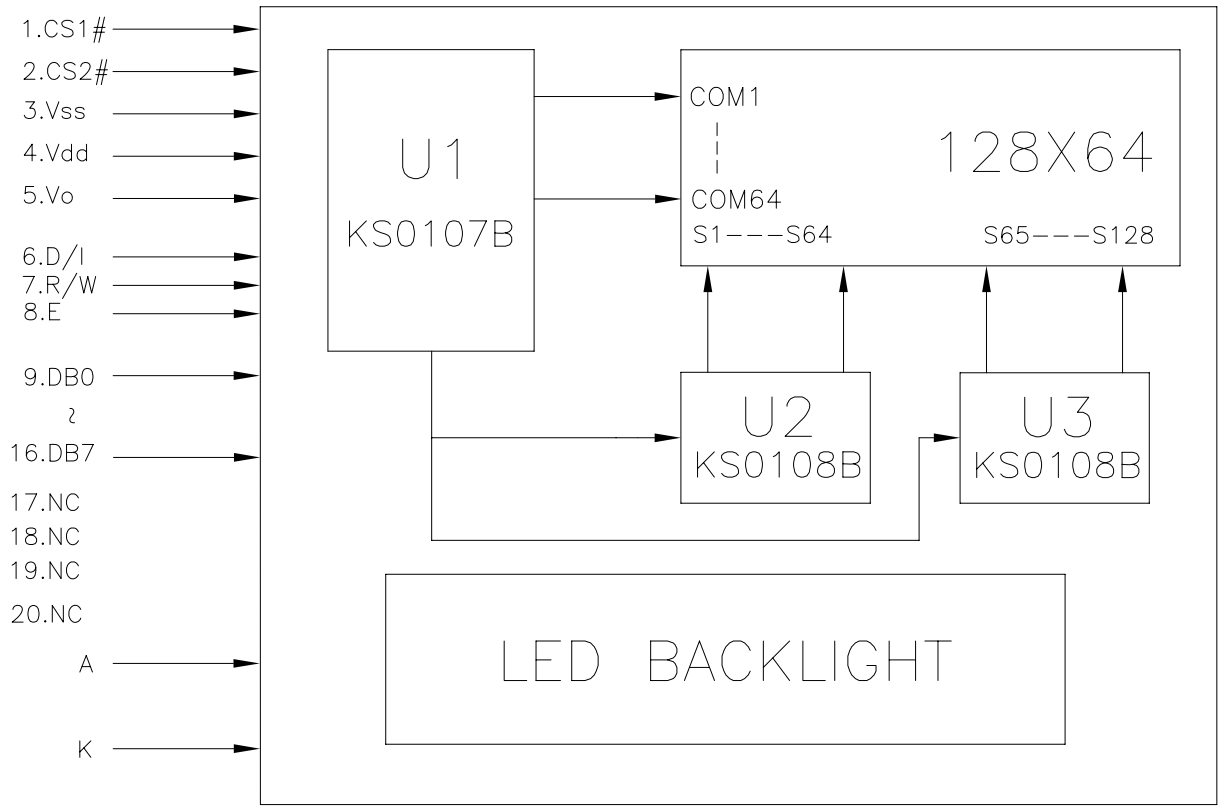
Note:

R: Reflective
S: Transflective
A: STN Gray
B: STN Yellow
C: FSTN

At: ϕ=0°, θ=0°

Item	Symbol	Condition	Min	Typ	Max	Unit
Response time (rise)	Tr	25 ºC	-	140	280	ms
Response time (fall)	Tf	25 ºC	-	80	160	ms

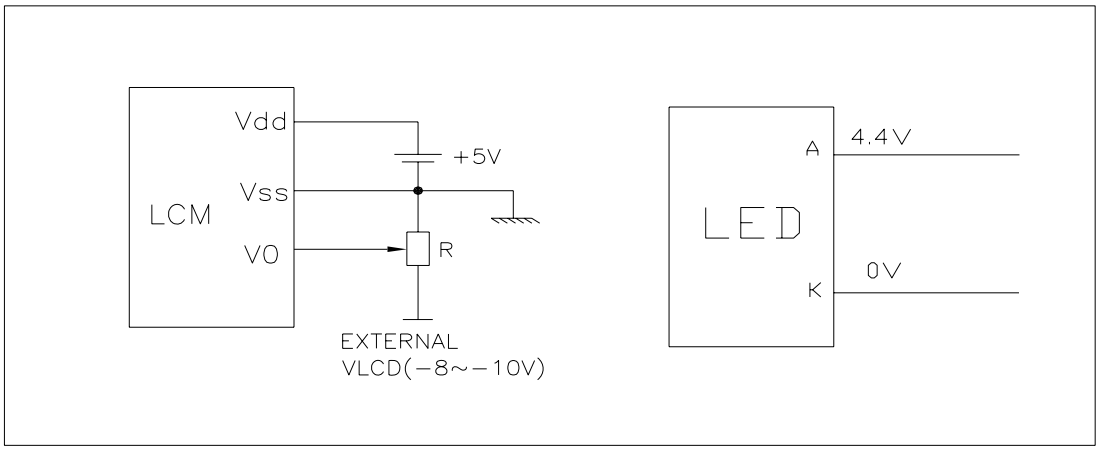
6.0 BLOCK DIAGRAM



7.0 PIN ASSIGNMENT

Pin No.	Symbol	Function	Level
1	CS1#	Chip selection for IC1	L
2	CS2#	Chip selection for IC2	L
3	Vss	Ground	-
4	Vdd	+5V	-
5	Vo	LCD contrast adjust voltage(VDD-V0=10.33V)	-
6	D/I	H: Data input L: Instruction code input	H/L
7	R/W	H: Data read L: Data write	H/L
8	E	Enable signal	H,H → L
9	DB0	Data bit 0	H/L
10	DB1	Data bit 1	H/L
11	DB2	Data bit 2	H/L
12	DB3	Data bit 3	H/L
13	DB4	Data bit 4	H/L
14	DB5	Data bit 5	H/L
15	DB6	Data bit 6	H/L
16	DB7	Data bit 7	H/L
17	NC		
18	NC		
19	NC		
20	NC		
-	A	Power Supply for BL+(4.4V)	-
-	K	Power Supply for BL-	-

8.0 POWER SUPPLY



R=10K~20KΩ

9.0 TIMING CHARACTERISTICS

Item	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Enable cycle time	t_{CYC}	Fig. a, Fig. b	1000	-	-	ns
E high level width	t_{WH}	Fig. a, Fig. b	450	-	-	ns
E low level width	t_{WL}	Fig. a, Fig. b	450	-	-	ns
Enable rise/fall time	T_r , t_f	Fig. a, Fig. b	-	-	25	ns
Address set up time	t_{AS}	Fig. a, Fig. b	140	-	-	ns
Address hold time	t_{AH}	Fig. a, Fig. b	10	-	-	ns
Data delay time	t_{DDR}	Fig. b	-	-	320	ns
Data set up time	t_{DSW}	Fig. a	200	-	-	ns
Data hold time (Write)	t_{DHW}	Fig. a	10	-	-	ns
Data hold time (Read)	t_{DHR}	Fig. b	20	-	-	ns

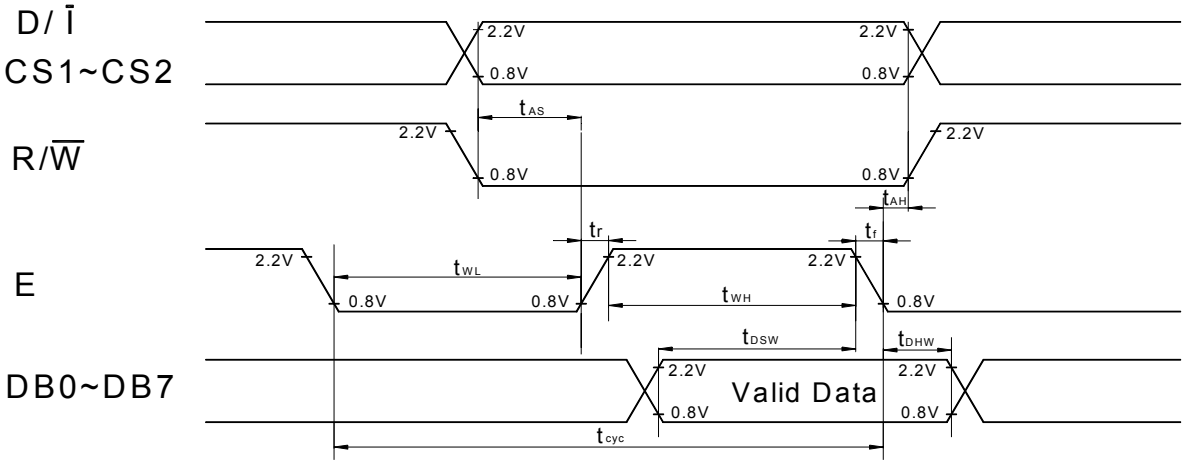


Fig. a Interface timing (data write)

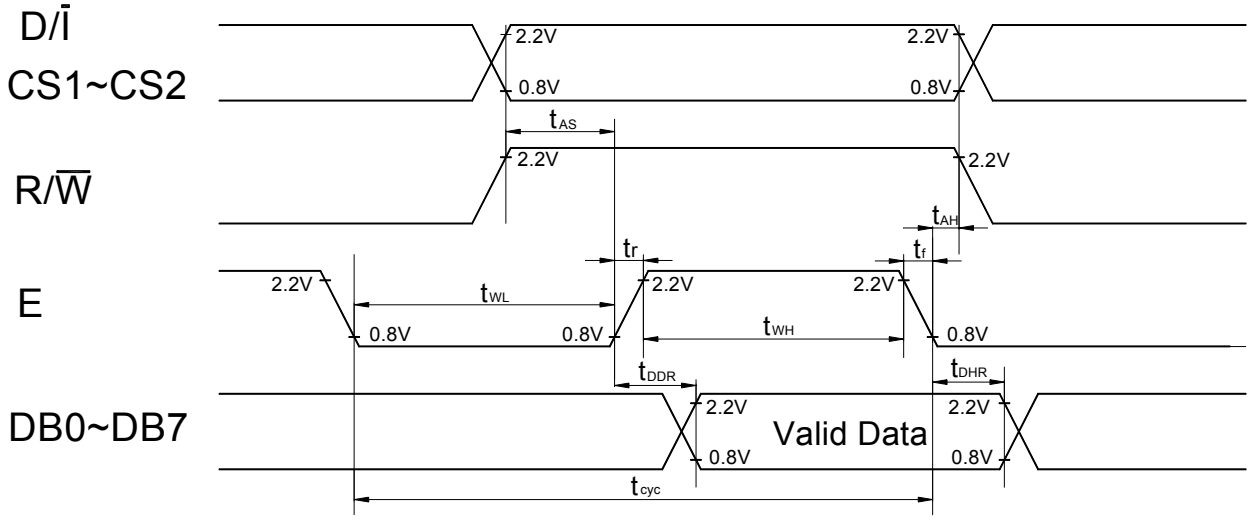


Fig. b Interface timing (data read)

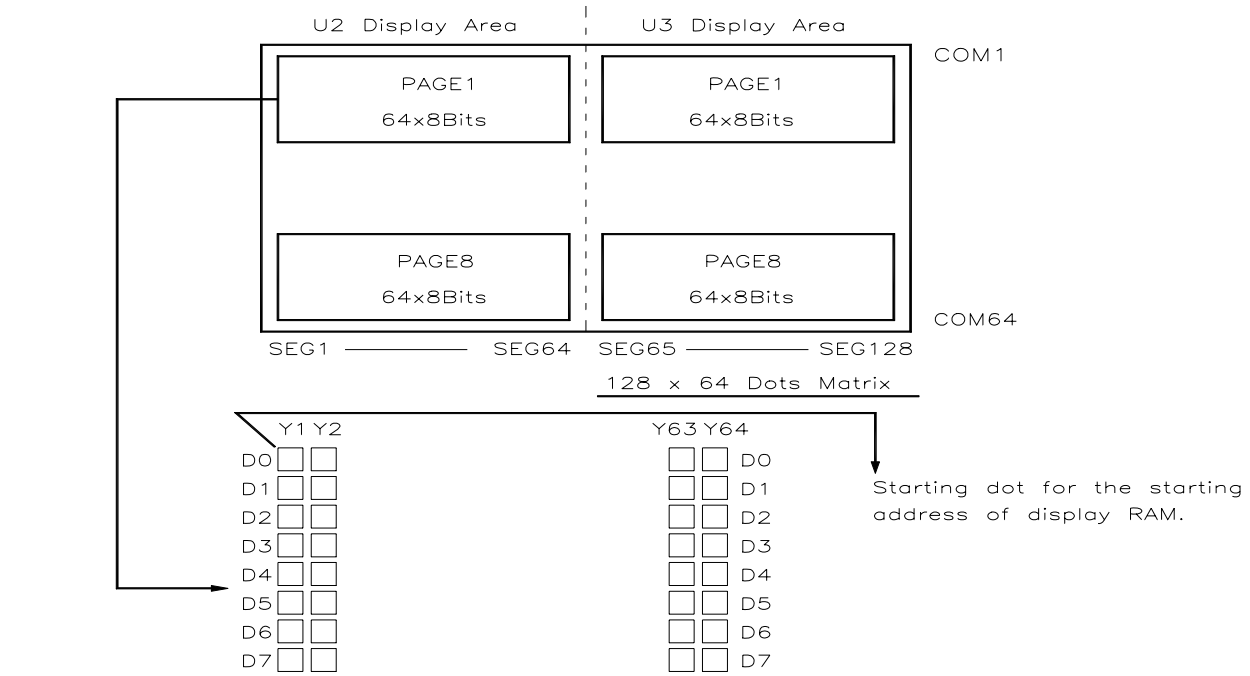
10.0 RELIABILITY TEST

Storage Condition	Content	Evaluations and Assessment*			
		Current Consumption	Oozing	Contrast	Other Appearances
Operation at high temperature and humidity	40ºC,90% RH,240hrs	Twice initial value or less	none	More than 80% of initial value	No abnormality
High temperature storage	60ºC, 240hrs	Twice initial value or less	none	More than 80% of initial value	No abnormality
Low temperature storage	-20ºC, 240hrs	Twice initial value or less		More than 80% of initial value	No abnormality

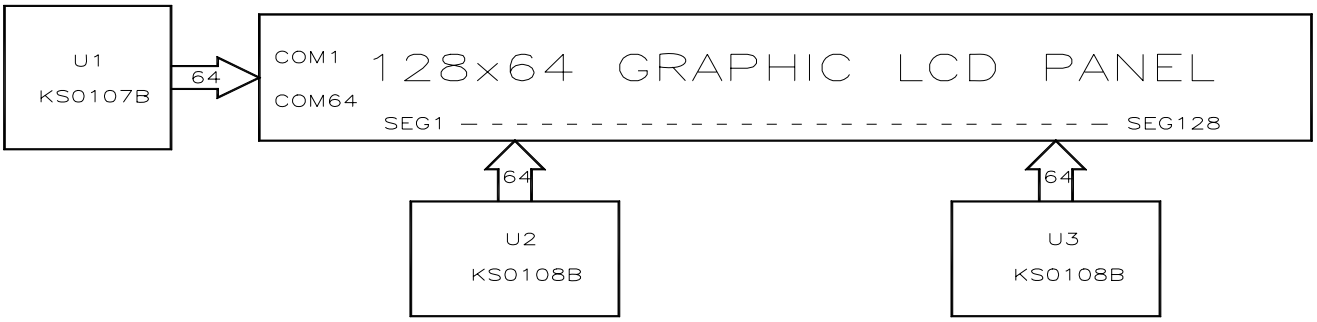
*Evaluations and assessment to be made two hours after returning to room temperature (25ºC±5ºC).

*The LCD|s subjected to the test must not have dew condensation.

11.0 RELATION BETWEEN DISPLAY PATTERN AND DRIVERS



Each segment driver has 8 pages RAM, and each page has 64x8 bits RAM.
D0~D7 are 8 bits transmitted data, where D0 is LSB and D7 is MSB.



12.0 DISPLAY CONTROL INSTRUCTION

The display control instructions control the internal state of the KS0108B. Instructions are received from MPU to KS0108B for the display control.

INSTRUCTION	D/I	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	DESCRIPTION
Display ON/OFF	0	0	0	0	1	1	1	1	1	1/0	Controls the display on or off. Display RAM data and internal status is not affected. 0: OFF. 1:ON
Set Address (Y address)	0	0	0	1	Y address (0~63)						Sets the Y address at the Y address counter.
Set Page (X address)	0	0	1	0	1	1	1	Page (0~7)			Sets the X address at the X address register.
Display Start Line (Z address)	0	0	1	1	Display start line (0~63)						Indicates the display data RAM displayed at the top of the screen.
Status Read	0	1	BUSY	0	ON/OFF	RESET	0	0	0	0	Read status: BUSY 0:Ready 1:In operation ON/OFF 0:Display ON 1:Display OFF RESET 0:Normal 1:Reset
Write Display Data	1	0	Write Data								Writes data DB0~DB7 into display data RAM. After writing instruction, Y address is increased by 1 automatically.
Read Display Data	1	1	Read Data								Reads data DB0~DB7 from display data RAM to the data bus.