# HDSP-076x Series 

Hexadecimal and Numeric Displays for Industrial Applications

## Data Sheet



HDSP-076x Series, HDSP-077x Series
HDSP-086x Series, HDSP-096x Series

## Description

These solid state display devices are designed and tested for use in adverse industrial environments. The character height is 7.4 mm ( 0.29 inch). The numeric and hexadecimal devices incorporate an on-board IC that contains the data memory, decoder and display driver functions.

The numeric devices decode positive BCD logic into charac-ters "0-9", a "-" sign, decimal point, and a test pattern. The hexadecimal devices decode positive BCD logic into 16 characters, "0-9, A-F." An input is provided on the hexadecimal devices to blank the display (all LEDs off) without losing the contents of the memory.

The over range device displays " $\pm 1$ " and right hand decimal point and is typically driven via external switching transistors.

## Features

- Three colors: High-efficiency red

Yellow
High performance green

- Two high-efficiency red options
- Low power
- High brightness
- Three character options
- Numeric
- Hexadecimal
- Over range
- Performance guaranteed over temperature
- Memory latch/decoder/driver: TTL compatible
- $4 \times 7$ dot matrix character
- Categorized for luminous intensity
- Yellow and green categorized for color


## Typical Applications

- Industrial equipment
- Computer peripherals
- Instrumentation
- Telecommunication equipment


## Device Selection Guide

| Part Number <br> HDSP- | Color | Description | Front <br> View |
| :--- | :--- | :--- | :--- |
| 0760 | High-Efficiency | Numeric, Right Hand DP | A |
| 0761 | Red | Numeric, Left Hand DP | B |
| 0762 | Low Power | Hexadecimal | C |
| 0763 |  | Over Range $\pm 1$ | D |
| 0770 | High-Efficiency | Numeric, Right Hand DP | A |
| 0771 | Red | Numeric, Left Hand DP | B |
| 0772 | High Brightness | Hexadecimal | C |
| 0763 |  | Over Range $\pm 1$ | D |
| 0860 | Yellow | Numeric, Right Hand DP | A |
| 0861 |  | Numeric, Left Hand DP | B |
| 0862 |  | Hexadecimal | C |
| 0863 |  | Over Range $\pm 1$ | D |
| 0960 | Green | Numeric, Right Hand DP | A |
| 0961 |  | Numeric, Left Hand DP | B |
| 0962 |  | Hexadecimal | C |
| 0963 |  | Over Range $\pm 1$ | D |

## Package Dimensions



| PIN | FUNCTION |  |
| :---: | :--- | :--- |
|  | NUMERIC | HEXADECIMAL |
| 1 | Input 2 | Input 2 |
| 2 | Input 4 | Input 4 |
| 3 | Input 8 | Input 8 |
| 4 | Decimal Point | Blanking Control |
| 5 | Latch Enable | Latch Enable |
| 6 | Ground | Ground |
| 7 | VCC | VCC |
| 8 | Input 1 | Input 1 |

NOTES:

1. DIMENSIONS IN MILLIMETERS AND (INCHES).
2. DIGIT CENTER LINE IS $\pm \mathbf{0 . 3 8} \mathbf{~ m m}( \pm \mathbf{0 . 0 1 5}$ INCH) FROM PACKAGE CENTER LINE.
3. UNLESS OTHERWISE SPECIFIED, THE TOLERANCE ON ALL DIMENSIONS IS $\pm 0.38 \mathrm{~mm}$ ( $\pm \mathbf{0 . 0 1 5}$ INCH).
4. HDSP-0860 AND HDSP-0960 SERIES.


Figure 1. Timing diagram.


| TRUTH TABLE |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | CD | $\mathrm{A}^{[1]}$ |  |  |  |
| $\mathrm{X}_{8}$ | $\mathrm{X}_{4}$ | $\mathrm{X}_{2}$ | $\mathrm{X}_{1}$ | NUMER | HEXADECIMAL |
| L | L | L | L | \% | : |
| L | L | L | H | - | ; |
| L | L | H | L | $\cdots$ | $\cdots$ |
| L | L | H | H | $\cdots$ | $\cdots$ |
| L | H | L | L | \% | ! |
| L | H | L | H | $\cdots$ | $\cdots$ |
| L | H | H | L | \%: | \%: |
| L | H | H | H | \% | $\overline{7}$ |
| H | L | L | L | \% | : |
| H | L | L | H | \% | \% |
| H | L | H | L | \% | \% |
| H | L | H | H | (BLANK) | $\%$ |
| H | H | L | L | (BLANK) | \% |
| H | H | L | H | .... | \% |
| H | H | H | L | (BLANK) | \%: |
| H | H | H | H | (BLANK) | $\cdots$ |
| DECIMAL PT. ${ }^{[2]}$ |  |  | ON |  | $\mathbf{V}_{\mathbf{D P}}=\mathbf{L}$ |
|  |  |  | OFF |  | $\mathrm{V}_{\mathrm{DP}}=\mathrm{H}$ |
| ENABLE ${ }^{[1]}$ |  |  | LOAD DATA |  | $\mathrm{V}_{\mathrm{E}}=\mathrm{L}$ |
|  |  |  | LATCH DATA |  | $\mathrm{V}_{\mathrm{E}}=\mathrm{H}$ |
| BLANKING ${ }^{[3]}$ |  |  | DISPLAY-ON |  | $\mathrm{V}_{\mathrm{B}}=\mathrm{L}$ |
|  |  |  | DISPLAY-0FF |  | $\mathrm{V}_{\mathrm{B}}=\mathrm{H}$ |

NOTES:

1. $\mathrm{H}=$ LOGIC HIGH; L = LOGIC LOW. WITH THE ENABLE INPUT AT LOGIC HIGH, CHANGES IN BCD INPUT LOGIC LEVELS HAVE NO EFFECT UPON DISPLAY MEMORY, DISPLAYED CHARACTER, OR DP.
2. THE DECIMAL POINT INPUT, DP, PERTAINS ONLY TO THE NUMERIC DISPLAYS.
3. The blanking control input, b, pertains only to the HEXADECIMAL DISPLAYS. BLANKING INPUT HAS NO EFFECT UPON DISPLAY MEMORY.

Figure 2. Logic block diagram.

## Absolute Maximum Ratings

| Description | Symbol | Min. | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: |
| Storage Temperature, Ambient | TS | -65 | +100 | ${ }^{\circ} \mathrm{C}$ |
| Operating Temperature, Ambient[1] | $\mathrm{T}_{\text {A }}$ | -55 | +85 | ${ }^{\circ} \mathrm{C}$ |
| Supply Voltage ${ }^{[2]}$ | $\mathrm{V}_{\text {CC }}$ | -0.5 | +7.0 | V |
| Voltage Applied to Input Logic, dp and Enable Pins | $\mathrm{V}_{1}, \mathrm{~V}_{\mathrm{DP}}, \mathrm{V}_{\mathrm{E}}$ | -0.5 | $\mathrm{V}_{\text {CC }}$ | V |
| Voltage Applied to Blanking Input[2] | $V_{B}$ | -0.5 | $\mathrm{V}_{\text {CC }}$ | V |
| Soldering Temperature [1.59 mm (0.063 inch) Below Body] |  |  |  |  |
| Solder Dipping, max. 5 Seconds |  |  | 260 | ${ }^{\circ} \mathrm{C}$ |
| W ave Soldering, max. 3 Seconds |  |  | 250 | ${ }^{\circ} \mathrm{C}$ |

## Recommended Operating Conditions

| Description | Symbol | Min. | Nom. | Max. |
| :--- | :--- | :--- | :--- | :--- |
| Unit |  |  |  |  |
| Supply Voltage[2] | $\mathrm{V}_{\mathrm{CC}}$ | 4.5 | 5.0 | 5.5 |
| Operating Temperature, A mbient[1] | $\mathrm{T}_{\mathrm{A}}$ | -55 | V |  |
| Enable Pulse W idth | $\mathrm{t}_{\mathrm{W}}$ | 100 | +85 | ${ }^{\circ} \mathrm{C}$ |
| Time Data M ust Be Held B efore Positive Transition of Enable Line | $\mathrm{t}_{\text {SeTup }}$ | 50 | nsec |  |
| Time Data M ust Be Held A fter Positive Transition of Enable Line | $\mathrm{t}_{\mathrm{HOLD}}$ | 50 | nsec |  |
| Enable Pulse Rise Time | $\mathrm{t}_{\mathrm{TLH}}$ |  | nsec |  |

Optical Characteristics at $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{CC}}=5.0 \mathrm{~V}$

| Device | Description | Symbol | Min. | Typ. | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| HDSP-0760 <br> Series | Luminous Intensity per LED (Digit A verage) ${ }^{[3,4]}$ | IV | 65 | 140 |  | $\mu \mathrm{cd}$ |
|  | Peak W avelength | $\lambda_{\text {PEAK }}$ |  | 635 |  | nm |
|  | Dominant W avelength[5] | $\lambda_{\mathrm{d}}$ |  | 626 |  | nm |
| HDSP-0770 Series | Luminous Intensity per LED (Digit A verage)[3,4] | IV | 260 | 620 |  | $\mu \mathrm{cd}$ |
|  | Peak W avelength | $\lambda_{\text {PEAK }}$ |  | 635 |  | nm |
|  | Dominant W avelength[5] | $\lambda_{\mathrm{d}}$ |  | 626 |  | nm |
| HDSP-0860 Series | Luminous Intensity per LED (Digit A verage)[3,4] | IV | 215 | 490 |  | $\mu \mathrm{cd}$ |
|  | Peak W avelength | $\lambda_{\text {PEAK }}$ |  | 583 |  | nm |
|  | Dominant W avelength[5,6] | $\lambda_{\mathrm{d}}$ |  | 585 |  | nm |
| HDSP-0960 <br> Series | Luminous Intensity per LED (Digit A verage)[3,4] | IV | 298 | 1100 |  | $\mu \mathrm{cd}$ |
|  | Peak W avelength | $\lambda_{\text {PEAK }}$ |  | 568 |  | nm |
|  | Dominant W avelength[5,6] | $\lambda_{\mathrm{d}}$ |  | 574 |  | nm |

## Notes:

1. The nominal thermal resistance of a display mounted in a socket that is soldered onto a printed circuit board is $R \theta_{J} A=50^{\circ} \mathrm{C} / \mathrm{W} /$ device. The device package thermal resistance is $R \theta_{J}-$ PIN $=15^{\circ} \mathrm{C} / \mathrm{W} /$ device. The thermal resistance device pin-to-ambient through the PC board should not exceed $35^{\circ} \mathrm{C} / \mathrm{W} /$ device for operation at $\mathrm{T}_{\mathrm{A}}=+85^{\circ} \mathrm{C}$.
2. Voltage values are with respect to device ground, pin 6 .
3. These displays are categorized for luminous intensity with the intensity category designated by a letter code located on the back of the display package. Case temperature of the device immediately prior to the light measurement is equal to $25^{\circ} \mathrm{C}$.

Electrical Characteristics; $\mathrm{T}_{\mathrm{A}}=-55^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$

| Description | Symbol | Test Conditions | Min. | Typ. ${ }^{[7]}$ | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Supply Current HDSP- 0760 Series | Icc | $V_{C C}=5.5 \mathrm{~V}$ (Characters "5." or "B" Displayed) |  | 78 | 105 | mA |
| HDSP-0770 Series HDSP-0860 Series HDSP-0960 Series |  |  |  | 120 | 175 |  |
| Power Dissipation HDSP-0760 Series | $\mathrm{P}_{\mathrm{T}}$ |  |  | 390 | 573 | mW |
| HDSP-0770 Series HDSP-0860 Series HDSP-0960 Series |  |  |  | 690 | 963 |  |
| Logic, Enable and Blanking Low-Level Input Voltage | VIL | $\mathrm{V}_{\mathrm{CC}}=4.5 \mathrm{~V}$ |  |  | 0.8 | V |
| Logic, Enable and Blanking High-Level Input Voltage | $\mathrm{V}_{\text {IH }}$ |  | 2.0 |  |  | V |
| Logic and Enable Low-Level Input Current | IIL | $\mathrm{V}_{\text {CC }}=5.5 \mathrm{~V}$ |  |  | -1.6 | mA |
| Blanking Low-Level Input Current | $I_{\text {BL }}$ | $\mathrm{V}_{\text {IL }}=0.4 \mathrm{~V}$ |  |  | -10 | $\mu \mathrm{A}$ |
| Logic, Enable and Blanking High-Level Input Current | IH | $\begin{aligned} & \mathrm{V}_{\mathrm{CC}}=5.5 \mathrm{~V} \\ & \mathrm{~V}_{\mathrm{IH}}=2.4 \mathrm{~V} \end{aligned}$ |  |  | +40 | $\mu \mathrm{A}$ |
| Weight |  |  |  | 1.0 |  | gm |
| Leak Rate |  |  |  |  | $5 \times 10^{-8}$ | cc/ sec |

Notes:
4. The luminous intensity at a specific operating ambient temperature, $I_{V}\left(T_{A}\right)$ may be approximated from the following exponential equation: Iv $\left(T_{A}=\operatorname{lv}\left(25^{\circ} \mathrm{C}\right) \mathrm{e}^{\left[k\left(\mathrm{~T}_{\mathrm{A}}-25^{\circ} \mathrm{C}\right)\right] \text {. }}\right.$

| Device | $\mathbf{K}$ |
| :--- | :---: |
| HDSP-0760 Series | $-0.0131 /{ }^{\circ} \mathrm{C}$ |
| HDSP-0770 Series |  |
| HDSP-0860 Series | $-0.0112 /{ }^{\circ} \mathrm{C}$ |
| HDSP-0960 Series | $-0.0104 /{ }^{\circ} \mathrm{C}$ |

5. The dominant wavelength, $\lambda_{d}$, is derived from the CIE Chromaticity Diagram and is that single wavelength which defines the color of the device.
6. The HDSP-0860 and HDSP-0960 series devices are categorized as to dominant wavelength with the category designated by a number on the back of the display package.
7. All typical values at $\mathrm{V}_{\mathrm{CC}}=5.0 \mathrm{~V}$ and $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$.

## Operational Considerations Electrical

These devices use a modified $4 \times 7$ dot matrix light emitting diode to display decimal/hexadecimal numeric information. The high efficiency red and yellow LEDs are GaAsP epitaxial layer on a GaP transparent substrate. The green LEDs are GaP epitaxial layer on a GaP transparent substrate. The LEDs are driven by constant current drivers, BCD information is accepted by the display memory when the enable line is at logic low and the data is latched when the enable is at logic high. Using the enable pulse width and data setup and hold times listed in the Recommended Operating Conditions allows data to be clocked into an array of displays at a 6.7 MHz rate.

The decimal point input is active low true and this data is latched into the display memory in the
same fashion as the BCD data. The decimal point LED is driven by the on-board IC.

The blanking control input on the hexadecimal displays blanks (turns off) the displayed information without disturbing the contents of display memory. The display is blanked at a minimum threshold level of 2.0 volts. When blanked, the display standby power is nominally 250 mW at $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$.

## Mechanical

The primary thermal path for power dissipation is through the device leads. Therefore, to insure reliable operation up to an ambient temperature of $+85^{\circ} \mathrm{C}$, it is important to maintain a case-toambient thermal resistance of less than $35^{\circ} \mathrm{C}$ watt/device as measured on top of display pin 3.

For information on soldering and post solder cleaning see Application Note 1027, Soldering LED Components.

## Contrast Enhancement

These display devices are designed to provide an optimum ON/OFF contrast when placed behind an appropriate contrast enhancement filter. For further information, please refer to Application Note 1015, Contrast Enhancement Techniques for LED Displays.

## Over Range Display

The over range devices display " $\pm 1$ " and decimal point. The character height and package configuration are the same as the numeric and hexadecimal devices. Character selection is obtained via external switching transistors and current limiting resistors.

## Package Dimensions



NOTE: DIMENSIONS IN MILLIMETERS AND (INCHES).

|  | Pin |  |  |  |
| :--- | :--- | :--- | :--- | :--- |
| Character | $\mathbf{1}$ | $\mathbf{2 , 3}$ | $\mathbf{4}$ | $\mathbf{8}$ |
| + | 1 | $X$ | $X$ | 1 |
|  | 0 | $X$ | $X$ | 1 |
| 1 | $X$ | 1 | $X$ | $X$ |
| Decimal Point | $X$ | $X$ | 1 | $X$ |
| Blank | 0 | 0 | 0 | 0 |

## Notes:

0: Line switching transistor in Figure 7 cutoff.
1: Line switching transistor in Figure 7 saturated.
X: 'don't care'

Absolute Maximum Ratings

| Description | Symbol | Min. | Max. | Unit |
| :--- | :--- | :--- | :--- | :--- |
| Storage Temperature, Ambient | $\mathrm{T}_{\mathrm{S}}$ | -65 | +100 | ${ }^{\circ} \mathrm{C}$ |
| Operating Temperature, Ambient | $\mathrm{T}_{\mathrm{A}}$ | -55 | +85 | ${ }^{\circ} \mathrm{C}$ |
| Forward Current, Each LED | $\mathrm{I}_{\mathrm{F}}$ |  | 10 | mA |
| Reverse Voltage, Each LED | $\mathrm{V}_{\mathrm{R}}$ |  | 5 | V |



Figure 3. Typical driving circuit.

Recommended Operating Conditions VCC $=5.0 \mathrm{~V}$

|  | Forward Current Per <br> Device LED, $\mathbf{m A}$ | Resistor Value |  |  |
| :--- | :--- | :--- | :--- | :--- |
|  | $\mathbf{R}_{\mathbf{1}}$ | $\mathbf{R}_{\mathbf{2}}$ | $\mathbf{R}_{\mathbf{3}}$ |  |
| HDSP-0763 | 2.8 | 1300 | 200 | 300 |
| Low Power | 2.8 | 360 | 47 | 68 |
| High Brightness | 8 | 360 | 36 | 56 |
| HDSP-0863 | 8 | 360 | 30 | 43 |
| HDSP-0963 | 8 |  |  |  |

Luminous Intensity per LED
(Digit Average) ${ }^{[3,4]}$ at $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$

| Device | Test Conditions | Min. | Typ. | Units |
| :--- | :--- | :--- | :--- | :--- |
| HDSP-0763 | $\mathrm{I}_{\mathrm{F}}=2.8 \mathrm{~mA}$ | 65 | 140 | $\mu \mathrm{Cd}$ |
|  | $\mathrm{I}_{\mathrm{F}}=8 \mathrm{~mA}$ |  | 620 | $\mu \mathrm{~cd}$ |
| HDSP-0863 | $\mathrm{I}_{\mathrm{F}}=8 \mathrm{~mA}$ | 215 | 490 | $\mu \mathrm{Cd}$ |
| HDSP-0963 | $\mathrm{I}_{\mathrm{F}}=8 \mathrm{~mA}$ | 298 | 1100 | $\mu \mathrm{~cd}$ |

Electrical Characteristics: $\mathrm{T}_{\mathrm{A}}=-55^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$

| Device | Description | Symbol | Test Conditions | Min. | Typ. | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| HDSP-0763 | Power Dissipation (All LEDs Illuminated) | $\mathrm{P}_{\mathrm{T}}$ | $\mathrm{I}_{\mathrm{F}}=2.8 \mathrm{~mA}$ |  | 72 |  | mW |
|  |  |  | $\mathrm{I}_{\mathrm{F}}=8 \mathrm{~mA}$ |  | 224 | 282 |  |
|  | Forw ard Voltage per LED | $V_{F}$ | $\mathrm{I}_{\mathrm{F}}=2.8 \mathrm{~mA}$ |  | 1.6 |  | V |
|  |  |  | $\mathrm{I}_{\mathrm{F}}=8 \mathrm{~mA}$ |  | 1.75 | 2.2 |  |
| HDSP-0863 | Power Dissipation (All LEDs Illuminated) | $\mathrm{P}_{\mathrm{T}}$ | $\mathrm{I}_{\mathrm{F}}=8 \mathrm{~mA}$ |  | 237 | 282 | mW |
|  | Forw ard Voltage per LED | $\mathrm{V}_{\mathrm{F}}$ |  |  | 1.90 | 2.2 | V |
| HDSP-0963 | Power Dissipation (All LEDs Illuminated) | $\mathrm{P}_{\mathrm{T}}$ | $\mathrm{I}_{\mathrm{F}}=8 \mathrm{~mA}$ |  | 243 | 282 | mW |
|  | Forw ard Voltage per LED | $\mathrm{V}_{\mathrm{F}}$ |  |  | 1.85 | 2.2 | V |

Intensity Bin Limits for HDSP-0760
Series

|  | Intensity Range $(\mu \mathbf{c d})$ |  |
| :--- | :--- | :--- |
| Bin | Min. | Max. |
| A | 65.00 | 102.14 |
| B | 84.29 | 137.86 |
| C | 114.29 | 200.00 |
| D | 165.71 | 300.00 |
| E | 260.00 | 487.86 |
| F | 402.86 | 755.71 |
| G | 625.00 | 1171.43 |

## Note:

Test conditions as specified in Optical Characteristic table.

Intensity Bin Limits for HDSP-0860
Series

|  | Intensity Range $(\mu \mathbf{c d})$ |  |
| :--- | :--- | :--- |
| Bin | Min. | Max. |
| A | 215.00 | 337.86 |
| B | 279.29 | 456.43 |
| C | 377.14 | 662.14 |
| D | 547.14 | 992.86 |
| E | 820.71 | 1539.29 |
| F | 1272.14 | 2385.71 |

## Note:

Test conditions as specified in Optical Characteristic table

Color Bin Limits

|  |  | Color Range (nm) |  |
| :--- | :--- | :--- | :--- |
| Color | Bin | Min. | Max. |
| Yellow | 1 | 581.5 | 585.0 |
|  | 2 | 584.0 | 587.5 |
|  | 3 | 586.5 | 590.0 |
| Green | 1 | 570.0 | 574.0 |
|  | 2 | 573.0 | 577.0 |
|  | 3 | 576.0 | 580.0 |

## Note:

Test conditions as specified in Optical Characteristic table.

Intensity Bin Limits for HDSP-0770
Series

|  | Intensity Range ( $\mu \mathbf{c d}$ ) |  |
| :--- | :--- | :--- |
| Bin | Min. | Max. |
| E | 260.00 | 487.64 |
| F | 403.00 | 755.79 |
| G | 624.64 | 1171.50 |
| H | 969.00 | 1817.00 |

## Note

Test conditions as specified in Optical
Characteristic table.

Intensity Bin Limits for HDSP-0960
Series

|  | Intensity Range ( $\mu \mathbf{c d}$ ) |  |
| :--- | :--- | :--- |
| Bin | Min. | Max. |
| C | 298.57 | 523.57 |
| D | 432.86 | 785.00 |
| E | 648.57 | 1175.00 |
| F | 971.43 | 1757.86 |
| G | 1452.86 | 2629.29 |

## Note:

Test conditions as specified in Optical
Characteristic table.

